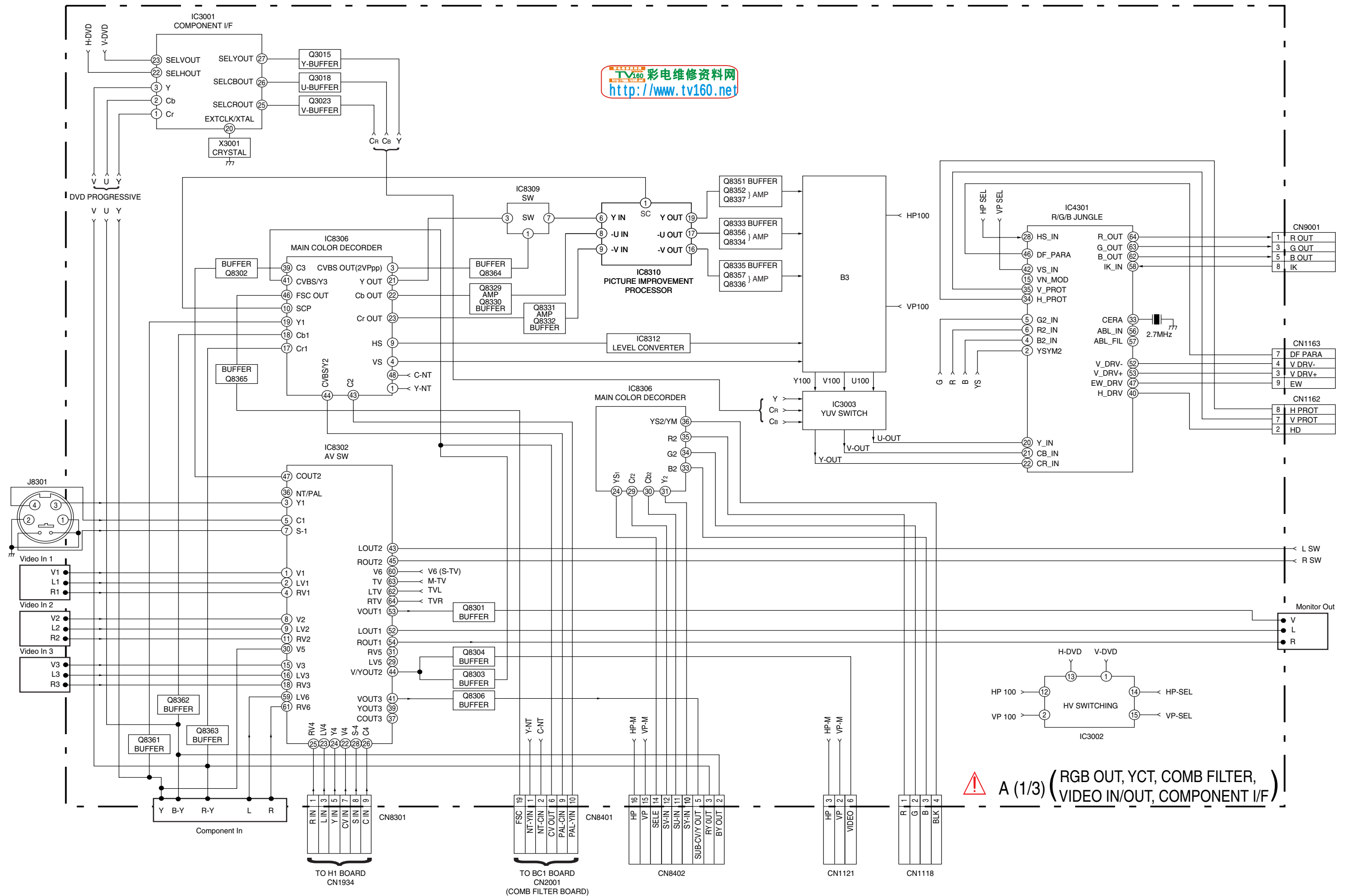
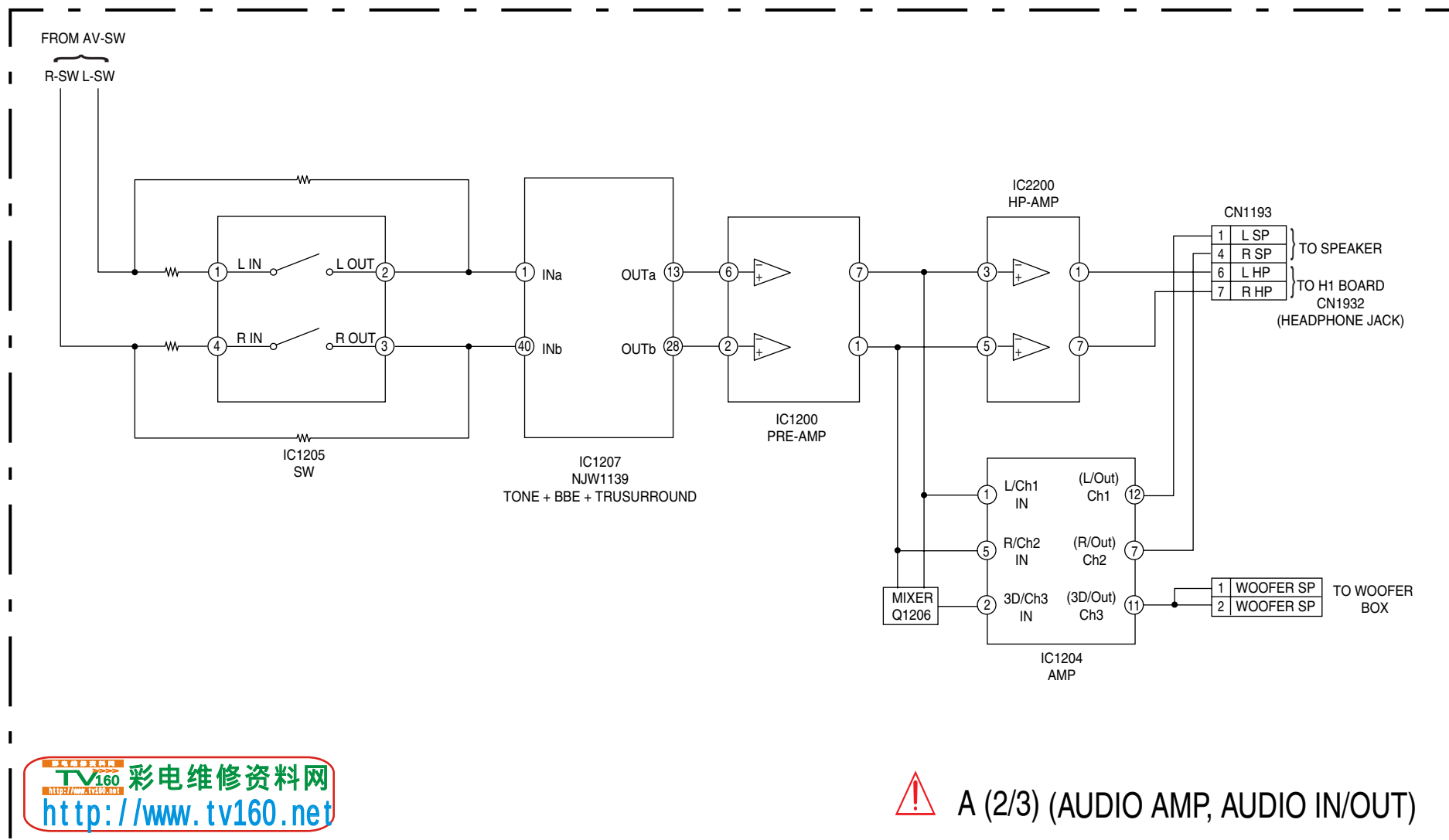
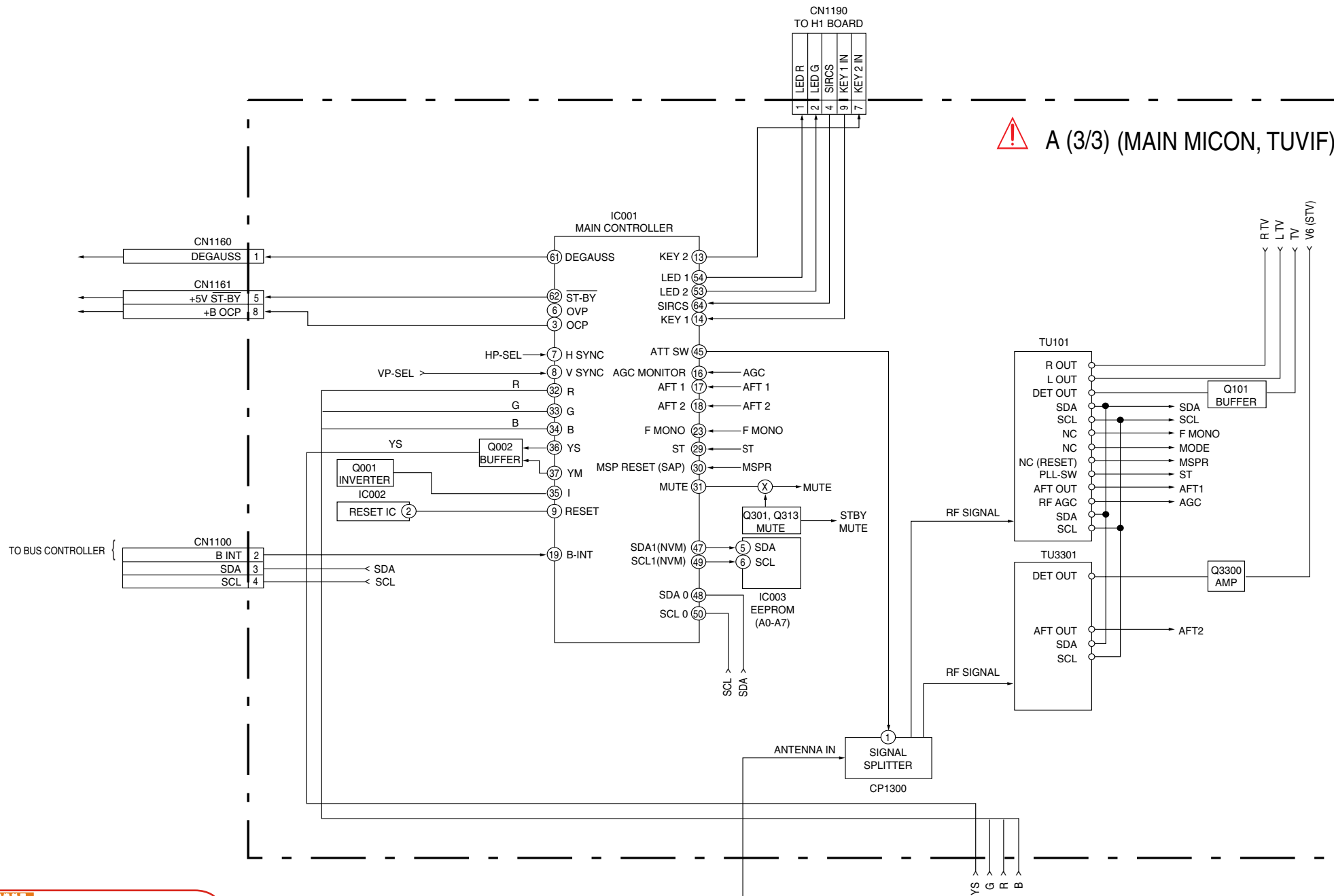


SECTION 7
DIAGRAMS

7-1. BLOCK DIAGRAM

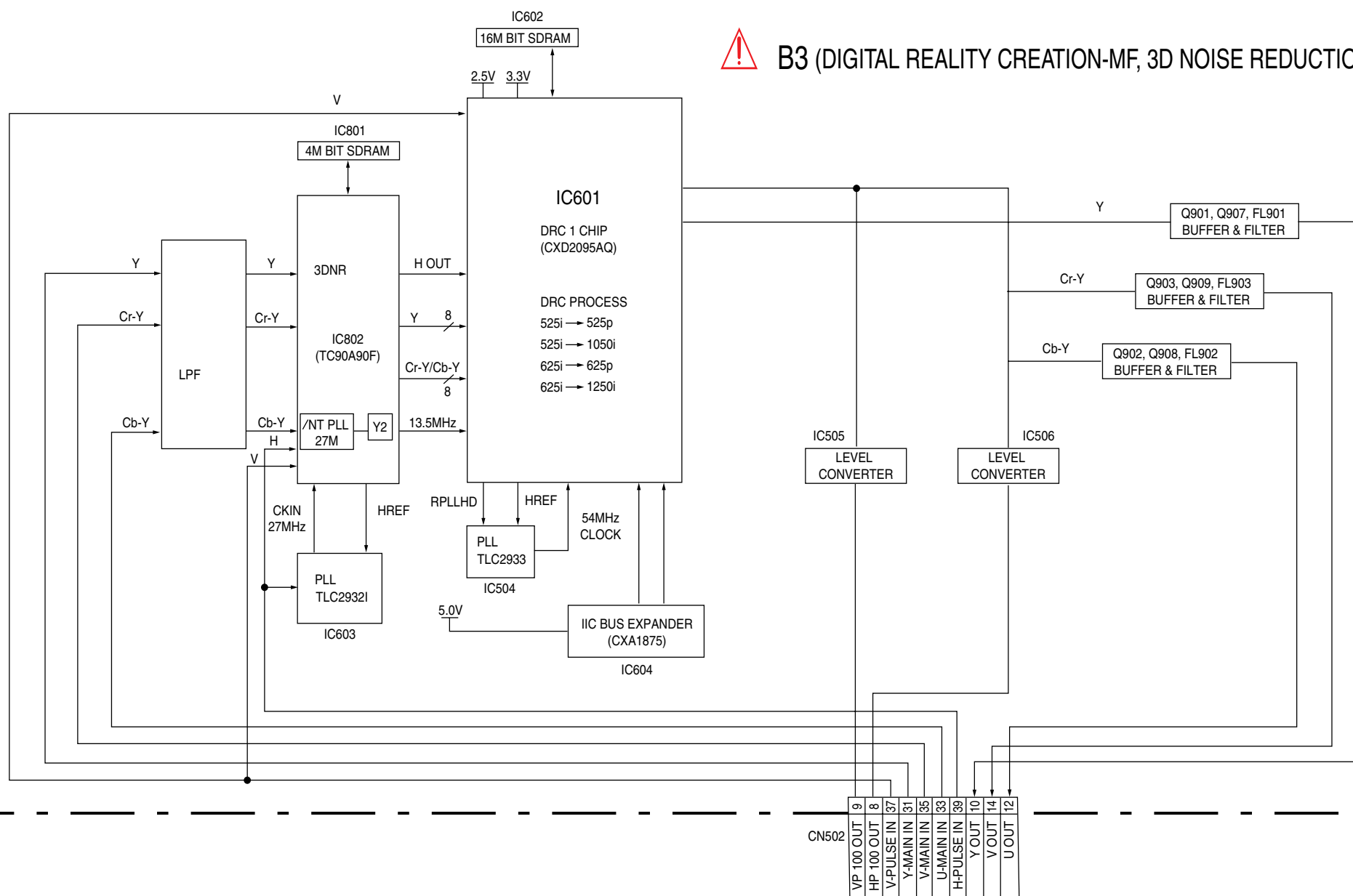


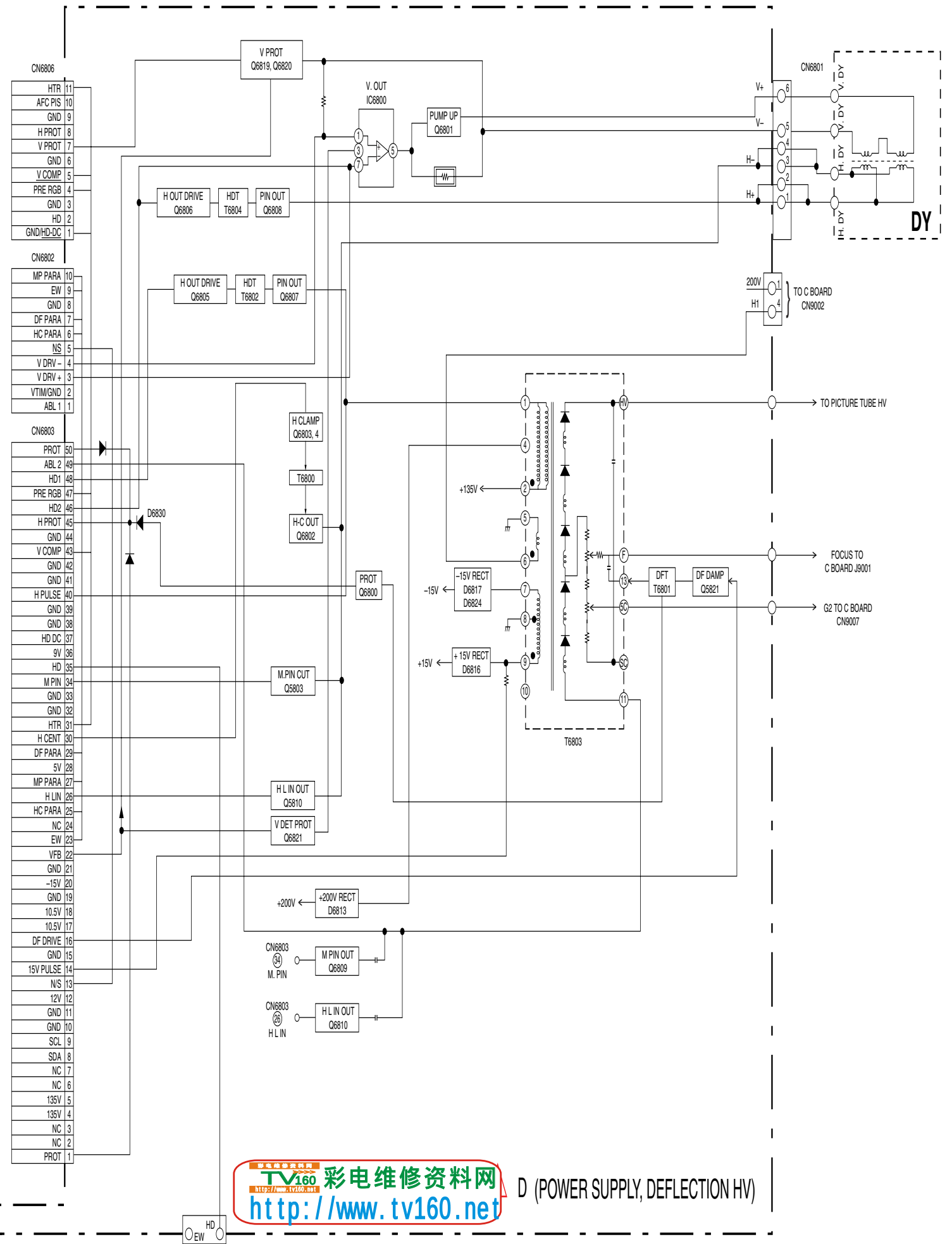
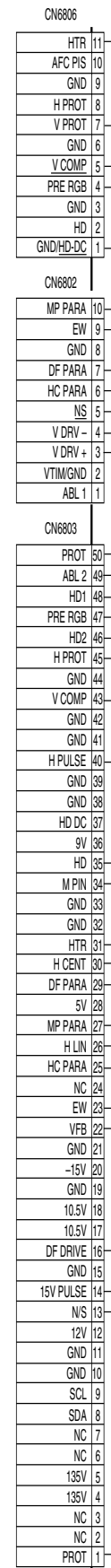


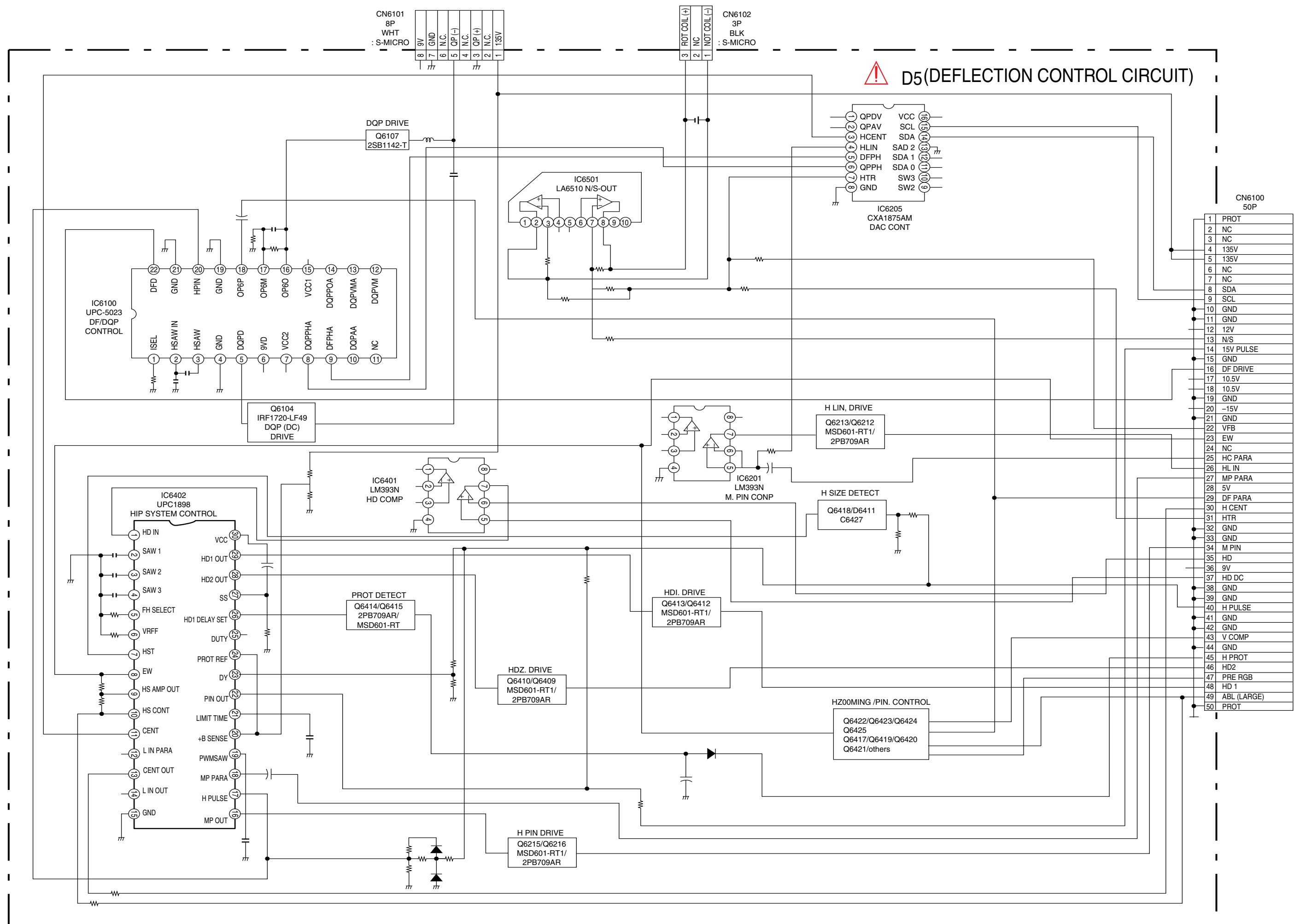




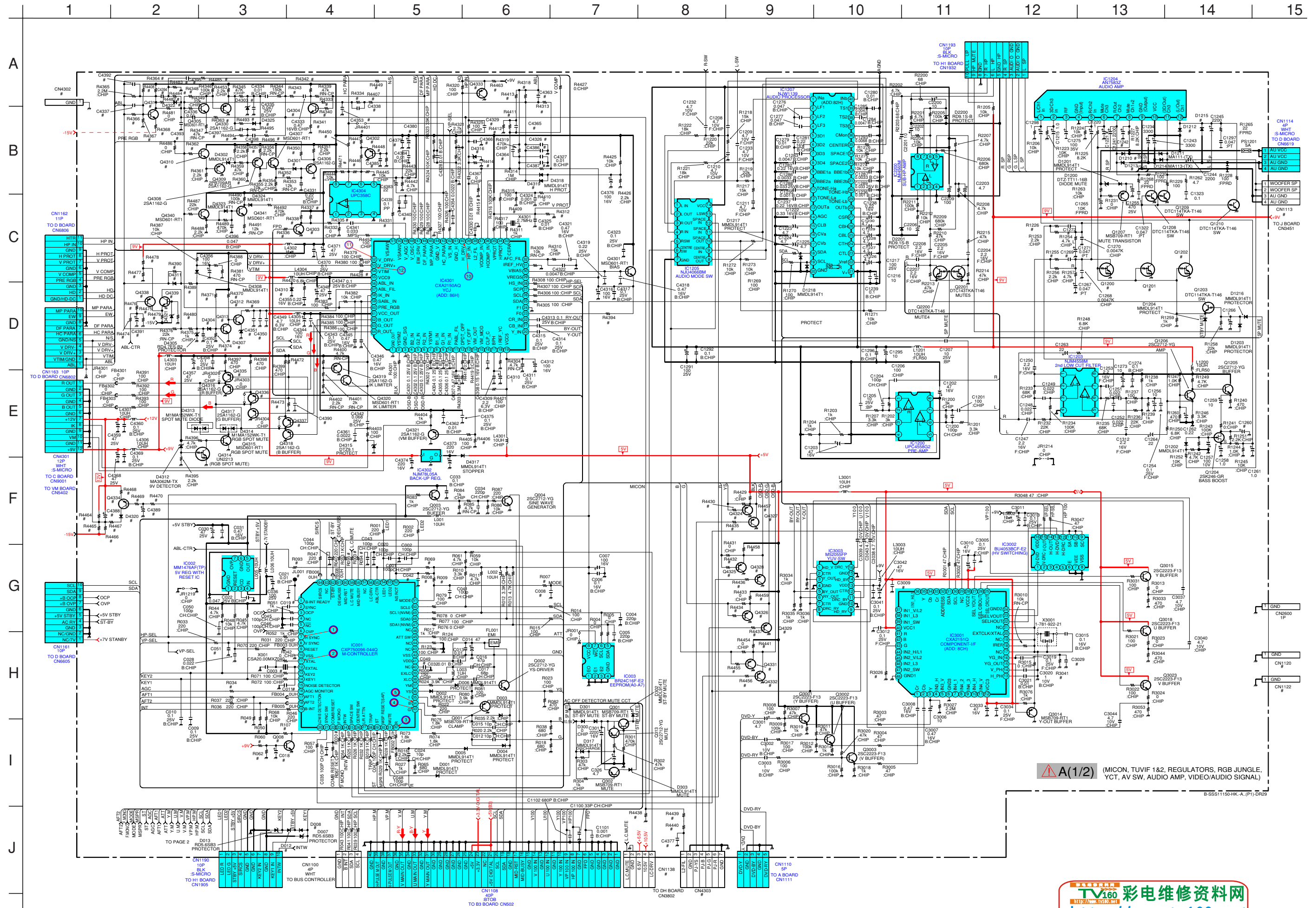
B3 (DIGITAL REALITY CREATION-MF, 3D NOISE REDUCTION)

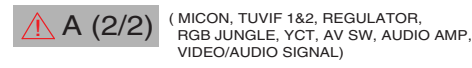




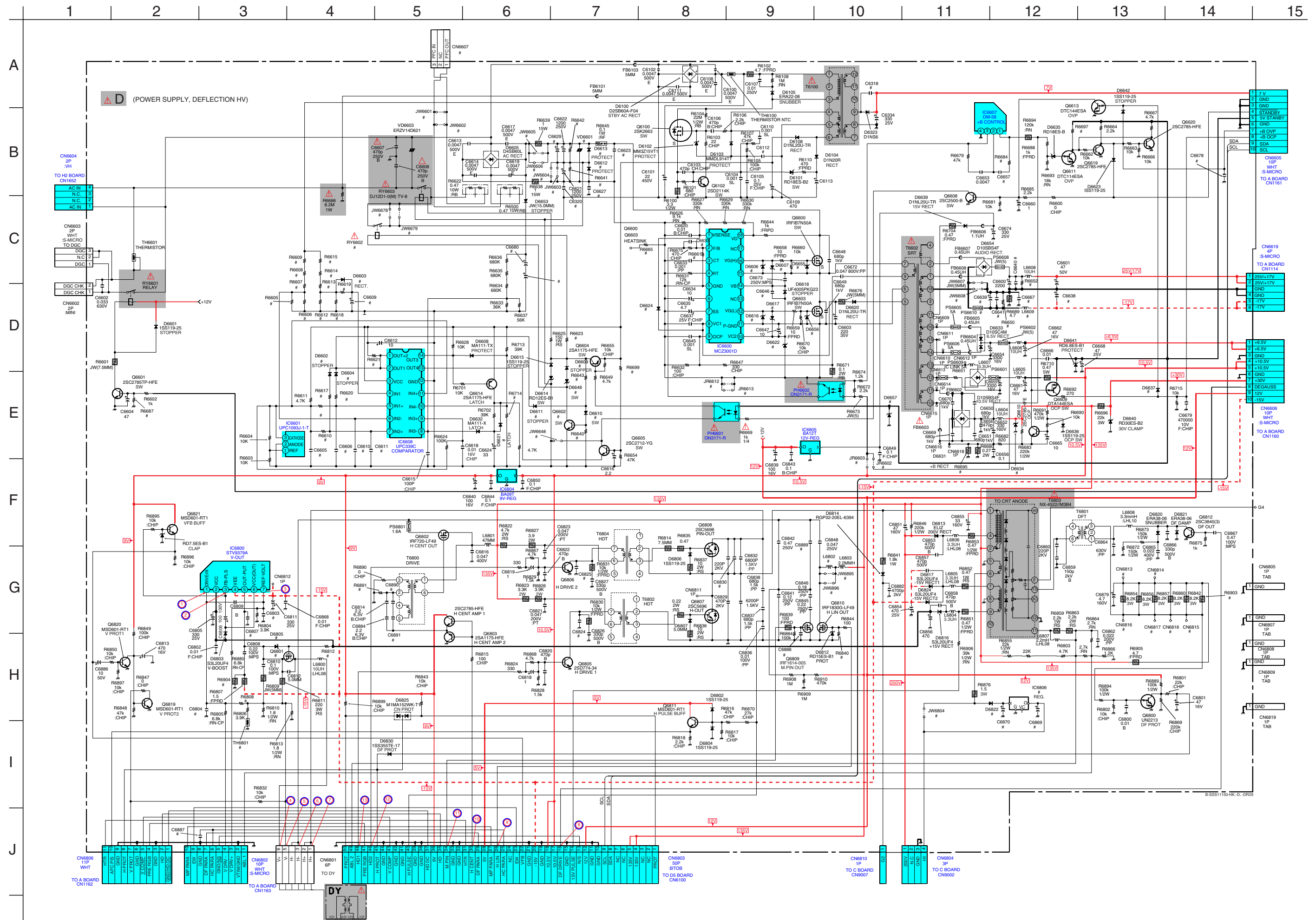


(1) A Board Schematic Diagrams

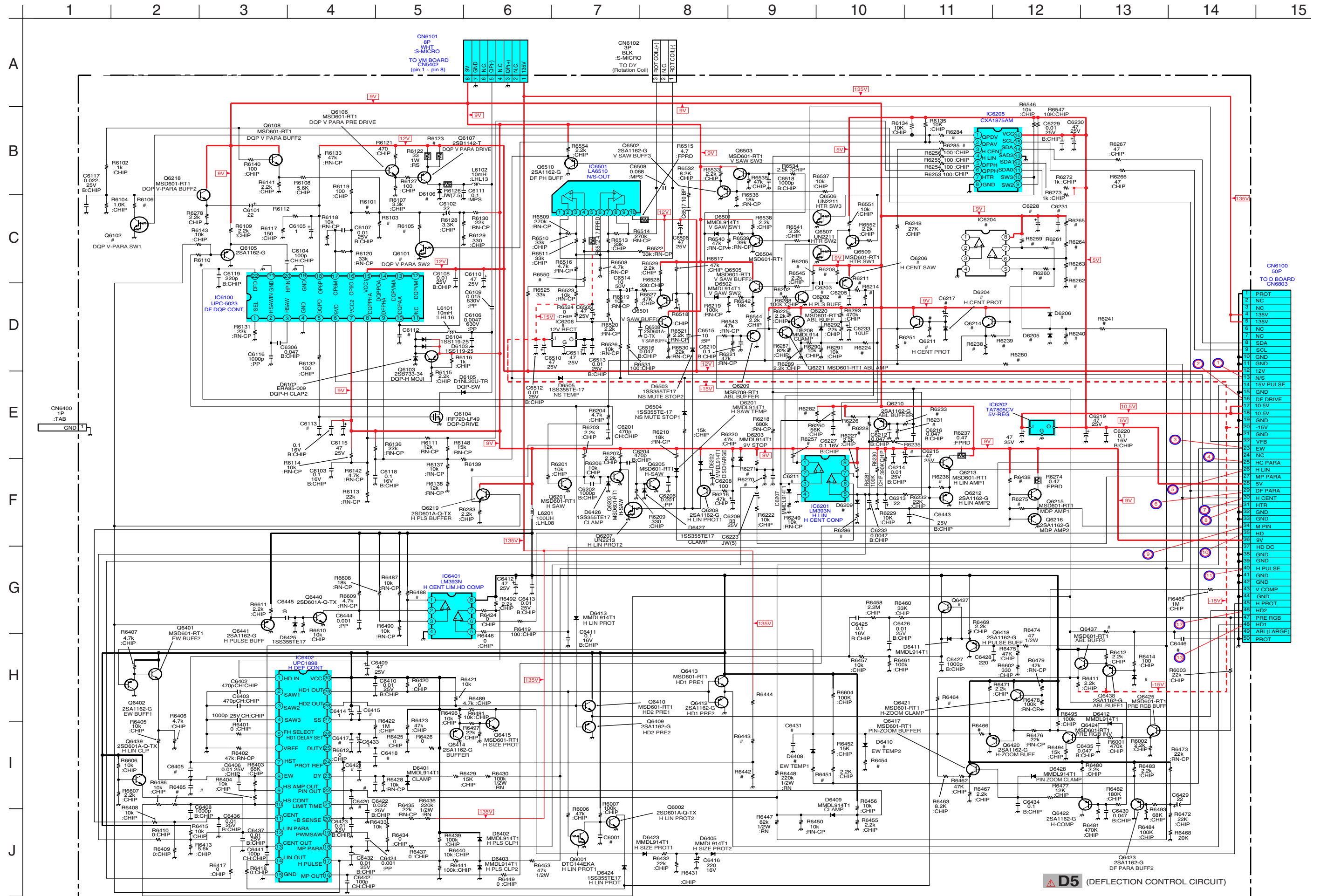




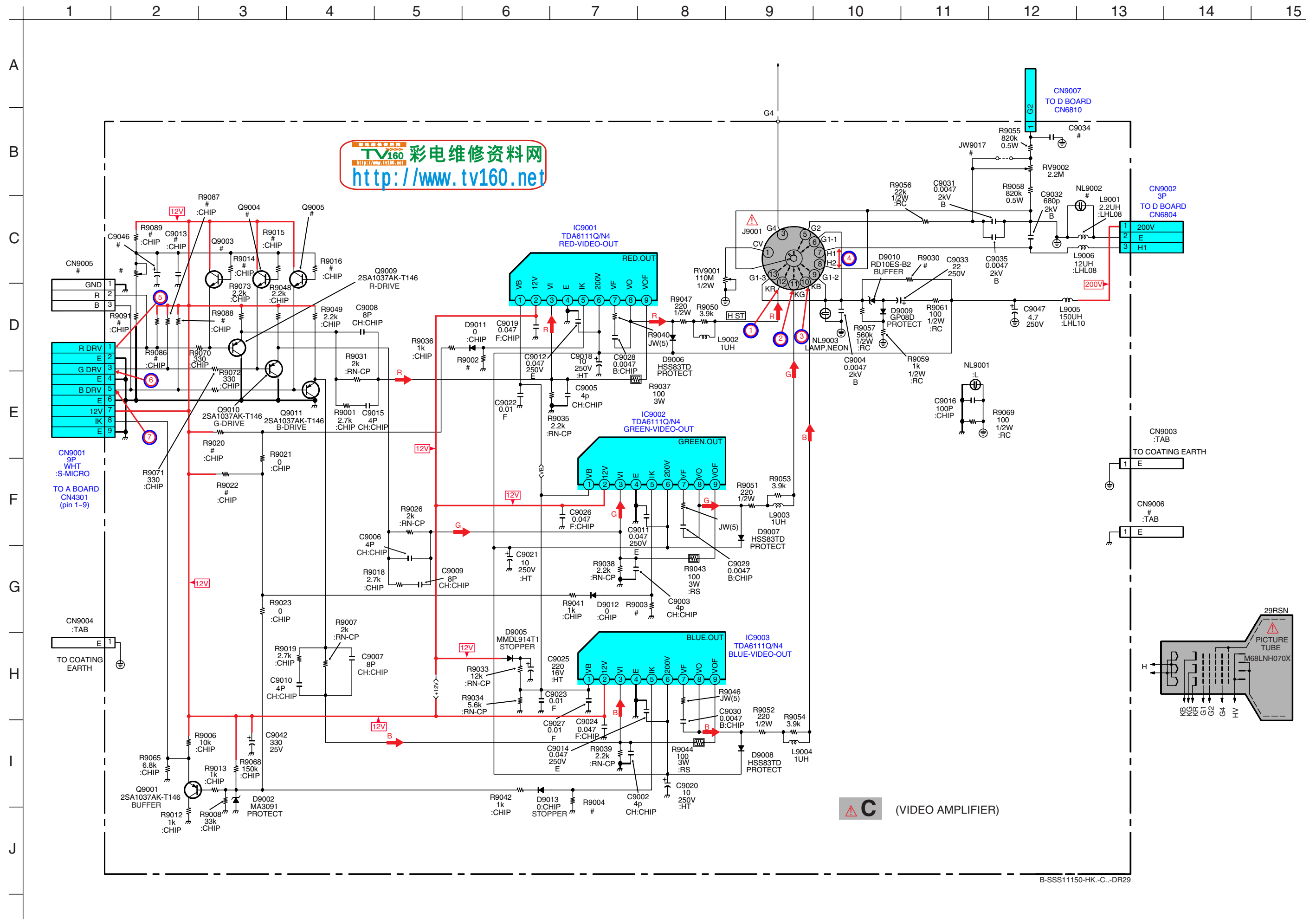
(3) D Board Schematic Diagram



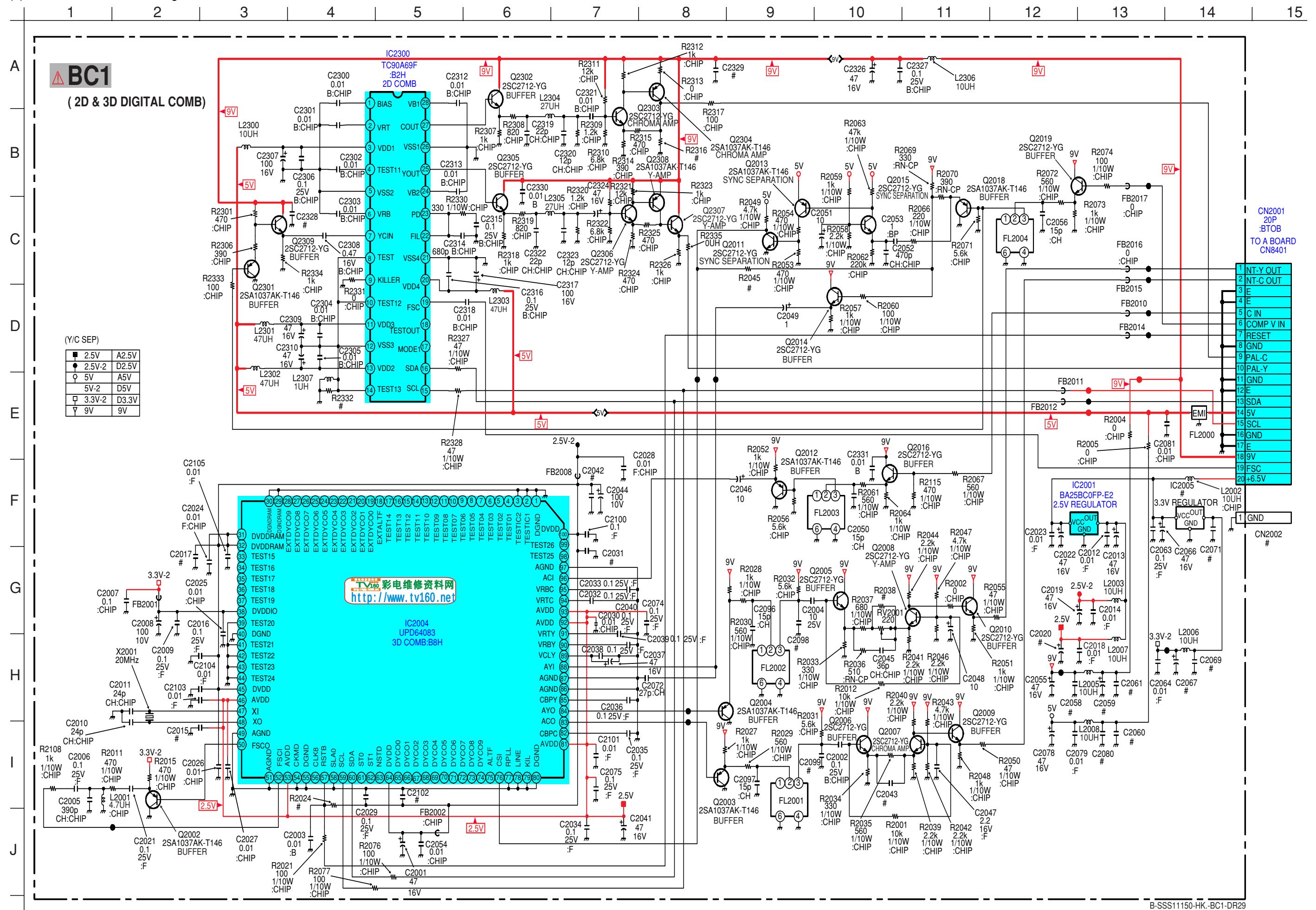
(4) D5 Board Schematic Diagram



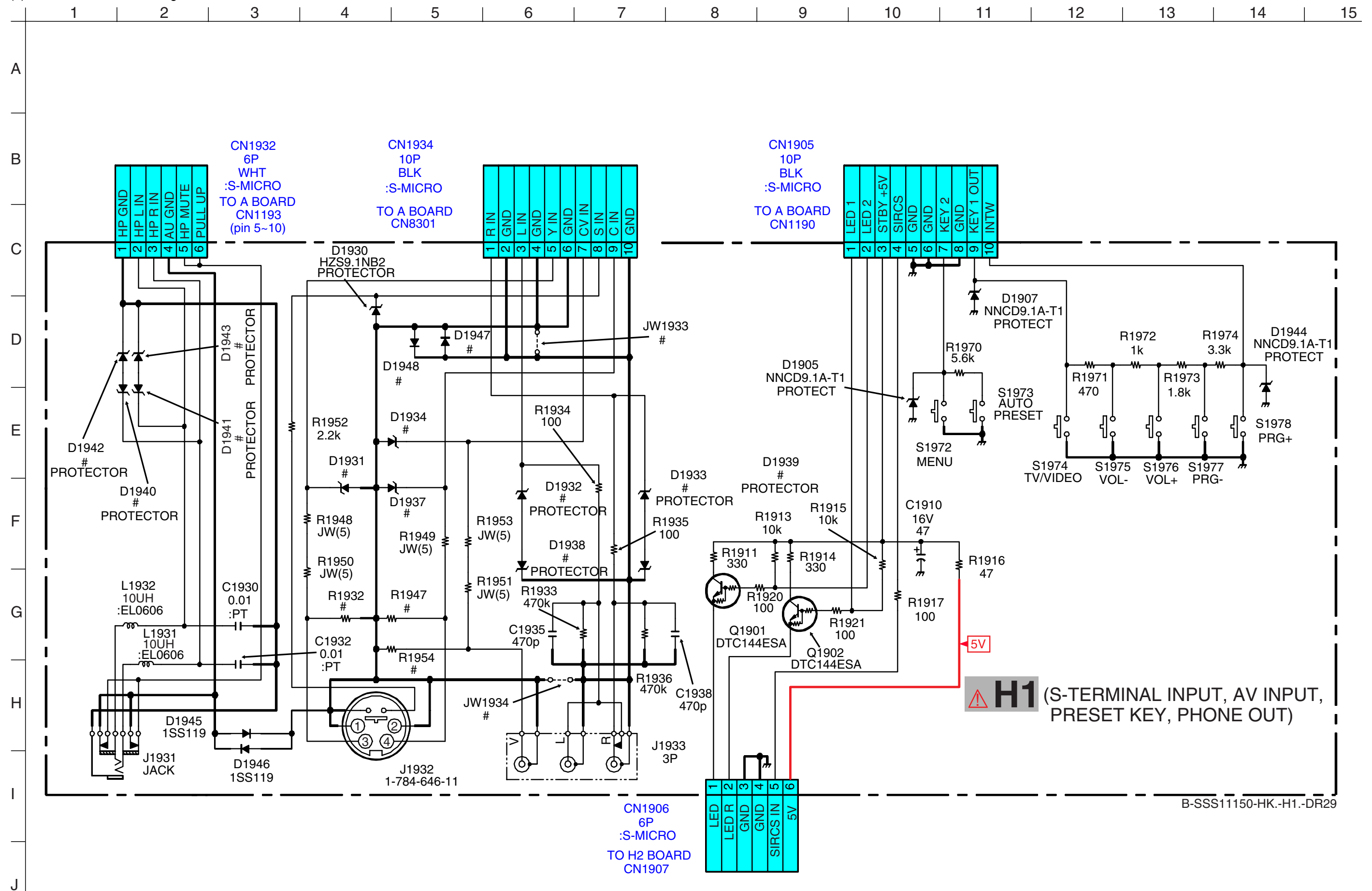
(5) C Board Schematic Diagram



(6) BC1 Board Schematic Diagram



(7) H1 Board Schematic Diagram



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http://www.tv160.net

AC IN 1
AC IN 2

CN1601 2P :VH
TO POWER CORD

CN1604 :TAB
GND

VDR161 ERZV14D621

JW1602 # JW1606 # JW1605 # JW1603 # JW1607 # JW1604 # JW1608 #

FB1601 1.1UH
FB1602 1.1UH
FB1603 1.1UH
FB1604 1.1UH

F1601 T5A 250V
C1602 0.68 250V
C1603 0.0047 250V

T1601 LFT
T1602 LFT

R1601 1M 1/2W
R1602 #
R1603 #

CN1602 2P :VH
TO H2 BOARD
CN1651

CN1603 # :TAB

F1 (AC LINE FILTER)

B-SSS11150-HK.-F1.-DR29

VM (VELOCITY MODULATION)

CN5401 8P :BTOB
TO NECK ASSY

1 QP-
2 N.C.
3 QP+
4 QP+
5 GND
6 GND
7 VM
8 VM

VM 11
GND 10
9V/12V 9
GND 8
N.C. 6
QP(-) 5
QP(+) 3
N.C. 2
135V 1

CN5402 11P WHT :S-MICRO
TO D5 BOARD
CN6101 (pin 1-8)
TO A BOARD
CN4301 (pin 10-12)

B-SSS11150-HK-VM-DR29