

Cortez + Hudson RD2 Reference Board Design
FLI8532 Cortez and FLI8125 Hudson

CONTENTS

SCHEMATIC Name	SHEET
01. Contents, Revision History	1
02. Top Level	2
03. Inputs	3
04. HDMI I/F	4
05. FLI8125 Hudson	5
06. FLI8532 Cortez	6
07. Frame Store	7
08. CORTEZ Memory I/F	8
09. Outputs	9
10. Power	10

REVISION HISTORY

Date	Author	Ver	Comments
June 2004	Abdul Aziz	1	Draft Release PCB# 400-243 Rev 1
Aug 9, 2004	Max Tang	2	PCB# 400-243 Rev 2 Added jumpers to select b/w NTSC/SCART input modules; Added option to connect FSEL from SCART3 connector; Added switches for reducing stubs from external DIP connector, Fixed Hudson Pin-out errors and IROM bootstrap, Added jumper option for new keypad interface, Changed U1011 regulator to LDO, Hudson Bootstrap
Aug 9, 2004	Max Tang	3	PCB# 400-243 Rev 3 Add more Option for AFE signal.

Approval	Position	Signature	Date
Abdul Aziz	Genesis		
Waqar Hyder	Genesis		

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<Variant Name>



Genesis Microchip Inc.

Title

01. Contents

Size B

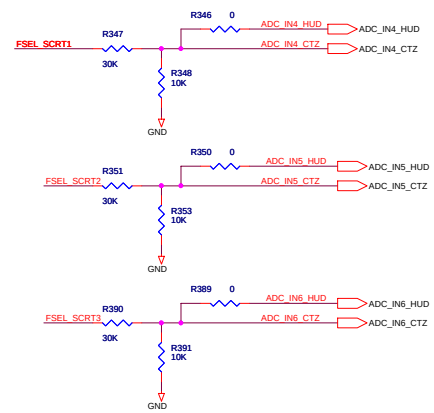
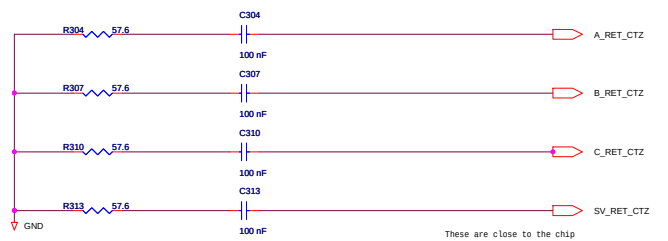
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Rev 3

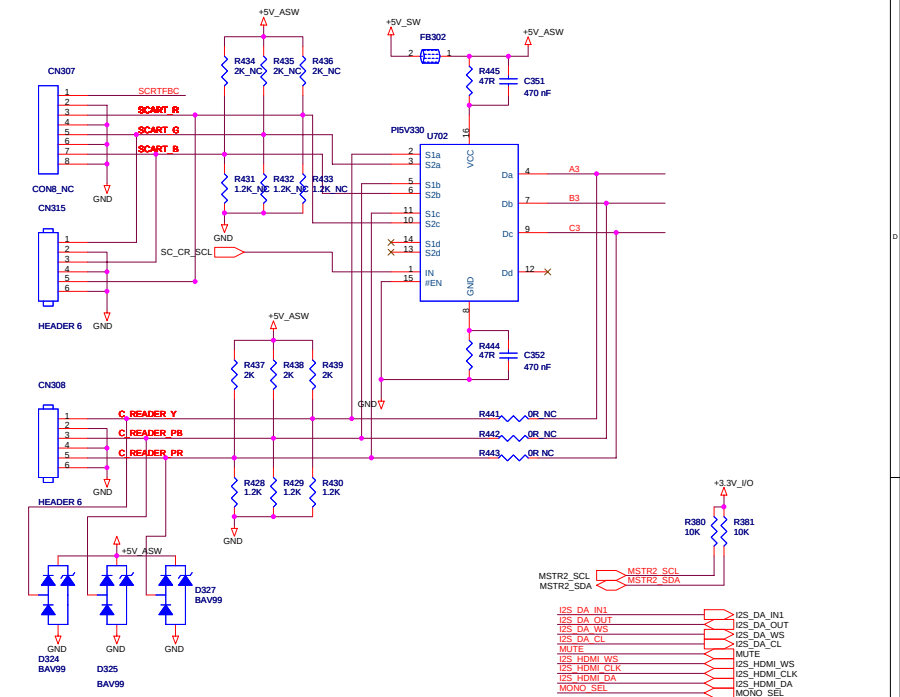
Date: Monday, November 08, 2004

Sheet 1 of 10

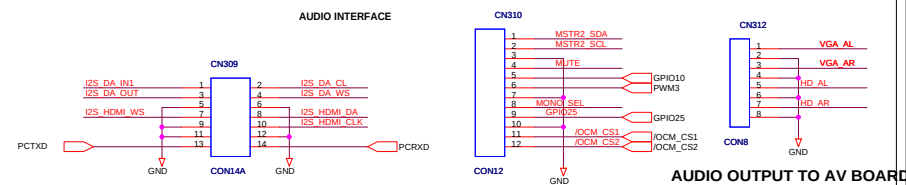
ANALOG VIDEO INPUT



Related Components	Separate Clamping	Single Clamping
JP304, JP305, JP306, JP307	2-3 100nF	1-2 0 Ohm
C304, C307, C310, C313	Do Not Stuff	0 Ohm
R320, R323, R327, R329	57.60hm	37.50hm
C332, C336, C337, C338, C353, C354, C355, C356, C357, C358, C359, C360, C361, C362, C363	0 Ohm	100nF
C301, C302, C303, C305, C306, C308, C309, C311, C312, C314, C315, C316, C317, C318, C319, C321, C322, C324, C325, C327, C328, C300, C300, C321, C343, C344, C345, C346, C348, C349	100nF	0 Ohm
R301, R302, R303, R305, R306, R308, R309, R311, R312, R314, R315, R316, R317, R318, R319, R321, R322, R324, R325, R328, R300, R330, R331, R332	200hm	0 Ohm

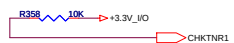


AUDIO INTERFACE

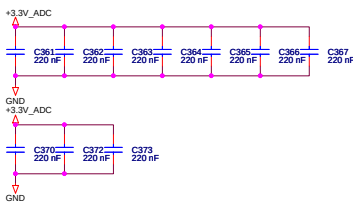
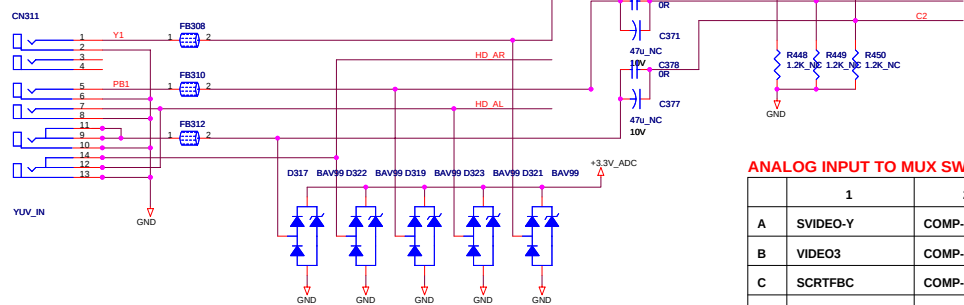


AUDIO OUTPUT TO AV BOARD

VIDEO INPUT FROM AV BOARD



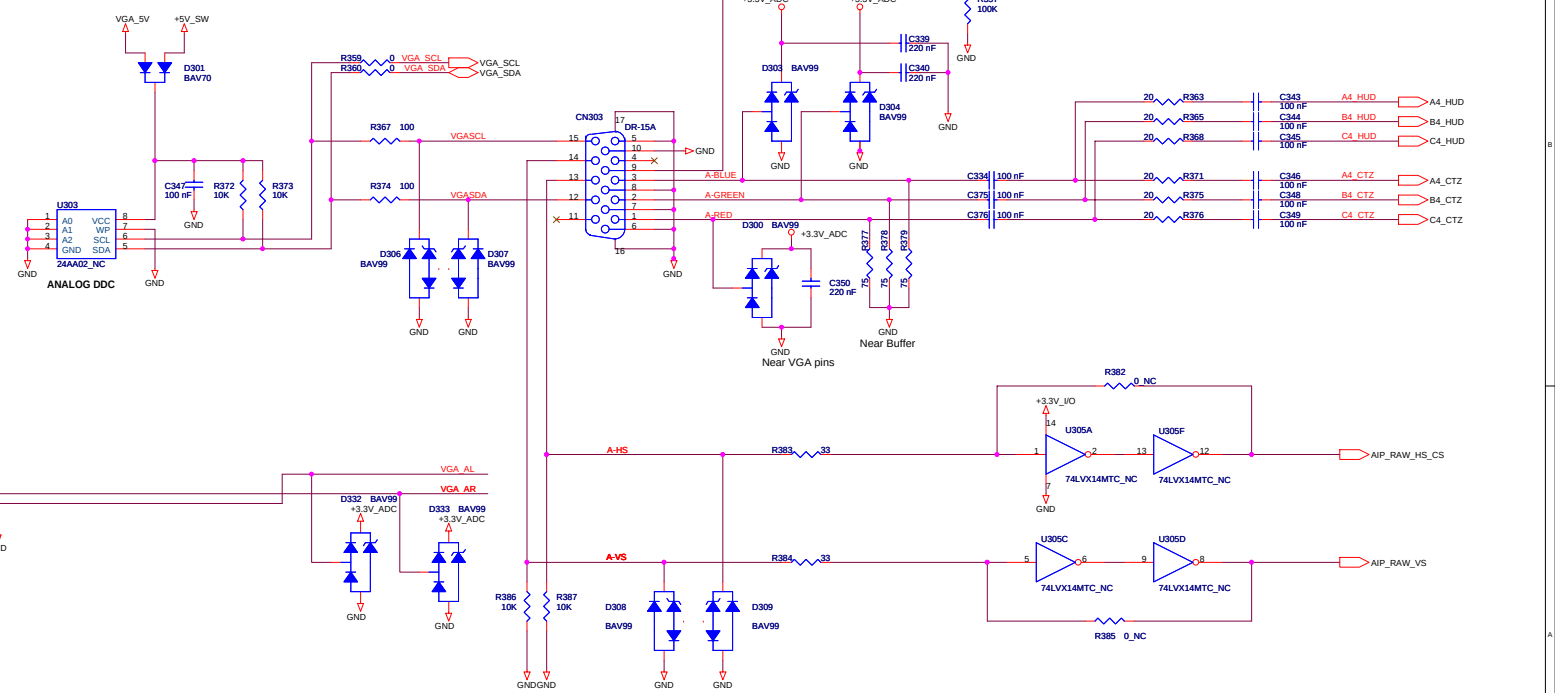
HD&AUDIO INPUT

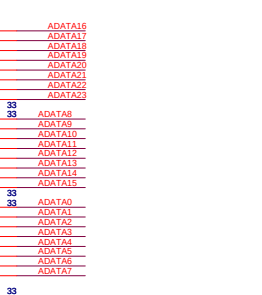
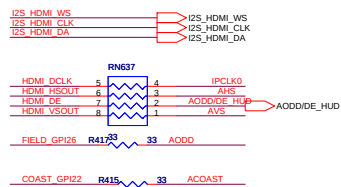


ANALOG INPUT TO MUX SWITCHING

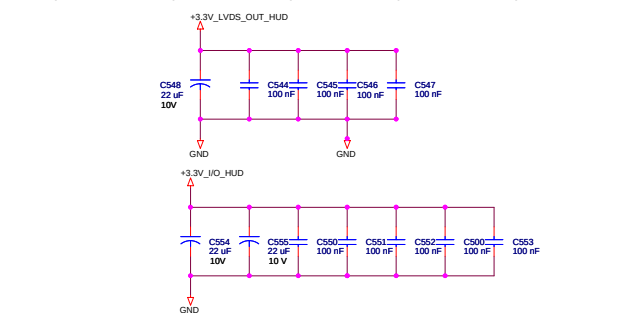
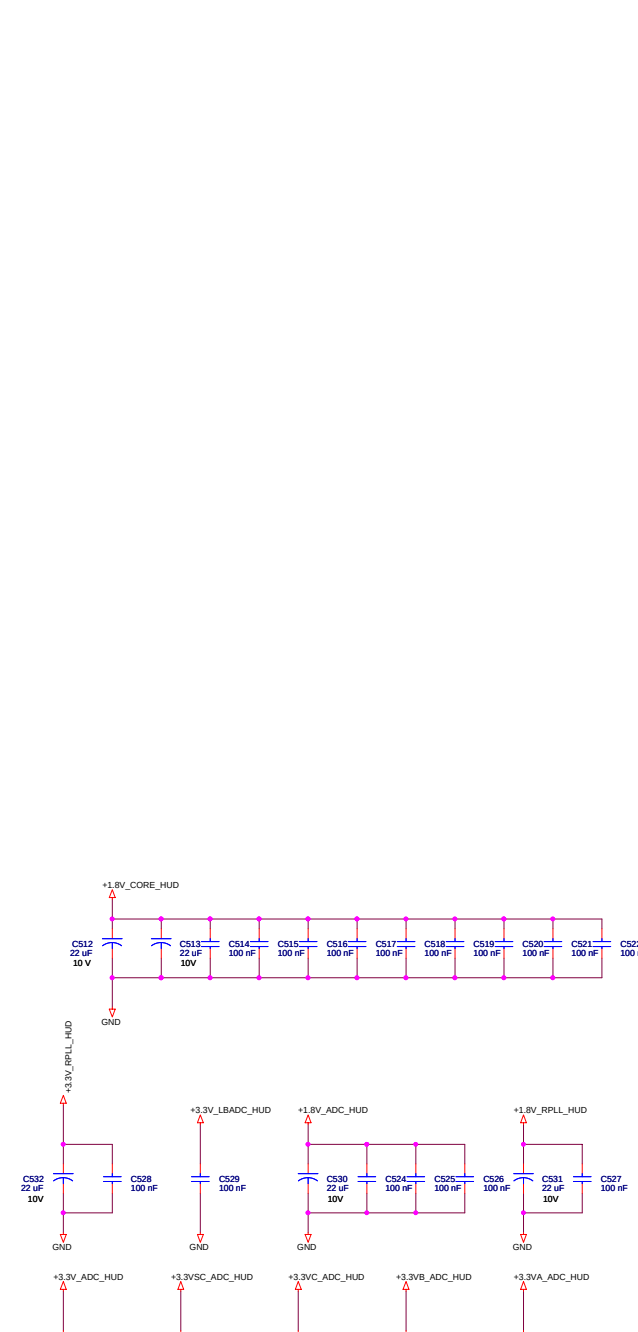
	1	2	3	4
A	SVIDEO-Y	COMP-1_Pr	SCART-RED	VGA-BLUE
B	VIDEO3	COMP-1_Y	SCART-GREEN	VGA-GREEN
C	SCRTFBC	COMP-1_Pb	SCART-RED	VGA-RED
SV	TUNER-CVBS	VIDEO2	SVIDEO-C	VIDEO1
RET				

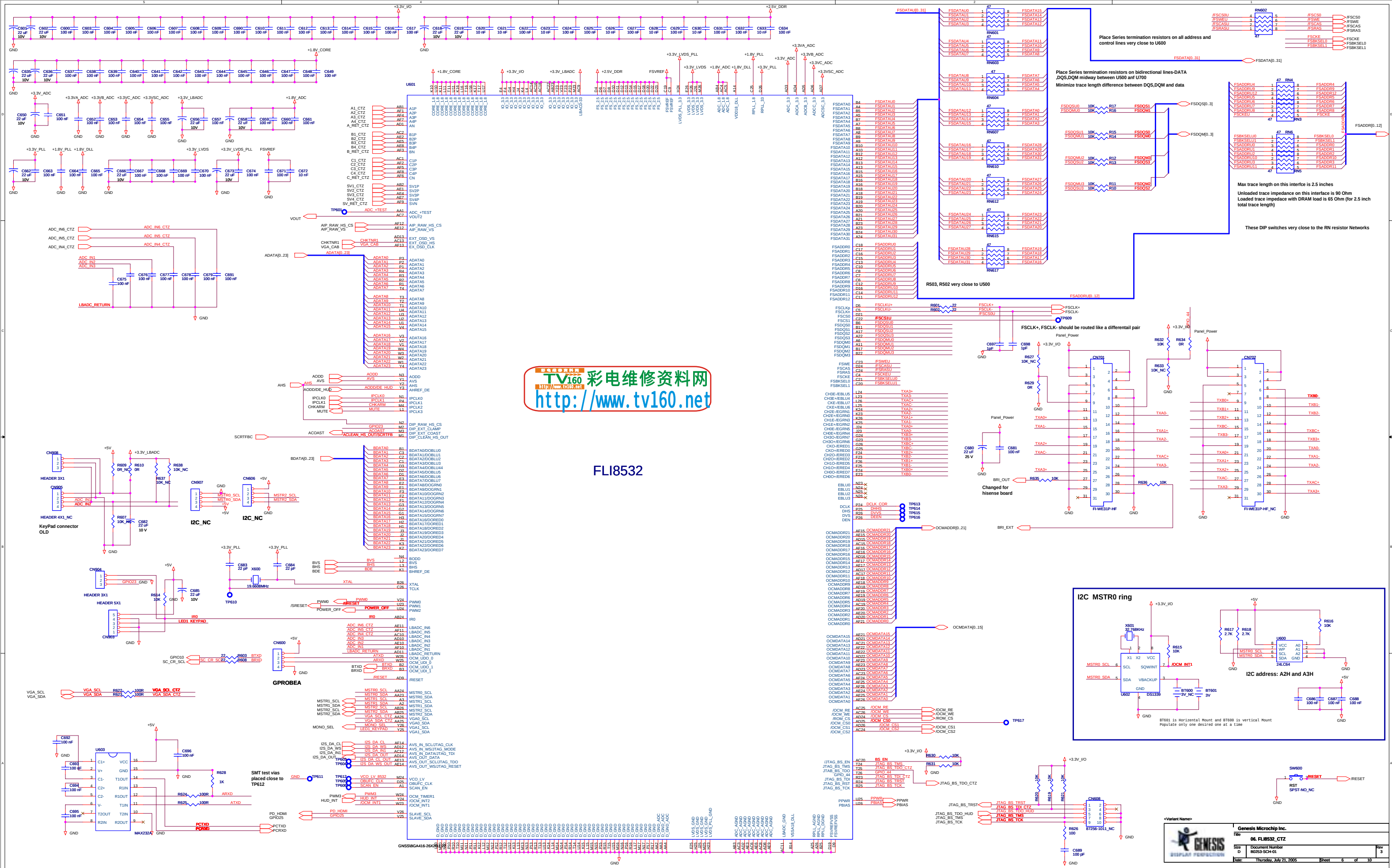
VGA INPUT

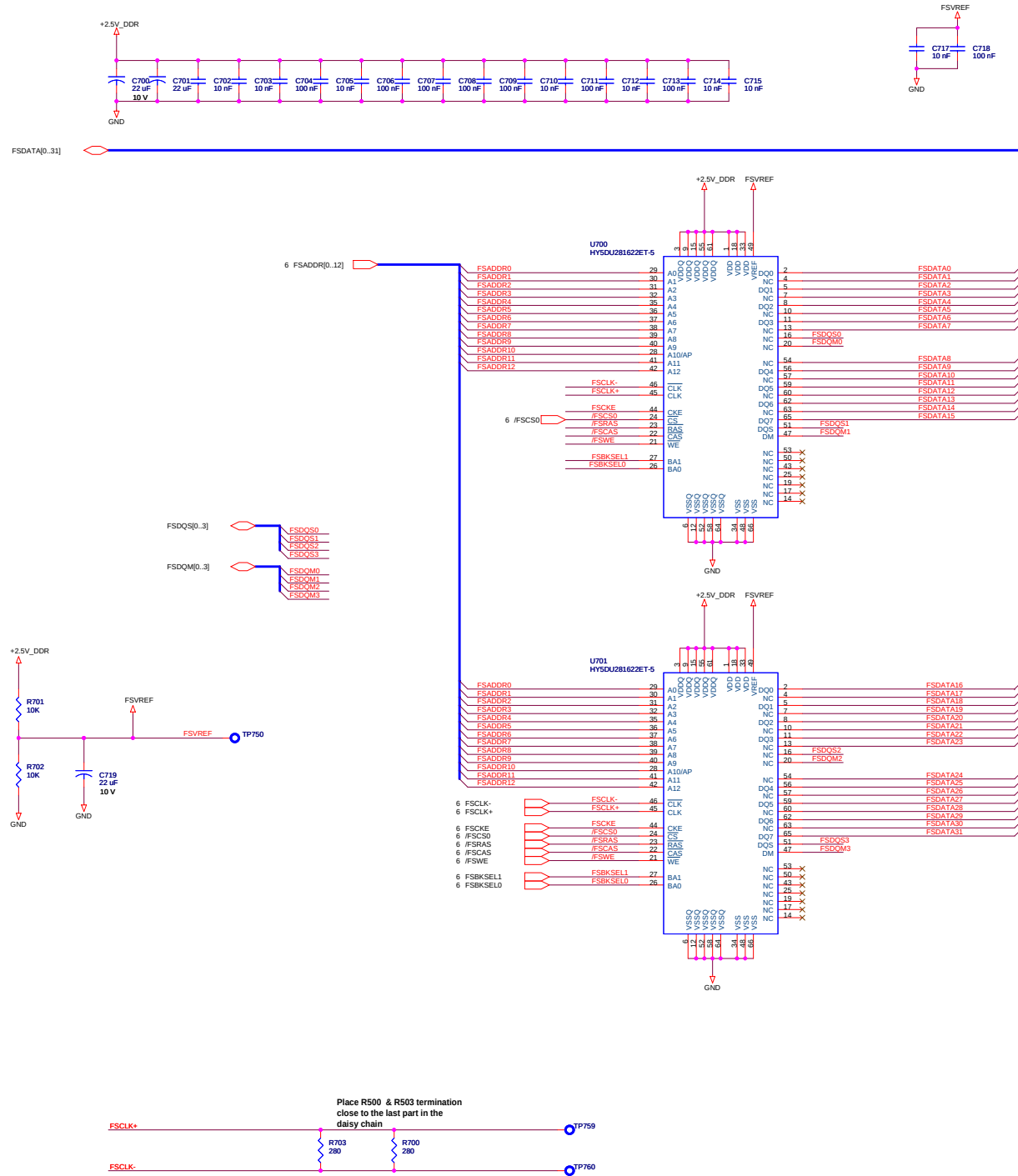


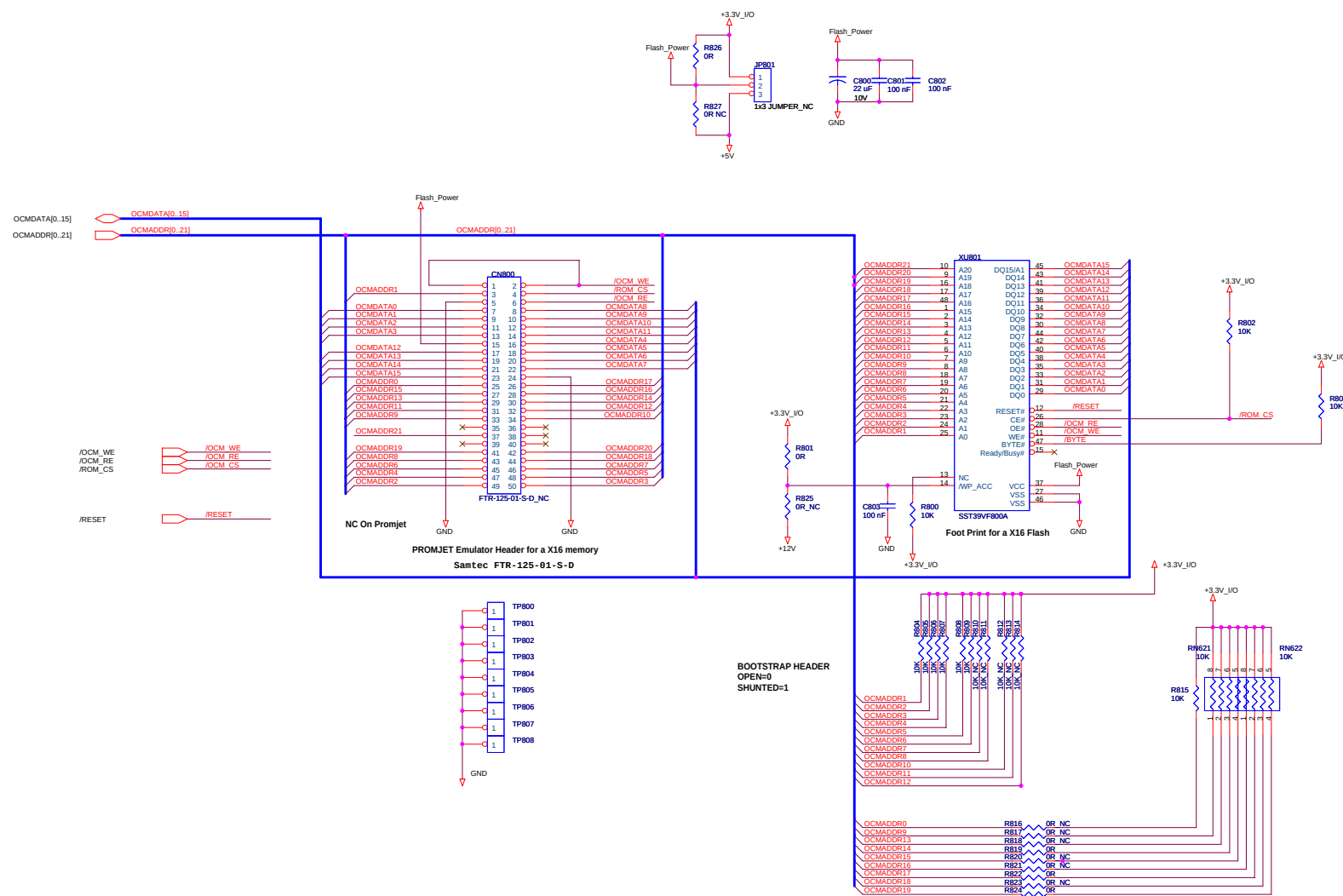


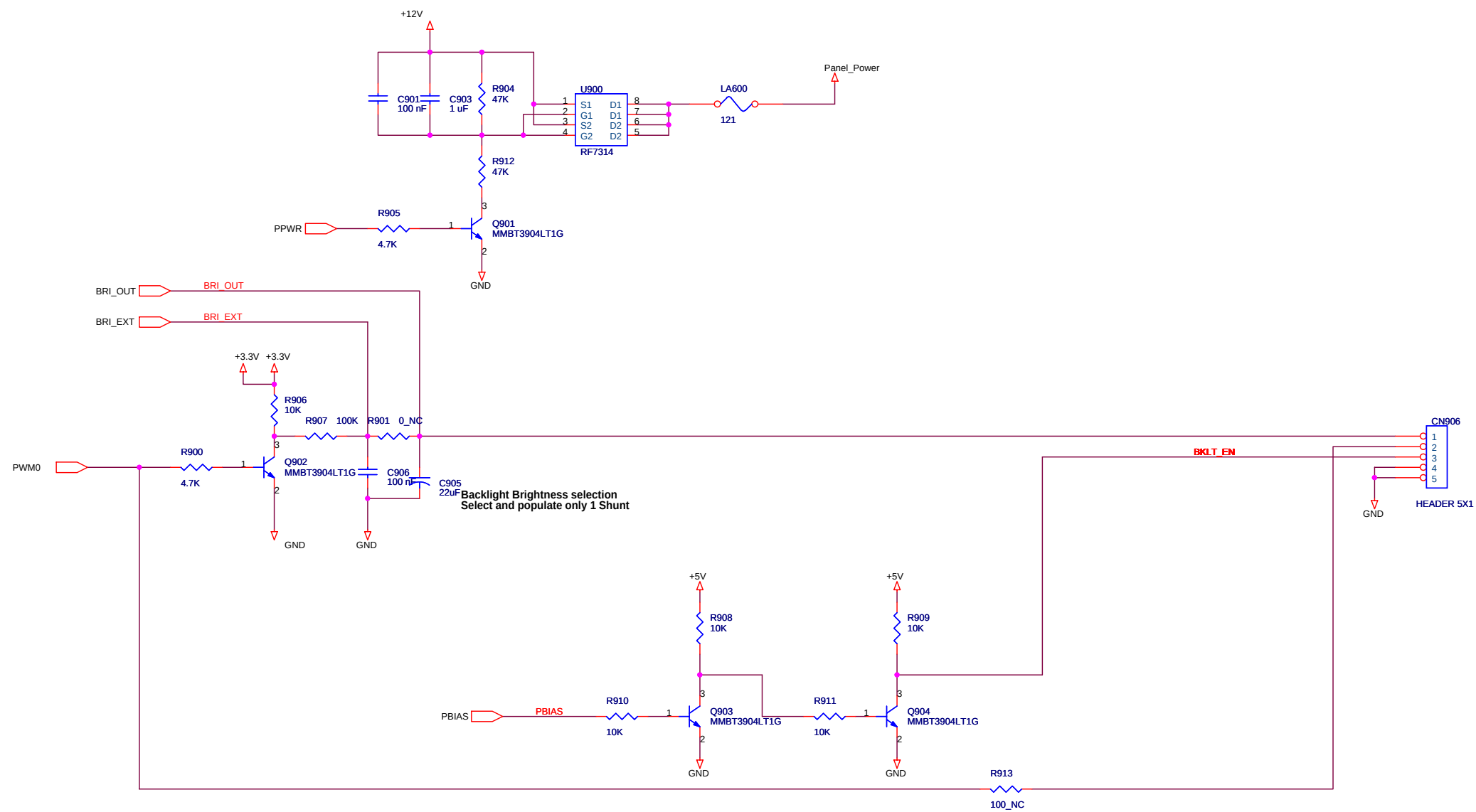
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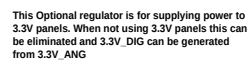
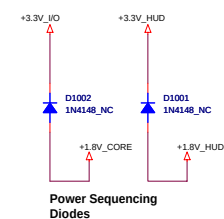






<Variant Name>

		Genesis Microchip Inc.			
		Title			
		09. Outputs			
		Size			
C	Document Number B0253-SCH-01			Rev 3	
Date:		Wednesday, July 20, 2005		Sheet 9 of 10	



This Optional regulator is for supplying power to 3.3V panels. When not using 3.3V panels this can be eliminated and 3.3V_DIG can be generated from 3.3V_ANG

