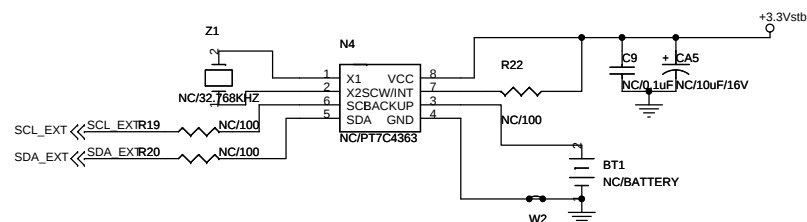
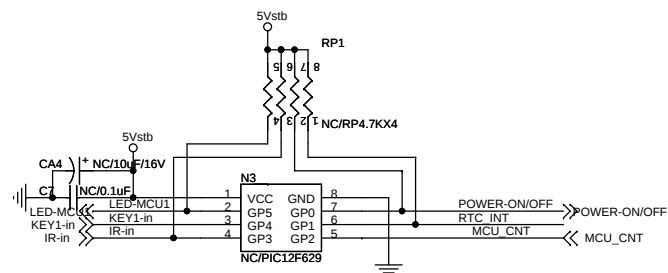




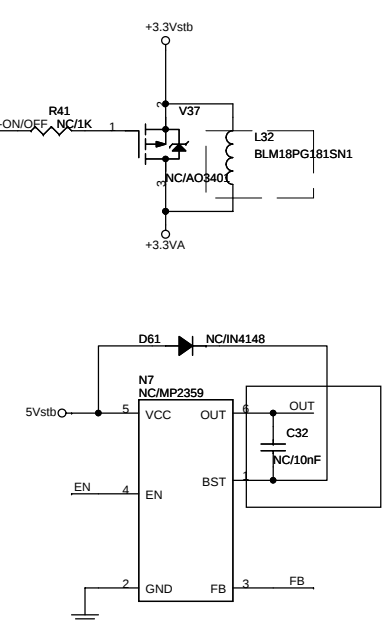
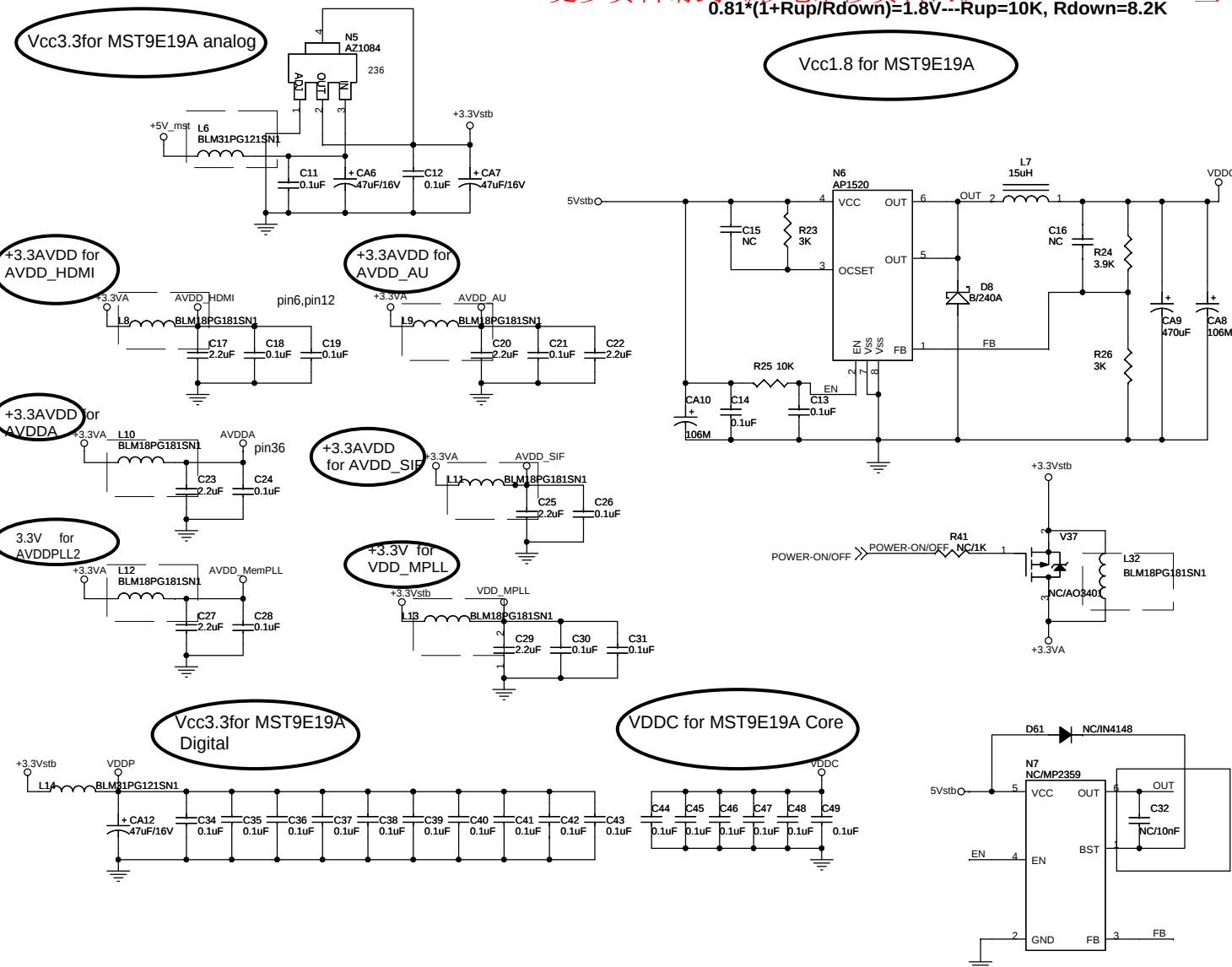
The schematic diagram shows the power supply section. It includes three input lines: +5V_mst, 5Vstb, and +12V_in. The +5V_mst and 5Vstb lines are connected to a common horizontal line. The +12V_in line is connected to a separate horizontal line, which is labeled +12V_all at its output end.



ter Board

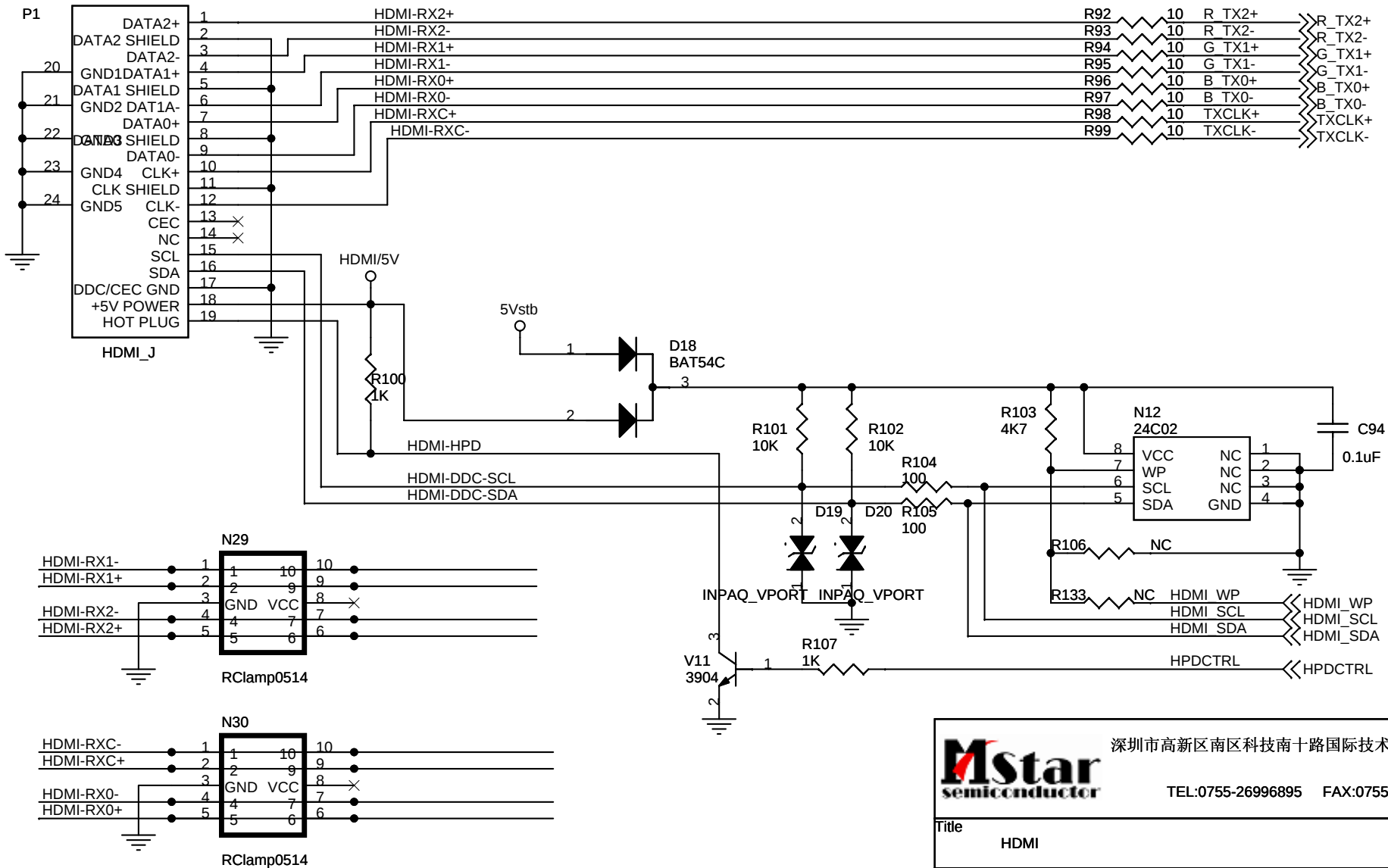
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 A <http://www.tv160.net>
 BY December 13, 2007 Sheet 1 of 10
<http://www.tv160.net>





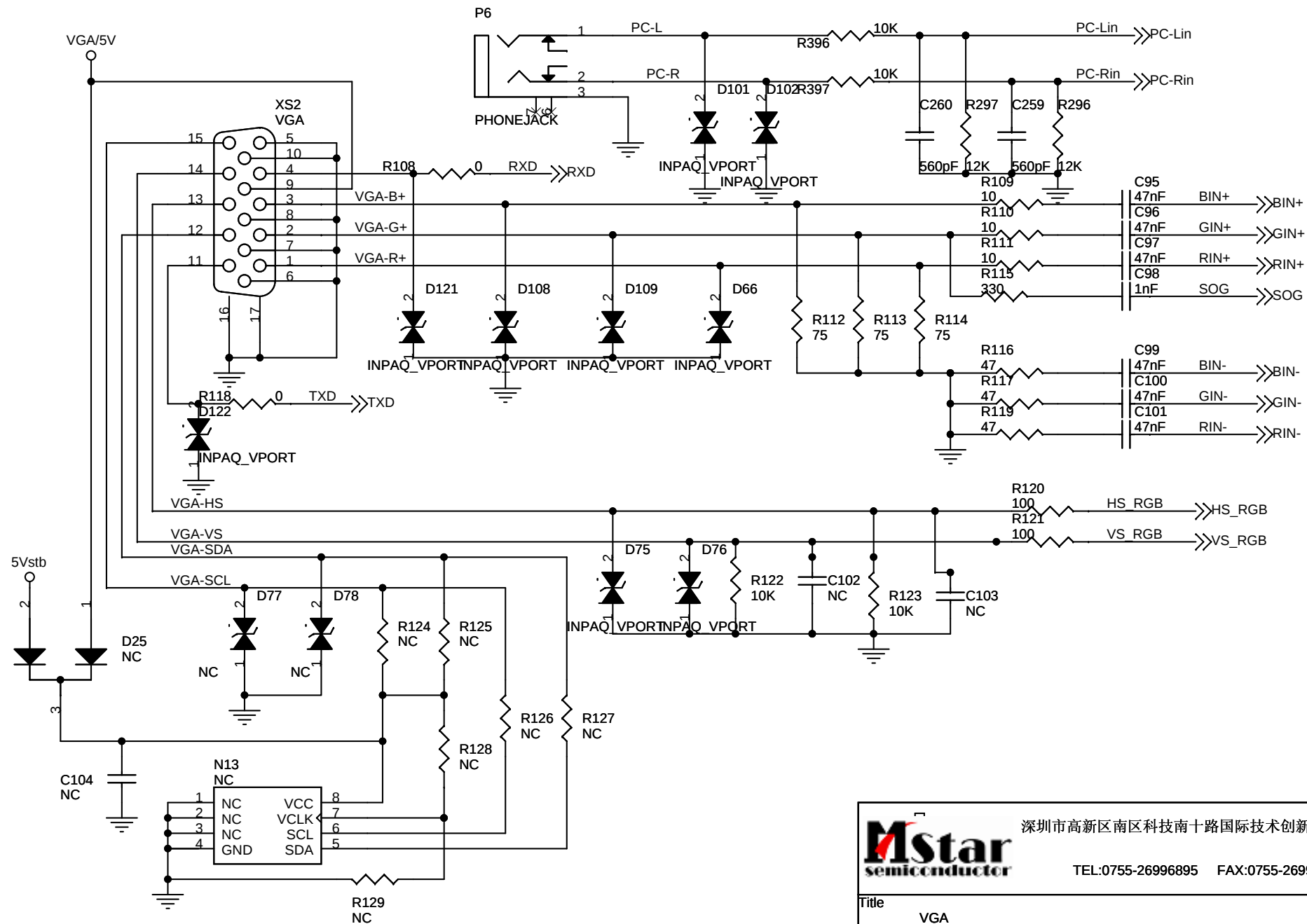


D
C
B
A



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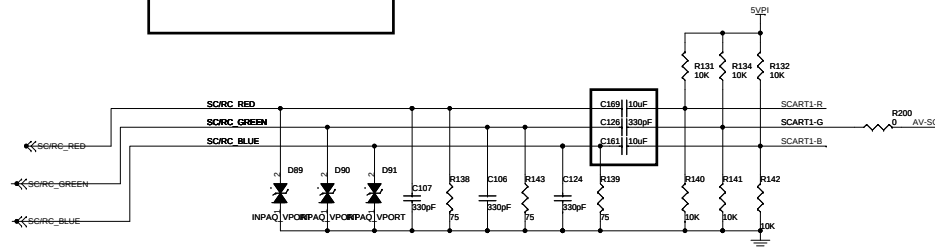
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Date: Wednesday, November 21, 2007	Rev 19



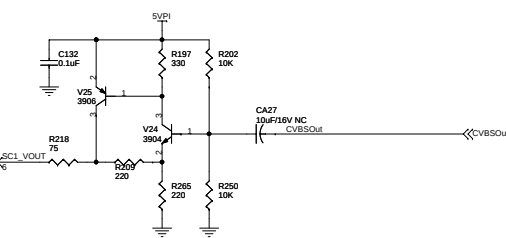
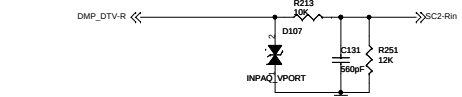
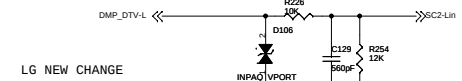
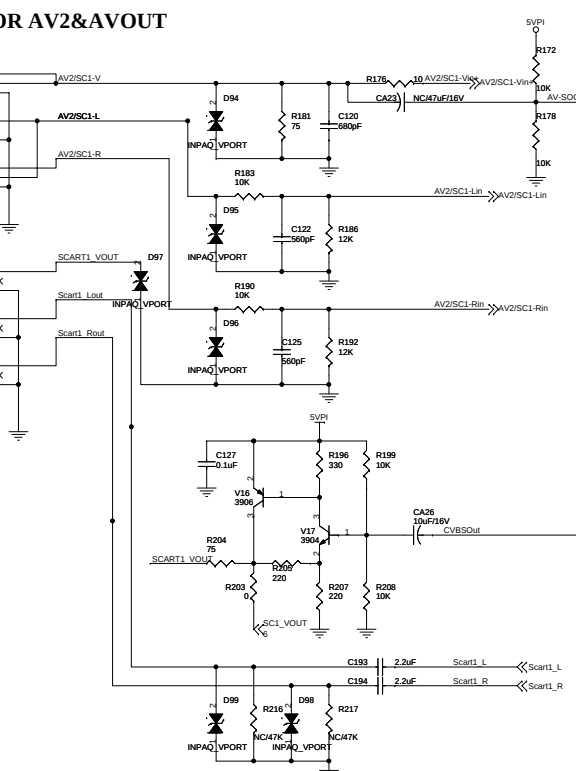
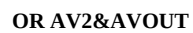
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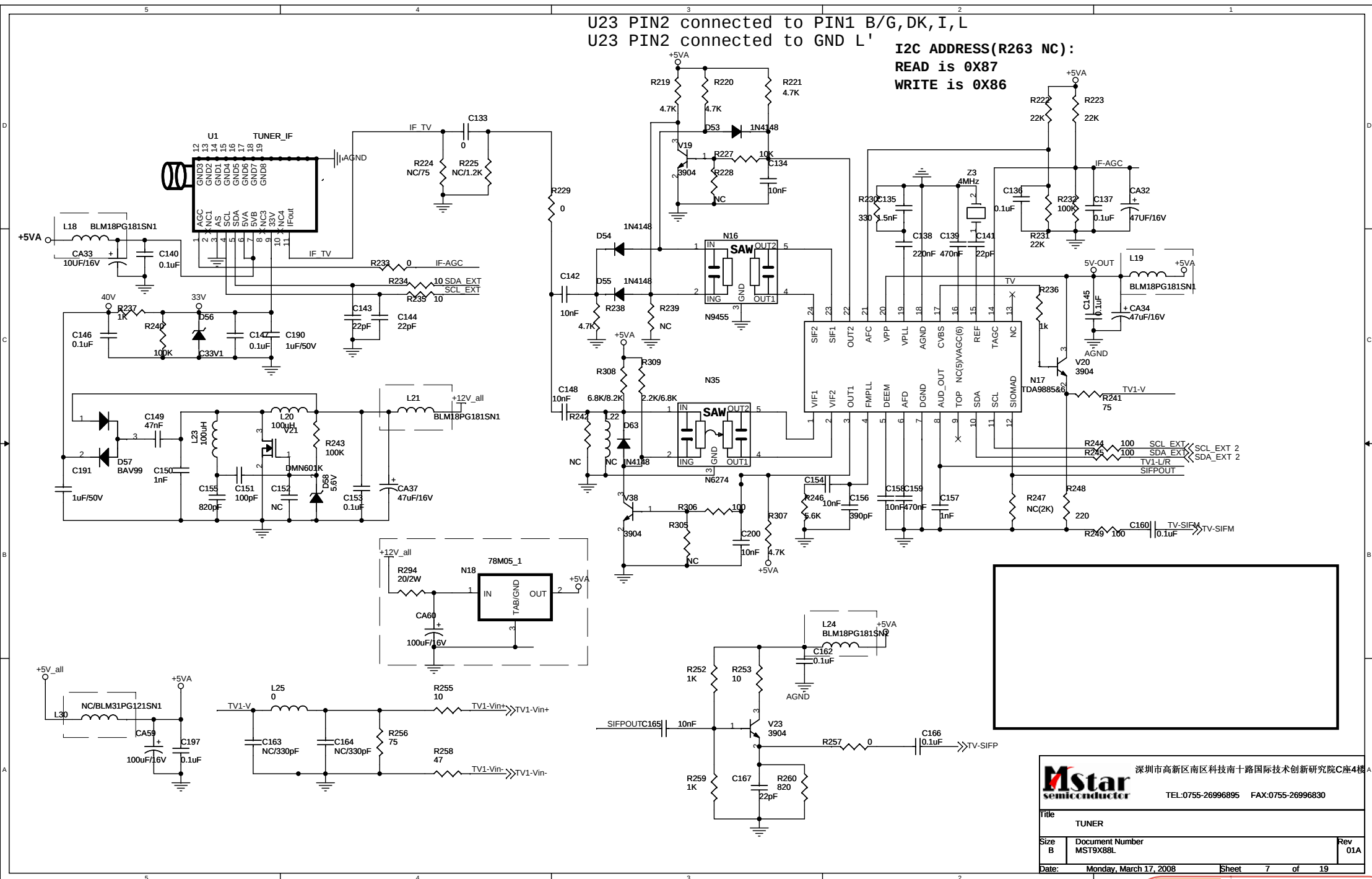
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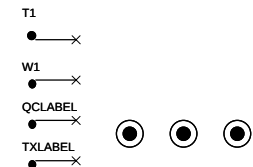
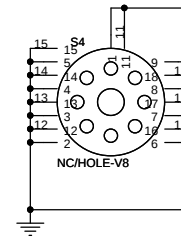
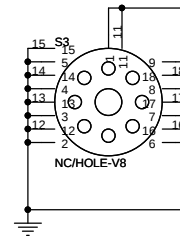
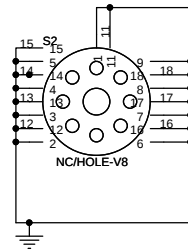
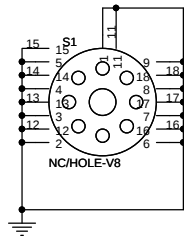
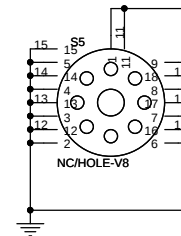
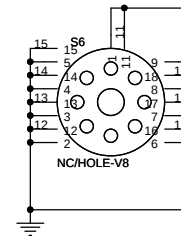
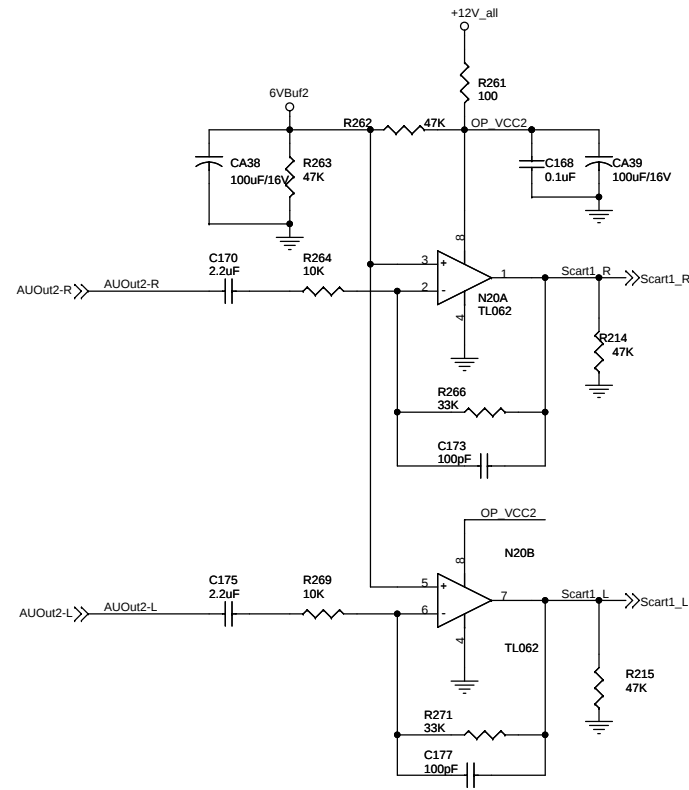
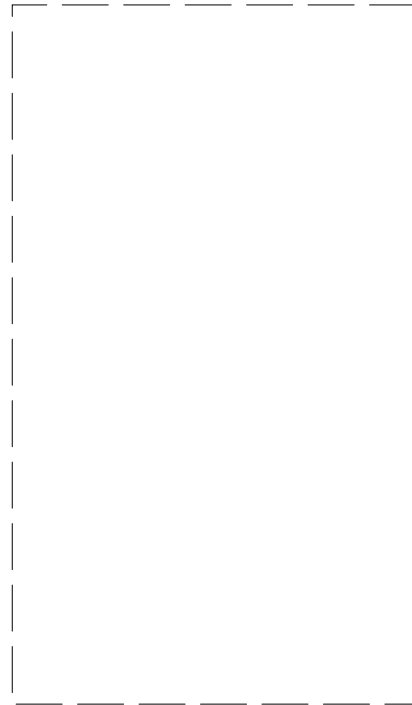


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Pb1	R174	0	330	1 B
Pr1	R175	0	330	1 R
330	1 G R299	0	330	SOG

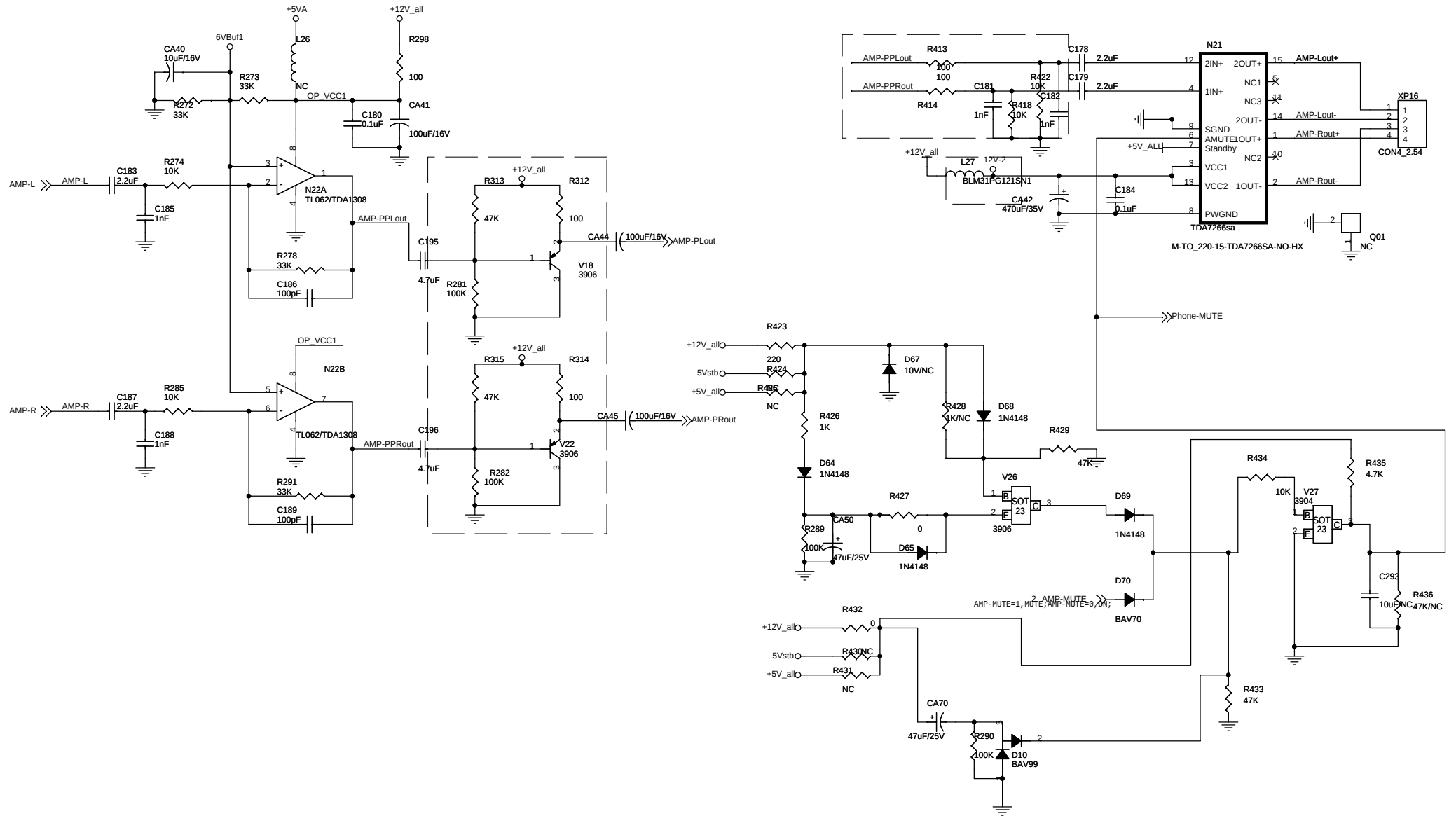




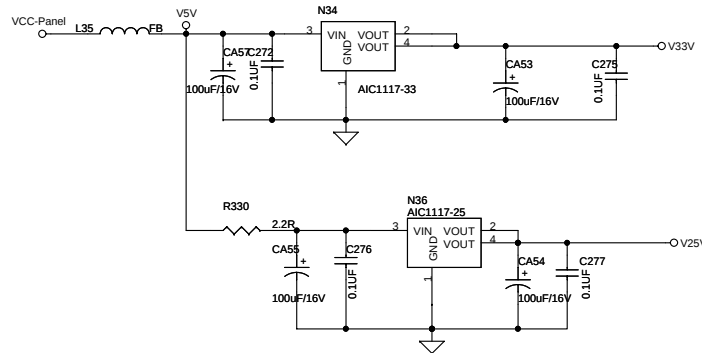
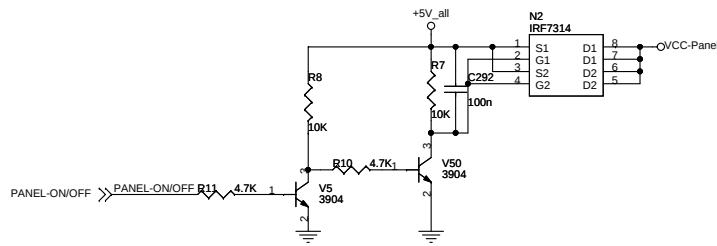
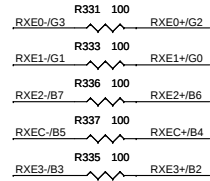
Audio OP(AUOUTL3/AUOUTR3; AUOUTL2/AUOUTR2)



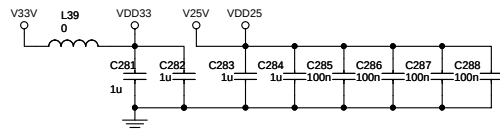
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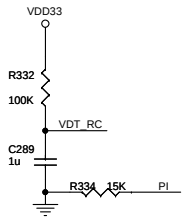
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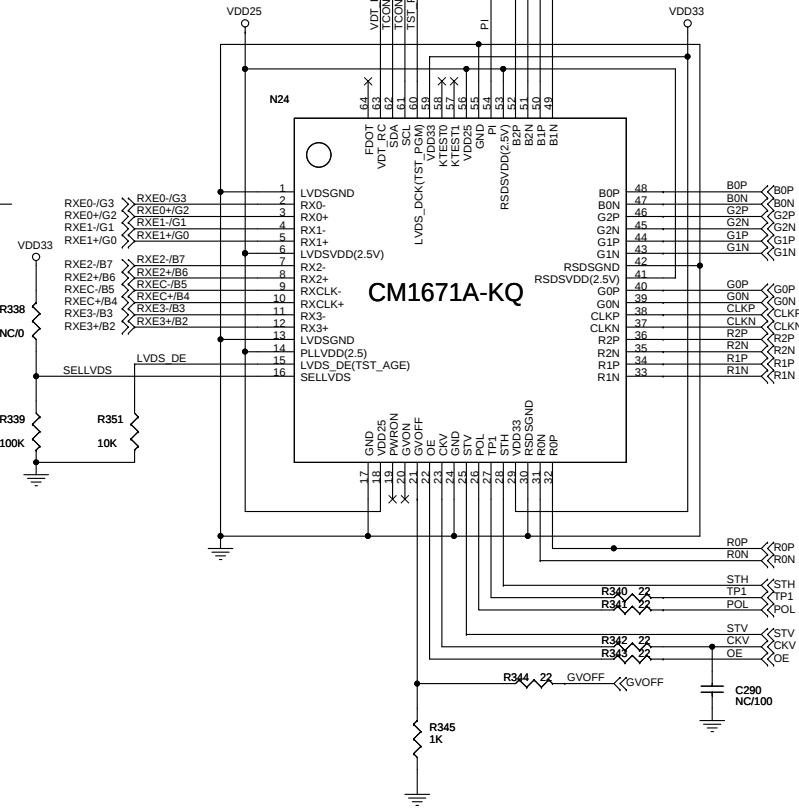
[Filter near T-CON]



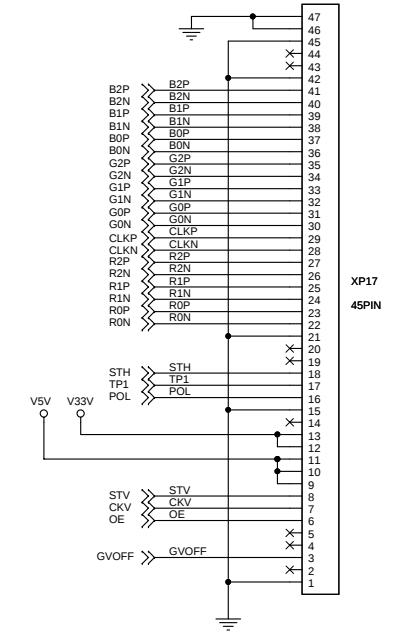
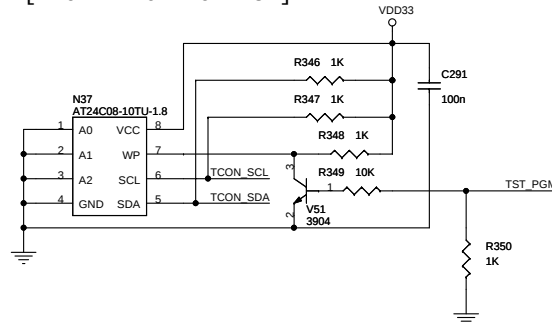
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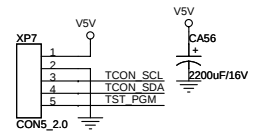
[T-CON]



[I2C EEPROM for LUT]



Debug port



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Size: Custom	Document Number: MST718B
Date:	Rev: B