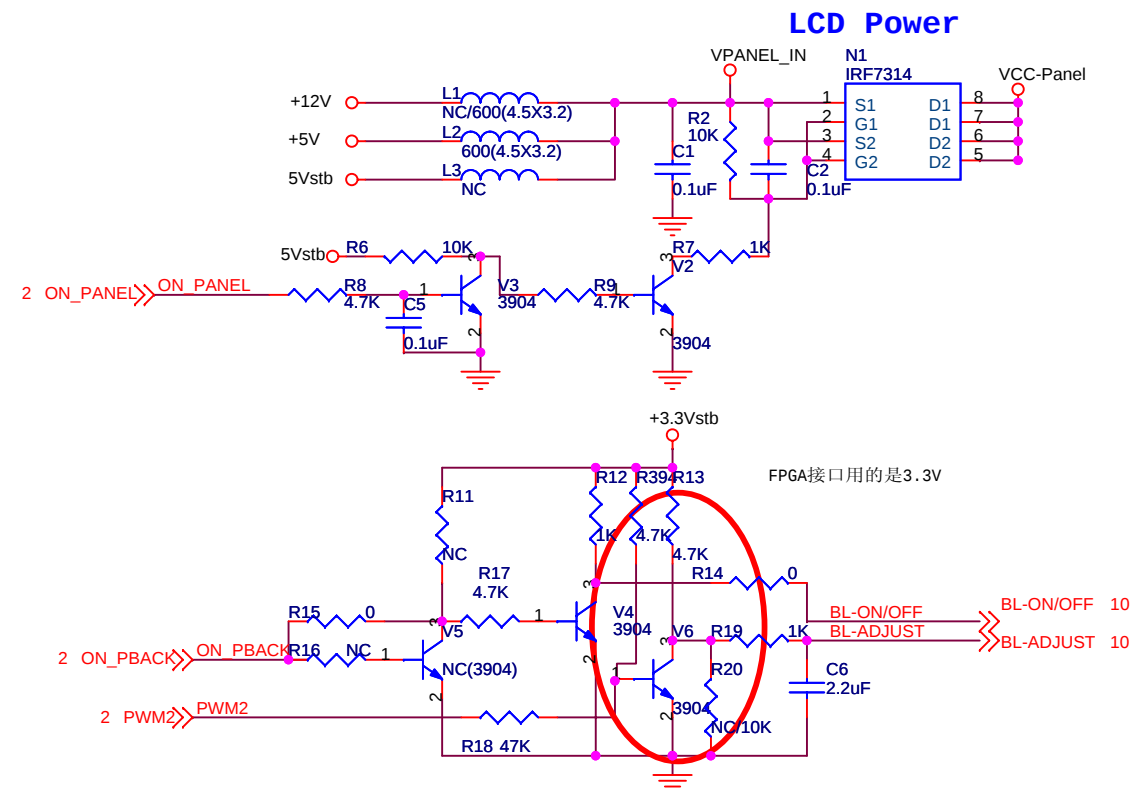
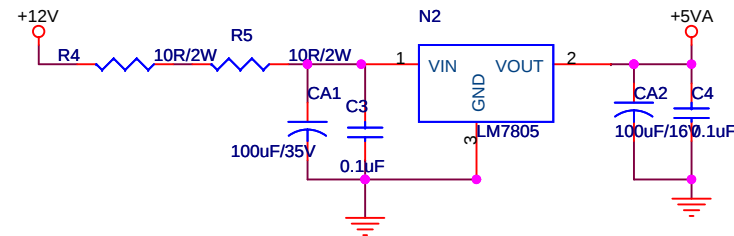
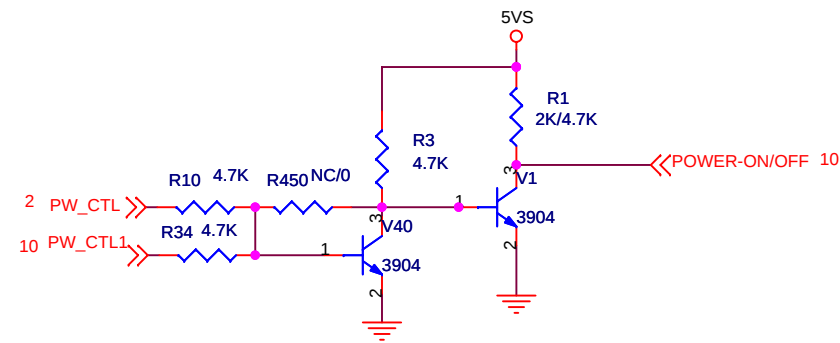
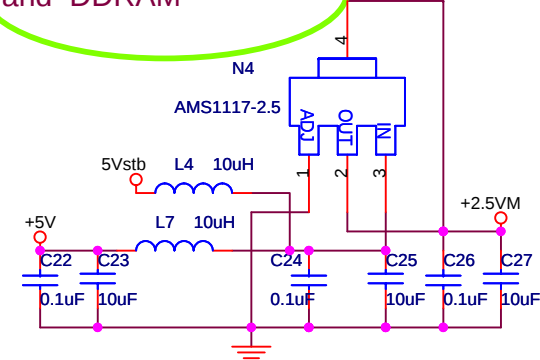
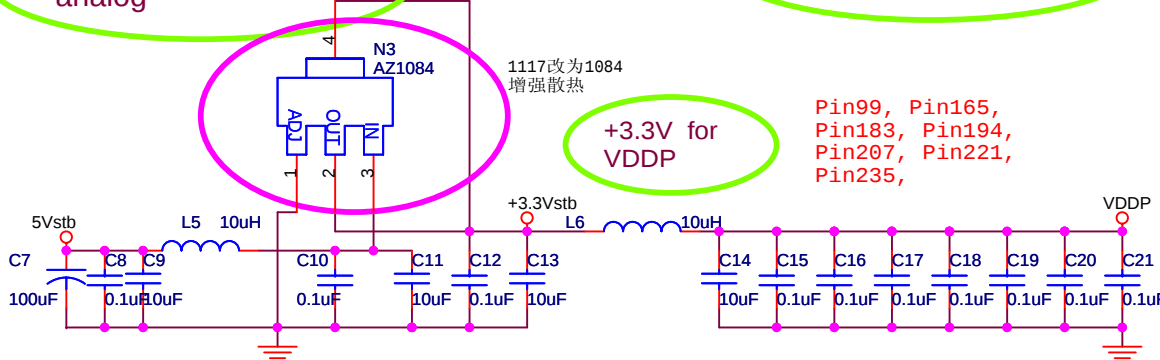




Vcc3.3for MST6X89BL analog

Vcc3.3for MST6X89BL analog

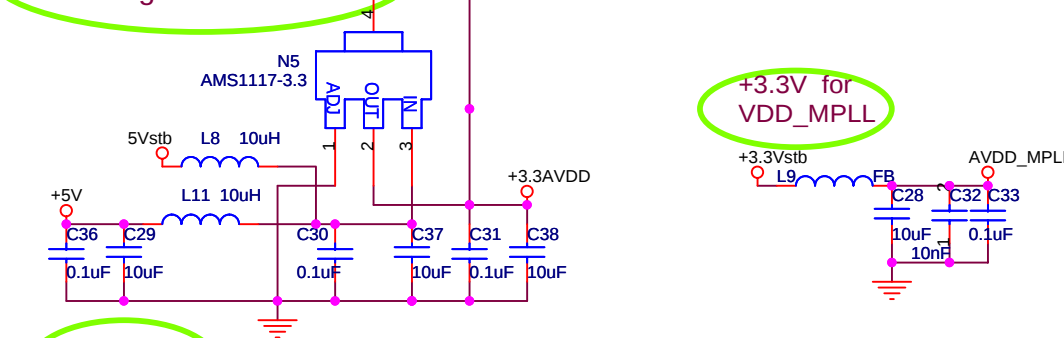
2.5V for MST6X89BL and DDRAM



Vcc3.3for MST6X89BL analog

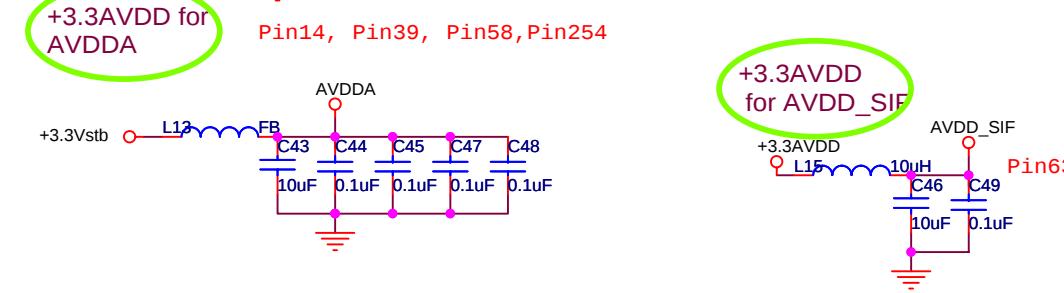
+3.3V for VDD_MPLL

2.5V for DDRAM



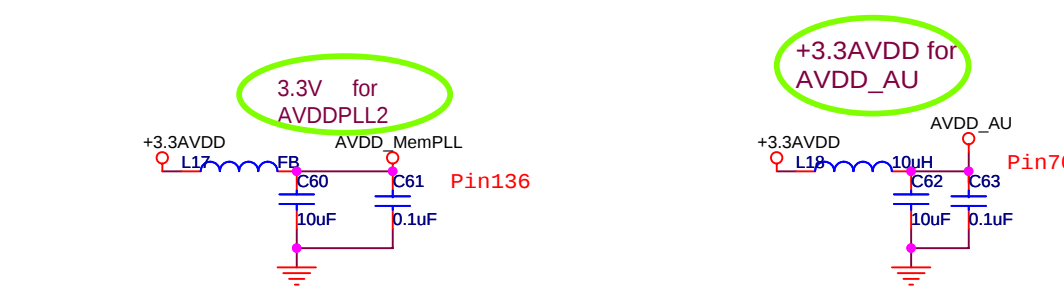
+3.3AVDD for AVDD_SIF

+2.5V for DDRAM

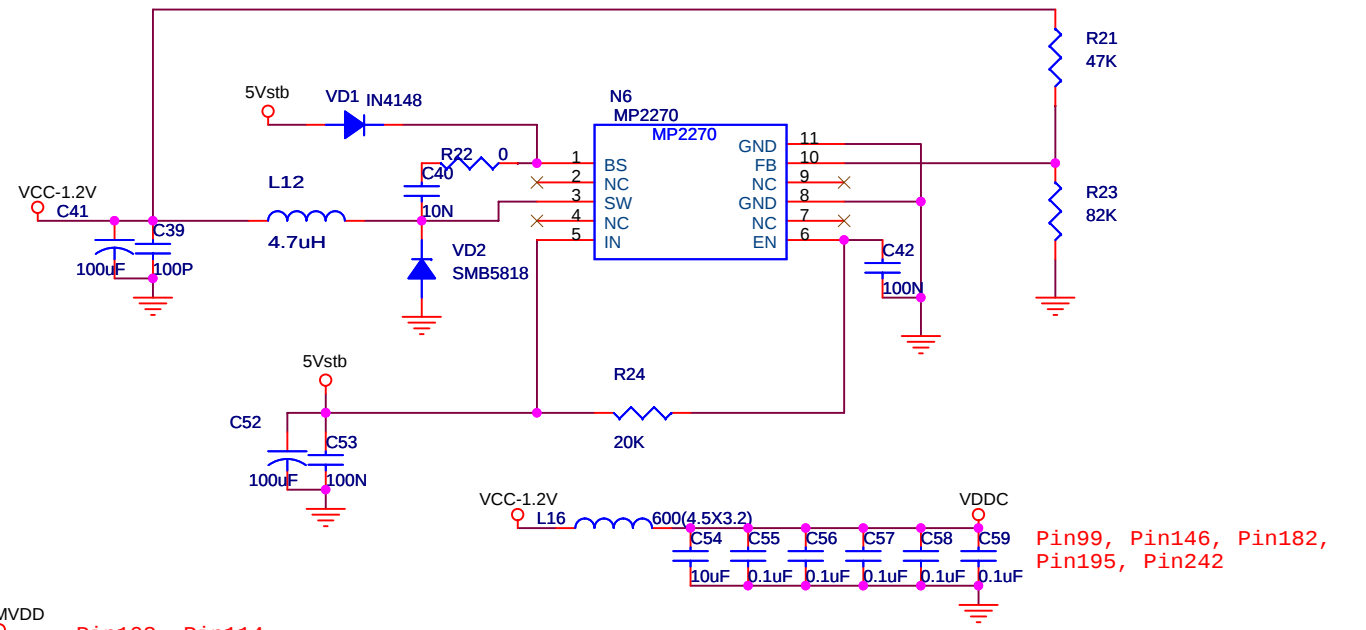


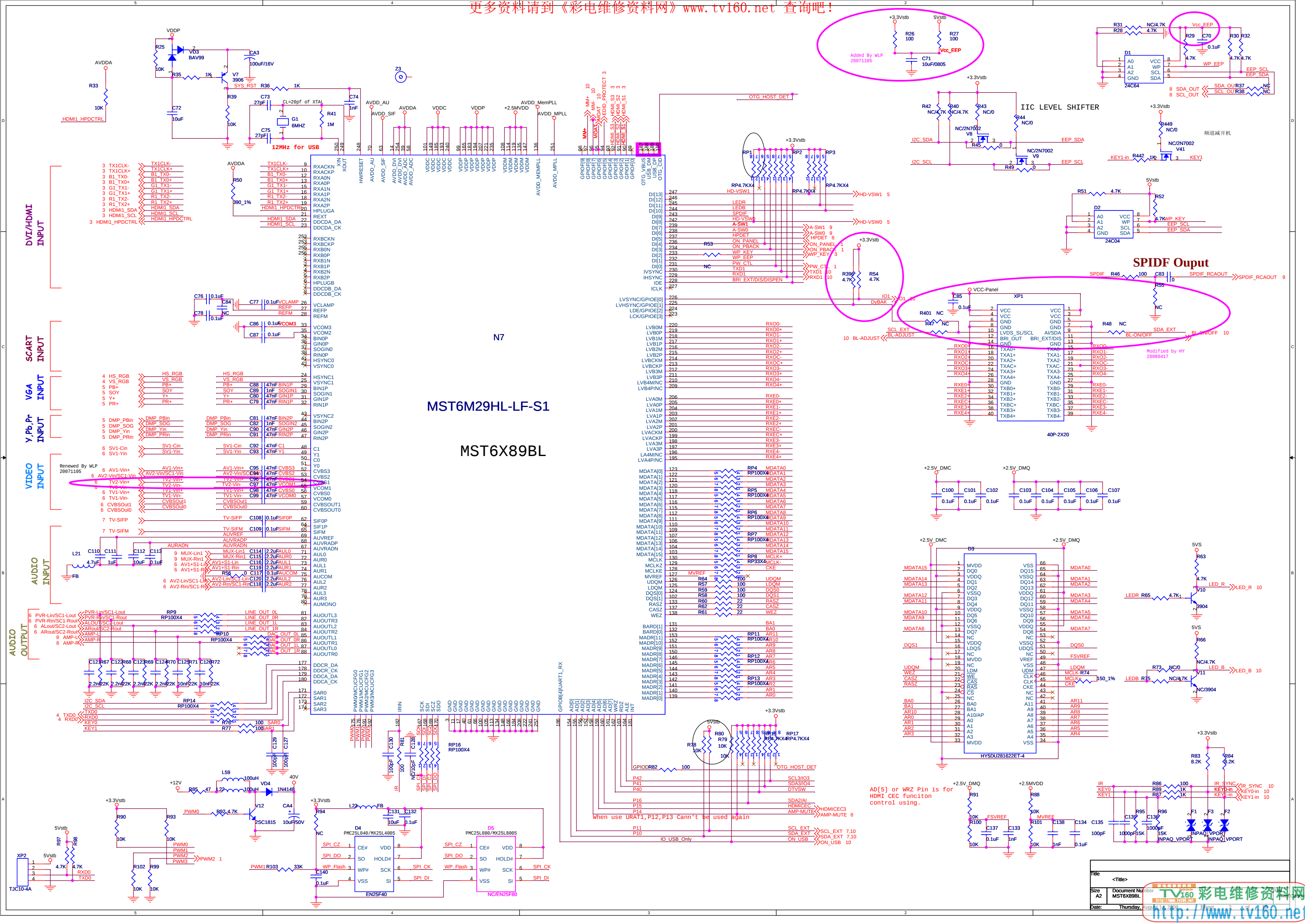
+3.3AVDD for AVDD_AU

+2.5V for DDRAM

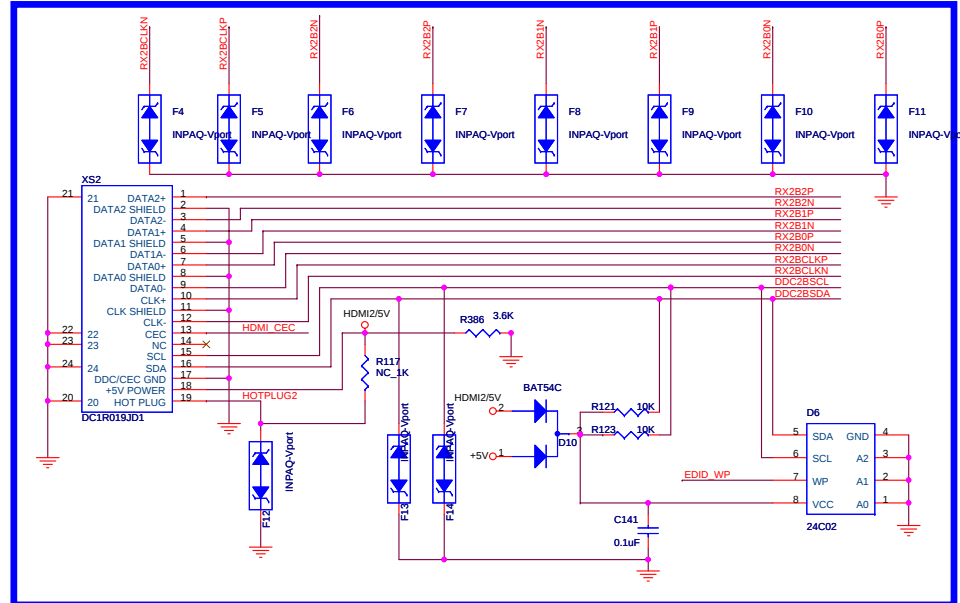


+2.5V for MST6X89BL

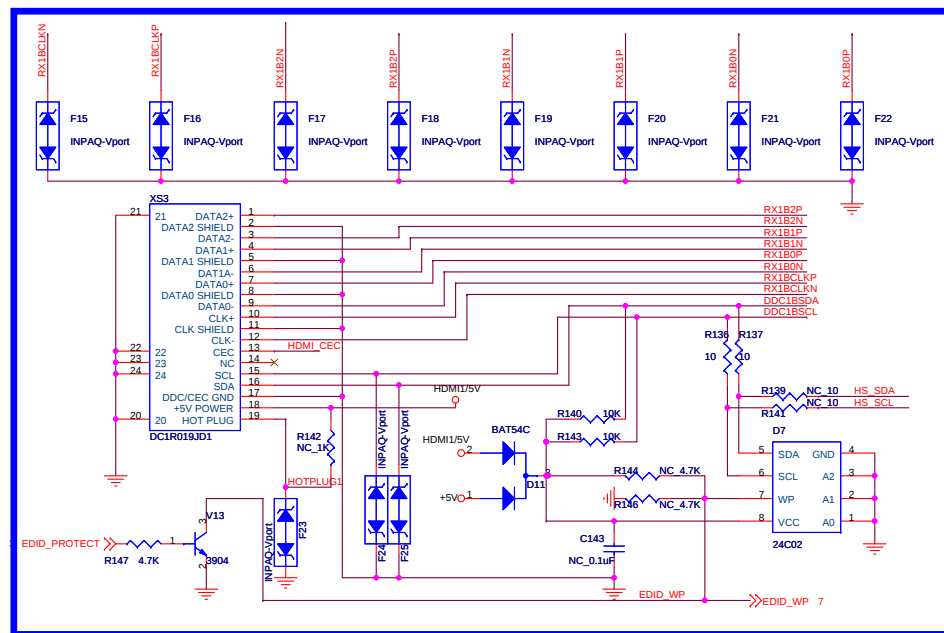




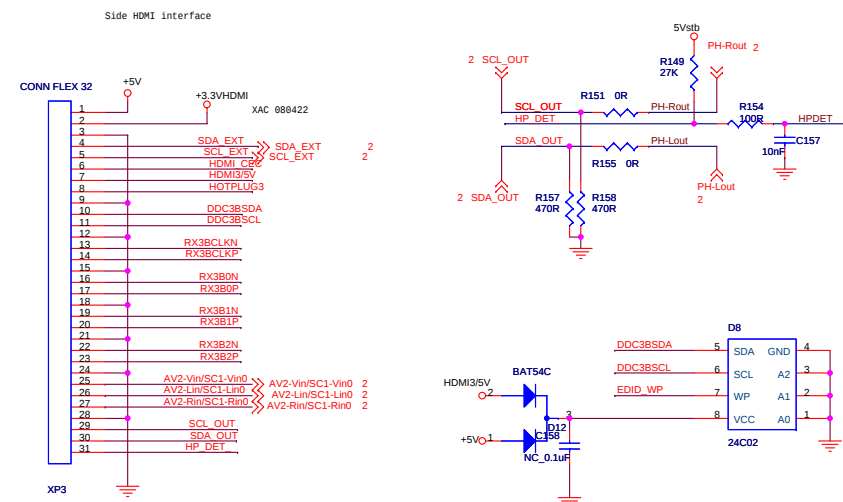
HDMI1



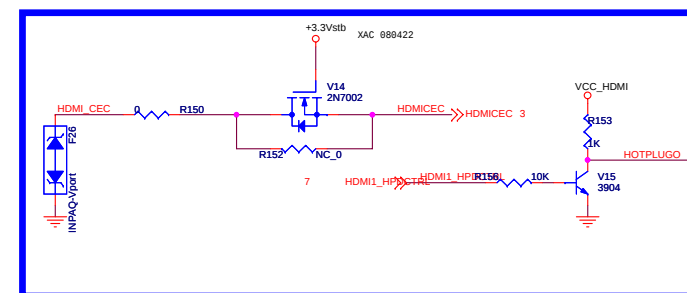
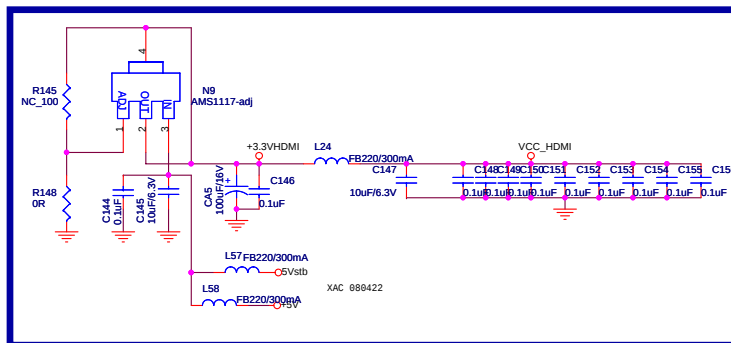
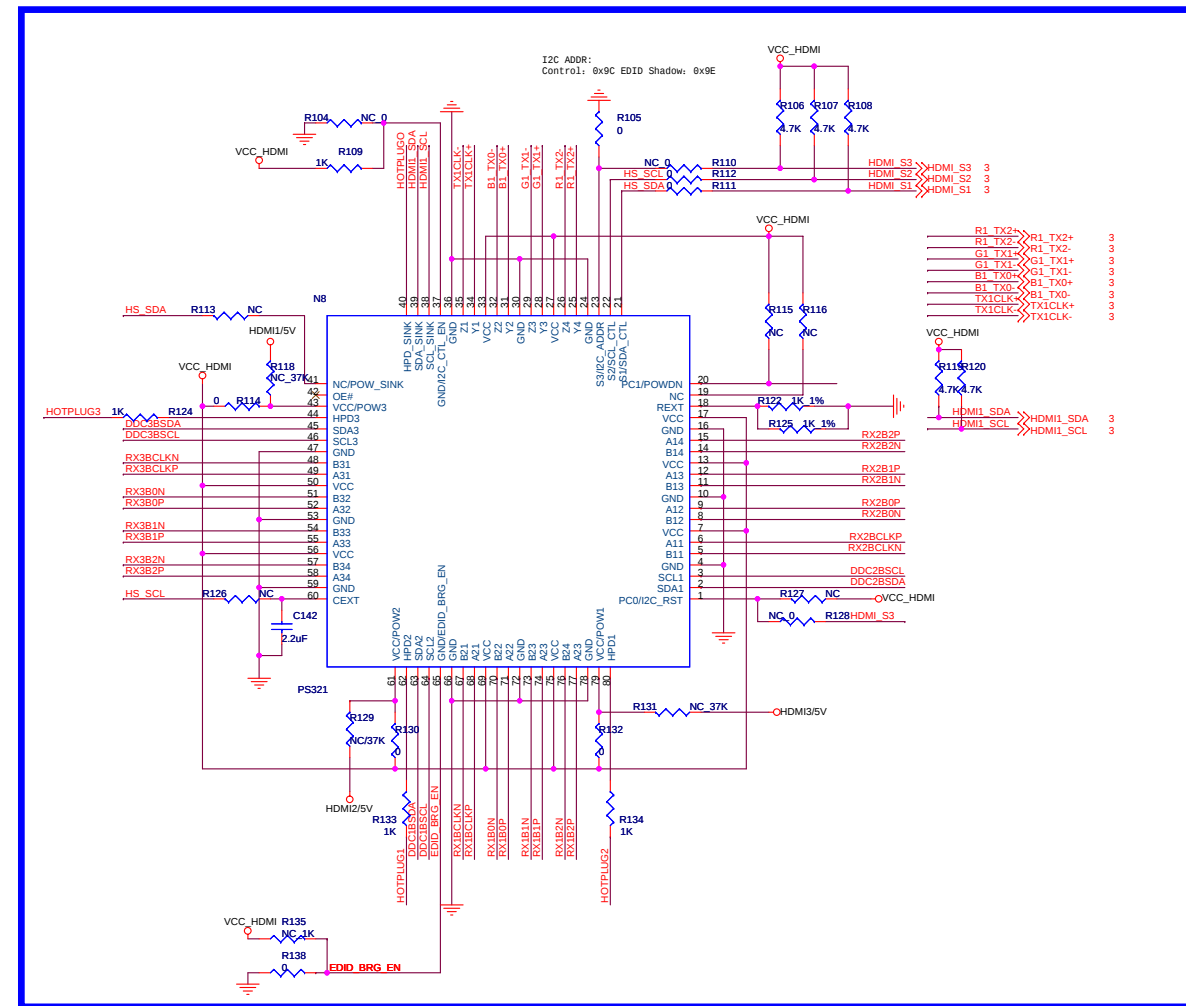
HDMI2

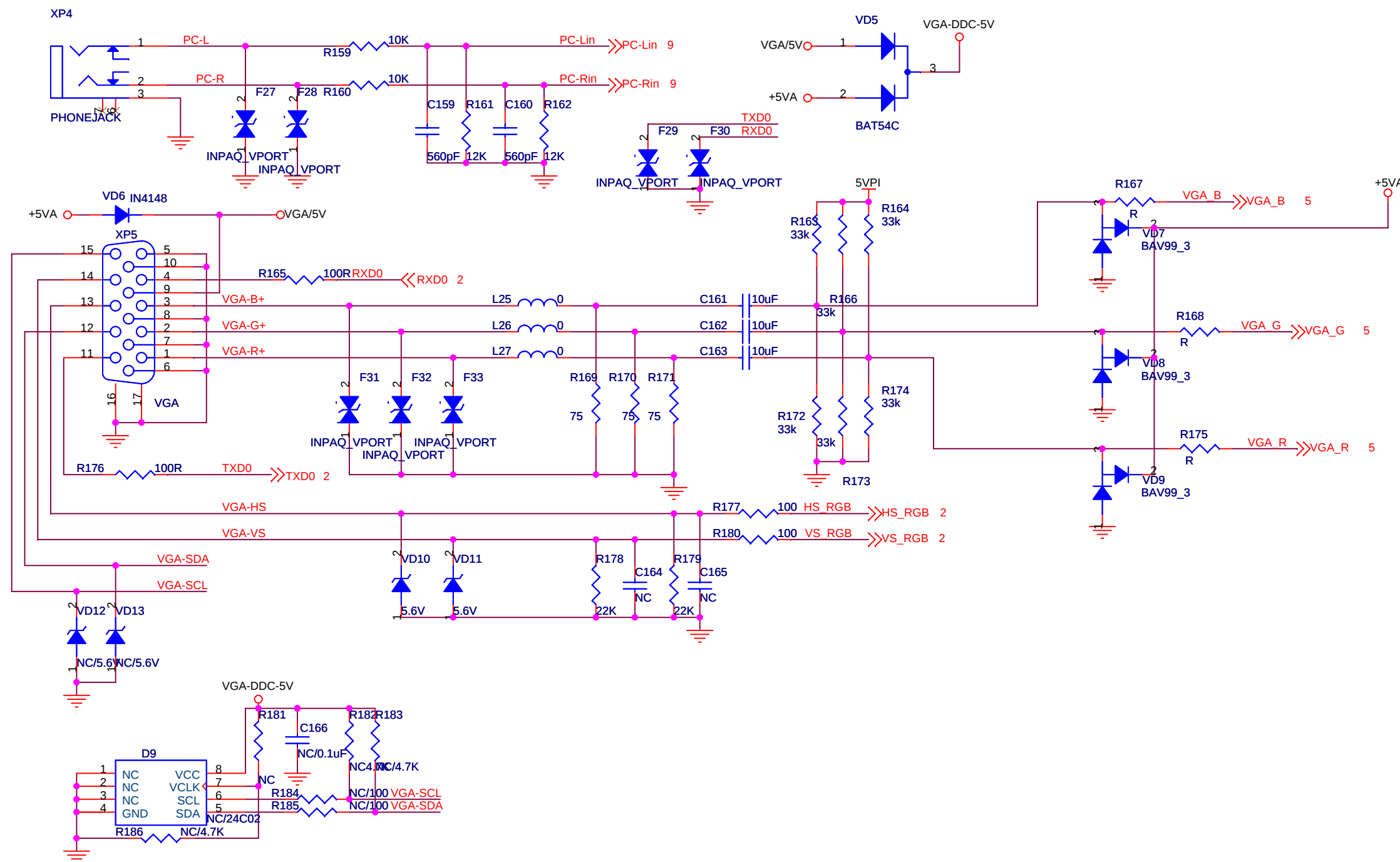


HDMI1/5V R389 3.6K

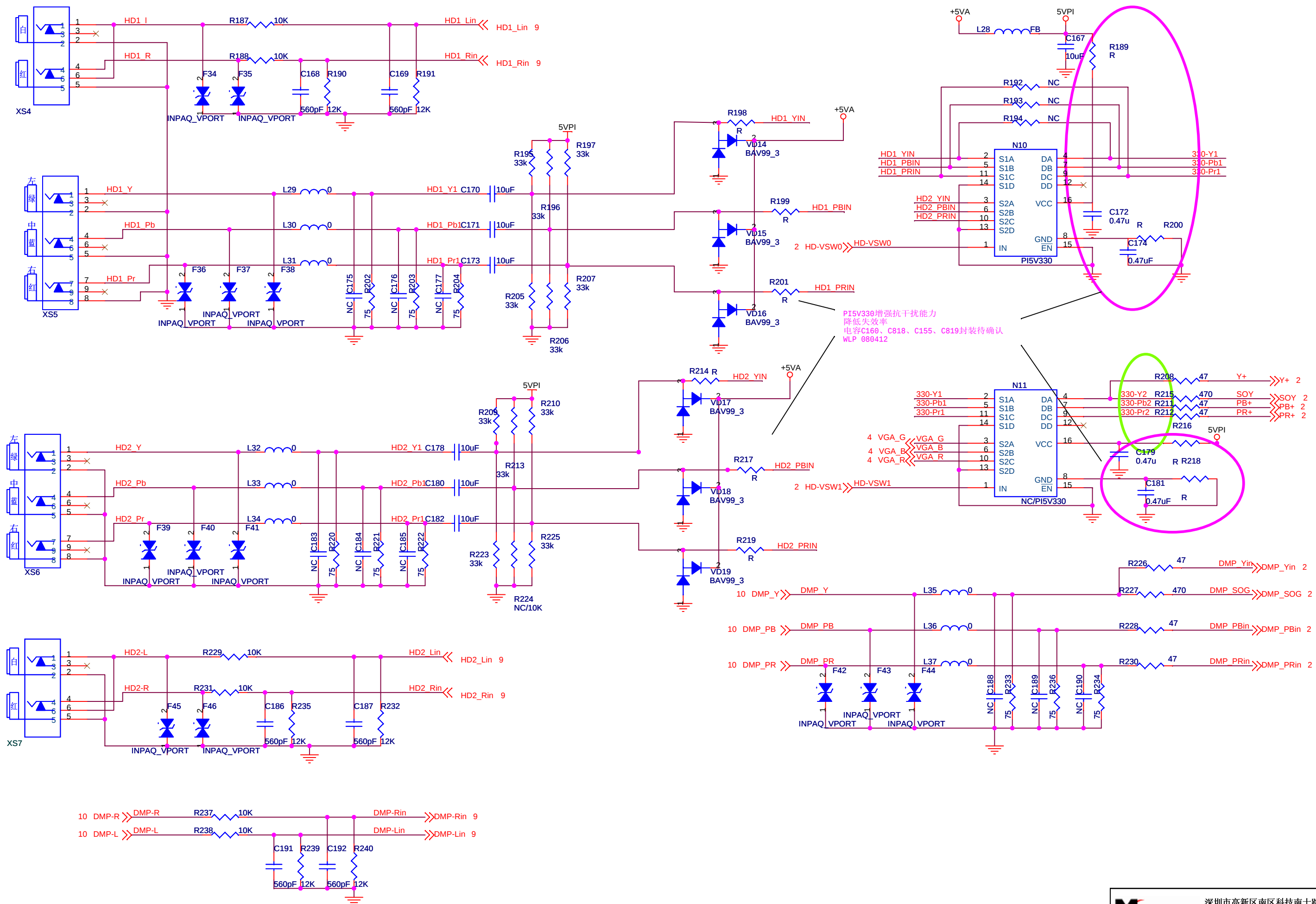


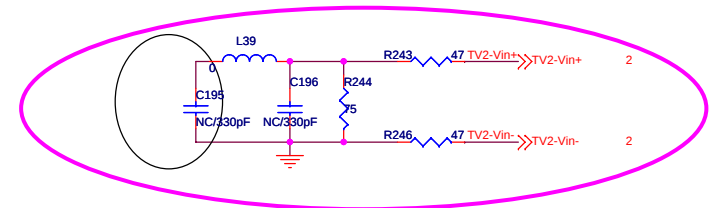
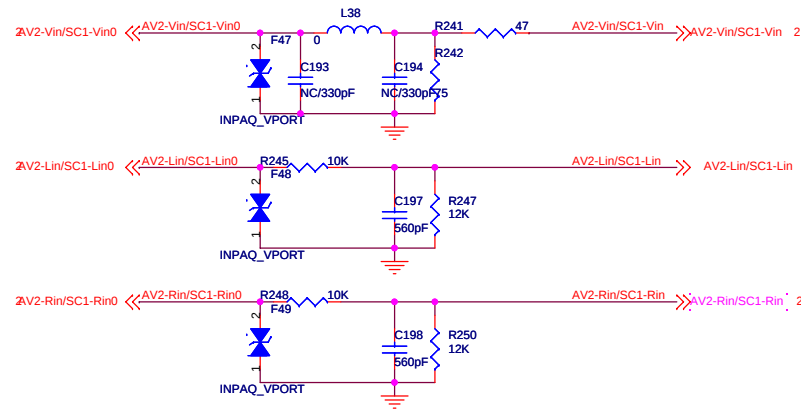
HDMI_CEC 10



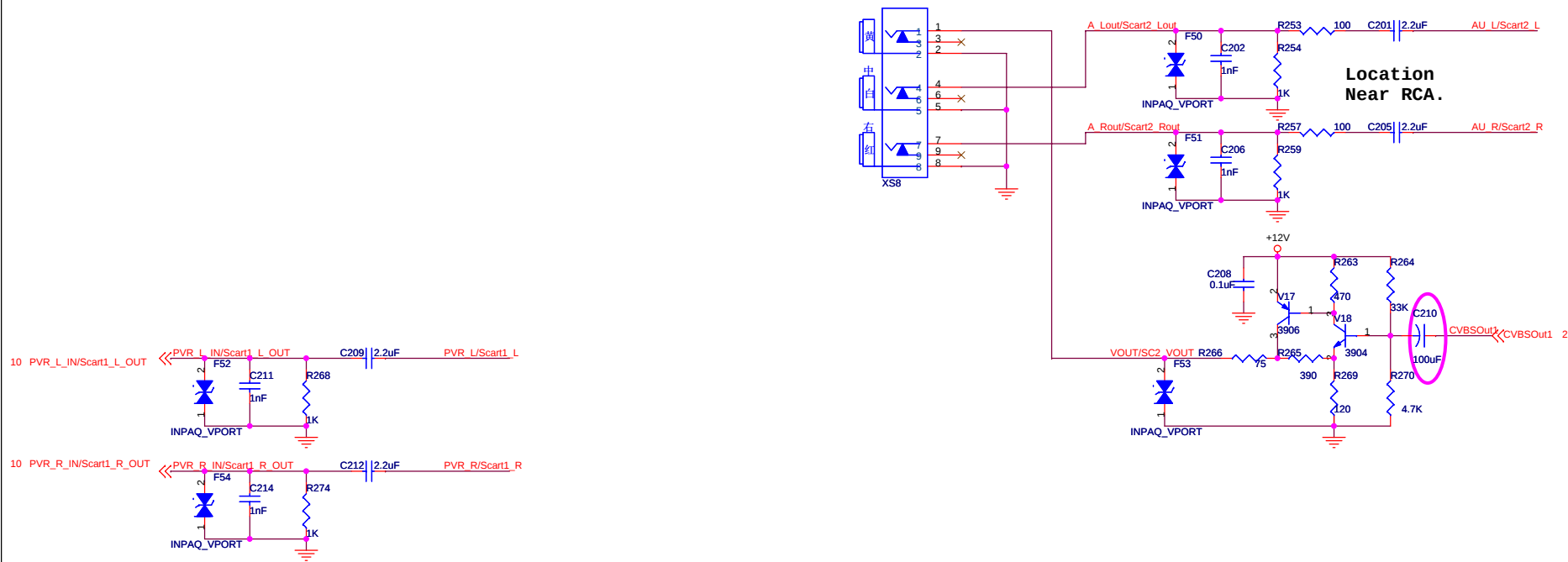
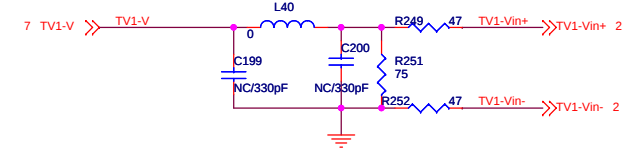


		深圳市高新区南区科技南十路国际技术创新研究院C座4楼	
		TEL:0755-26996895 FAX:0755-26996830	
Title			
VGA			
Size B	Document Number MST6X89BL		Rev 01A
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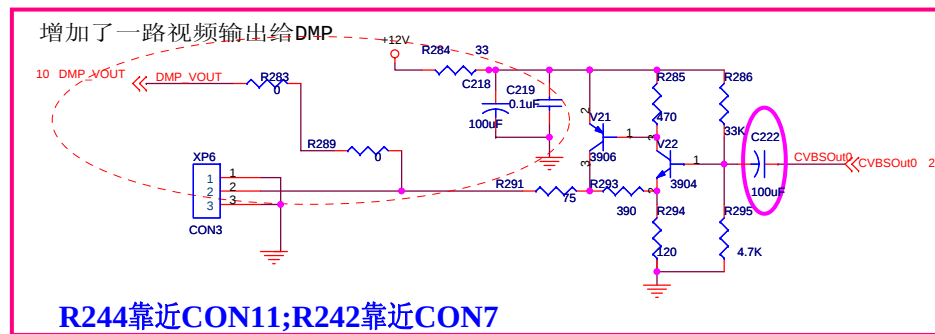
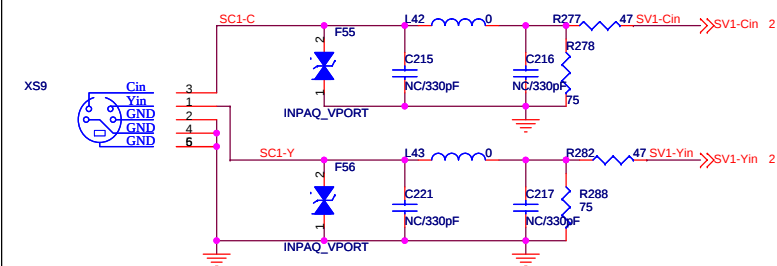




TV Input

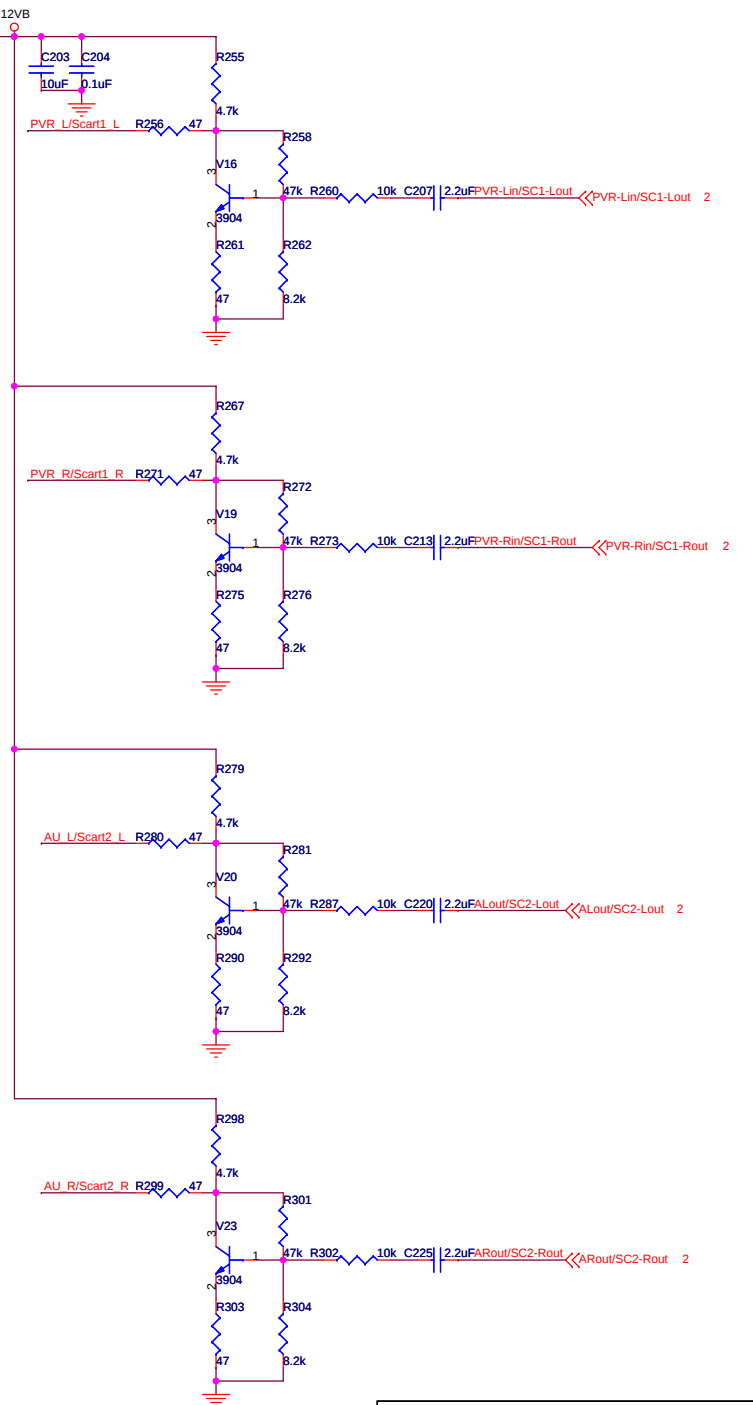
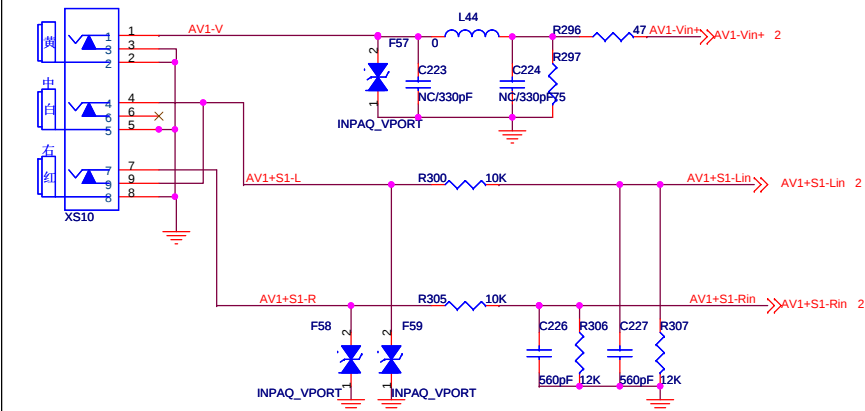


S-Video Input



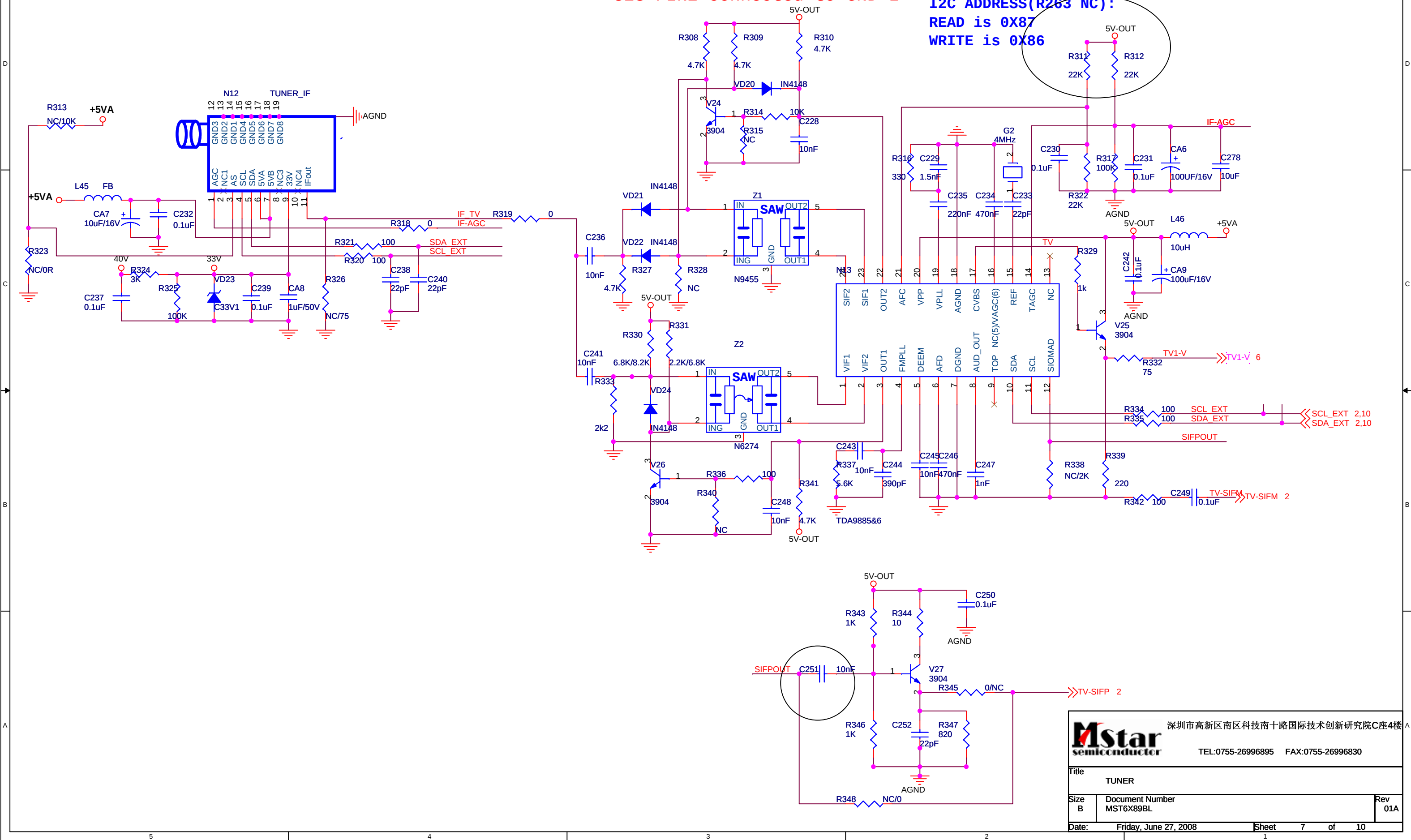
Note: The IC's CVBSOUT level is 0.5Vp-p when input signal is 1Vp-p, so it must multiply signal by four (GAIN X 4) for 2Vp-p output

AV1 Input



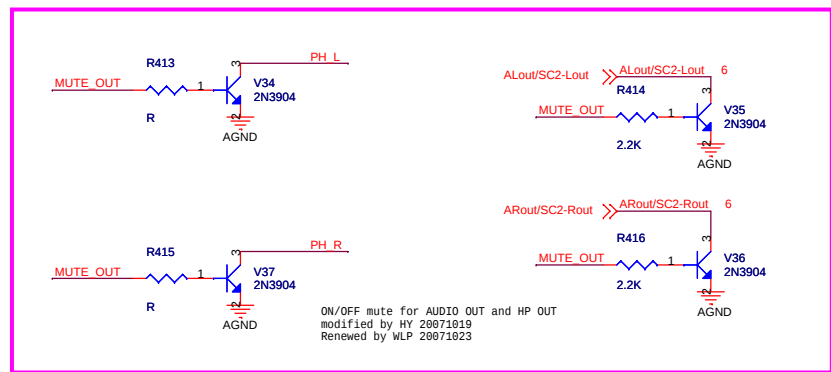
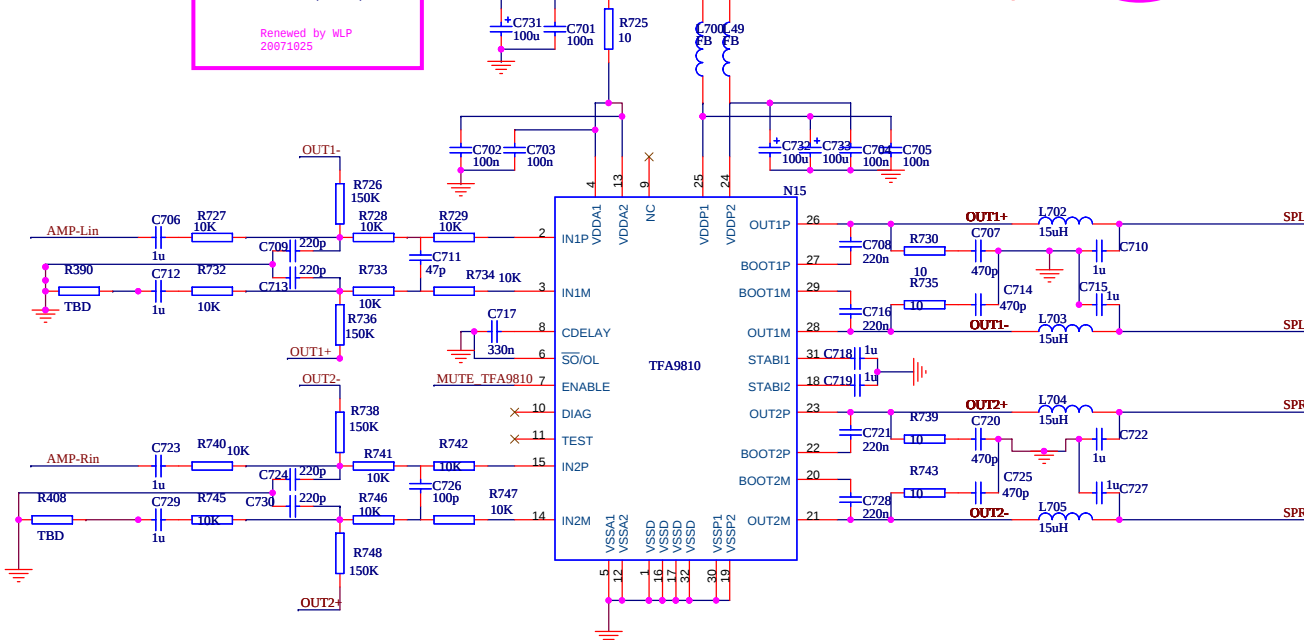
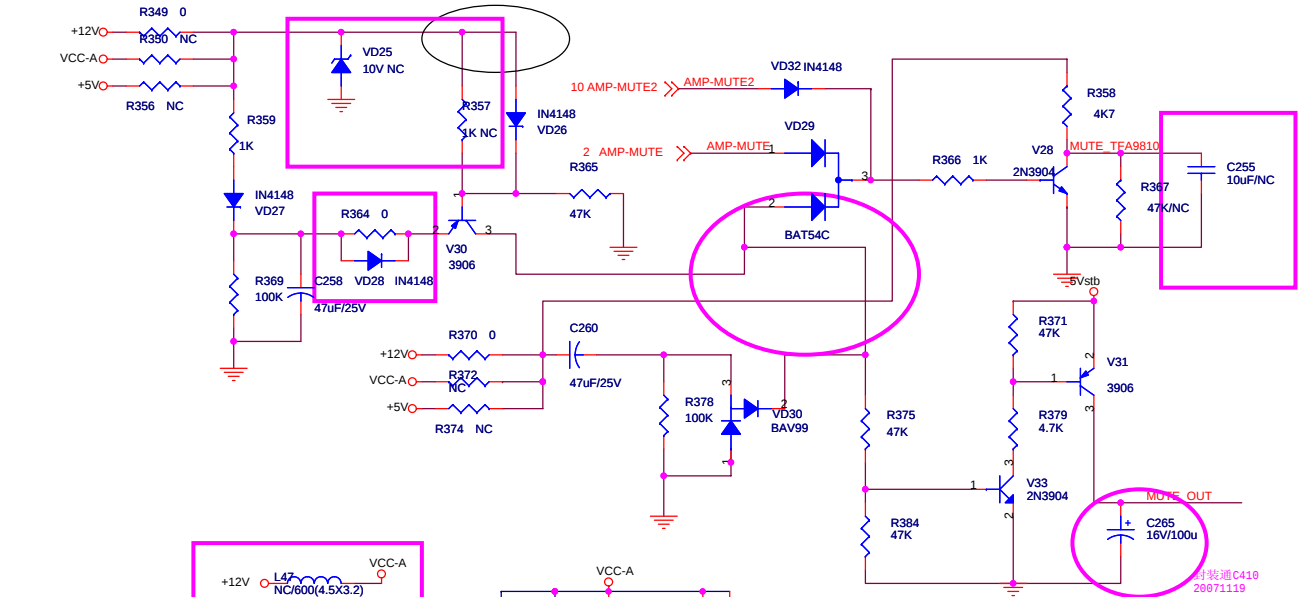
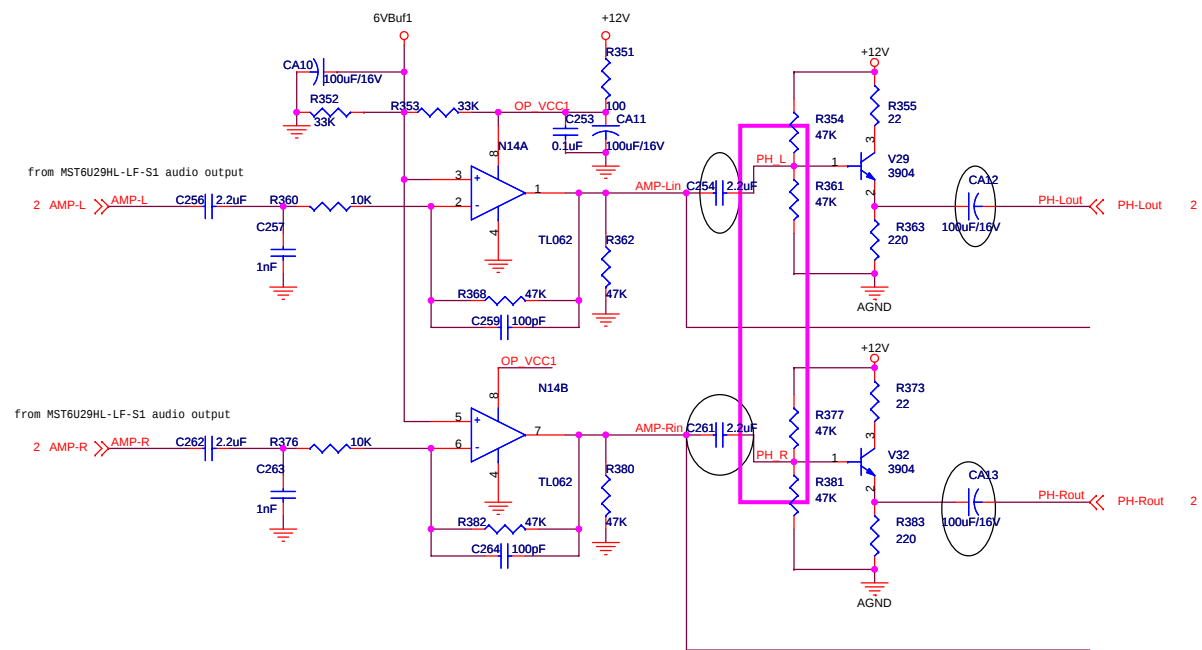
U23 PIN2 connected to PIN1 B/G,DK,I,L
U23 PIN2 connected to GND L'

I2C ADDRESS(R263 NC):
READ is 0X87
WRITE is 0X86



Mstar semiconductor
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Shield for MST6X89BL AND DDR

