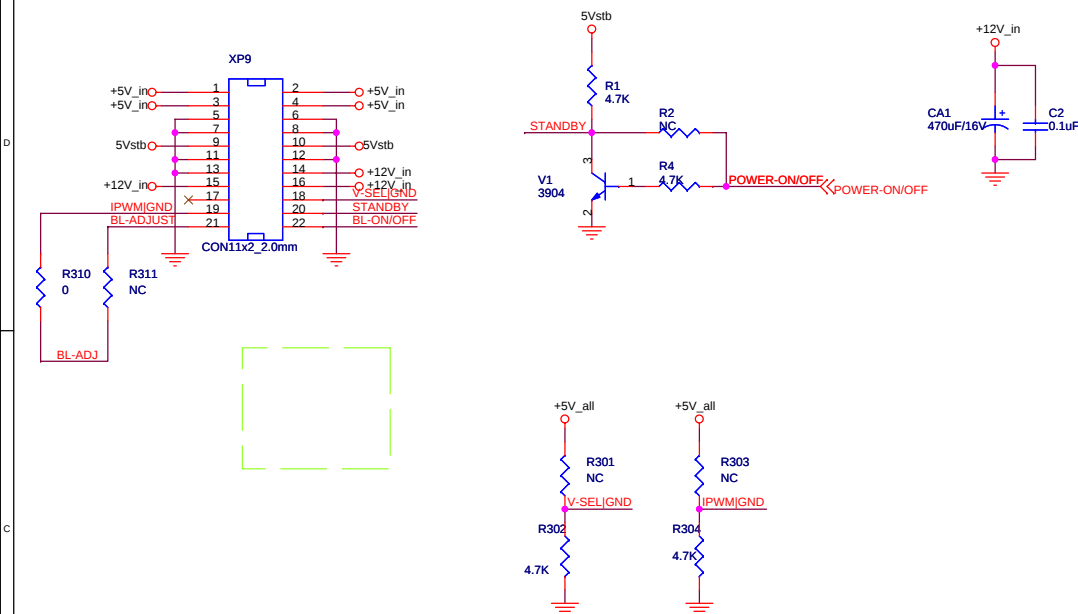
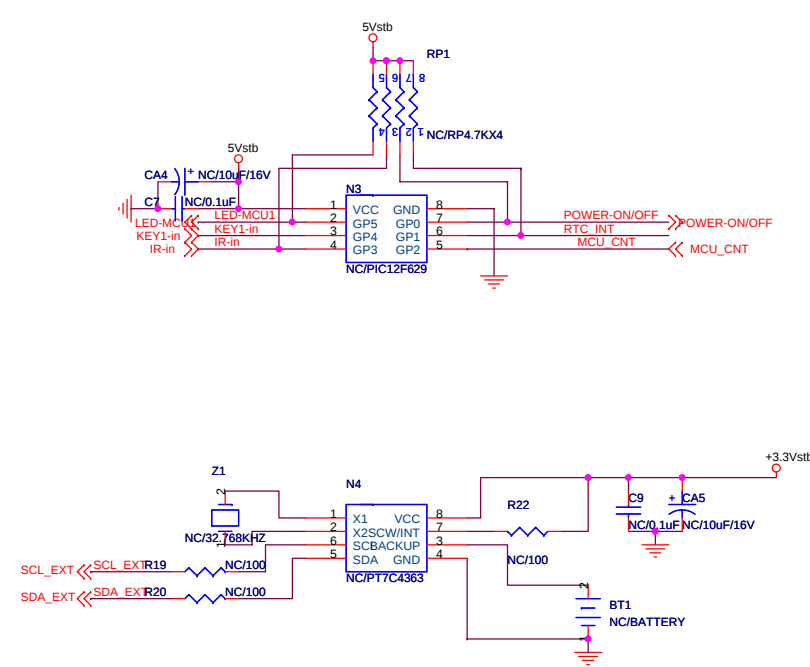
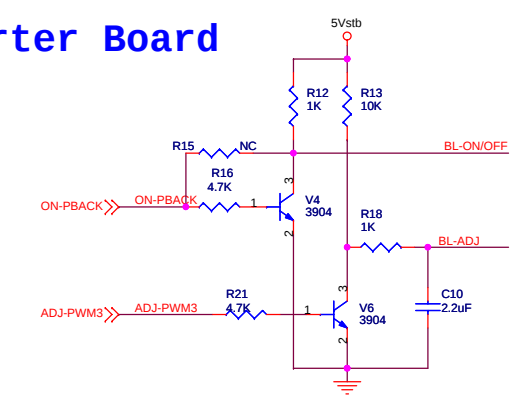


T0 Inverter Board



Vcc3.3for MST9E19A analog

Vcc1.8 for MST9E19A

+3.3AVDD for AVDD_HDMI

+3.3AVDD for AVDD_AU

+3.3AVDD for AVDDA

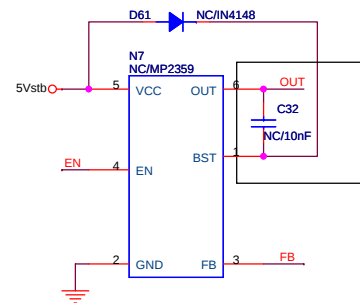
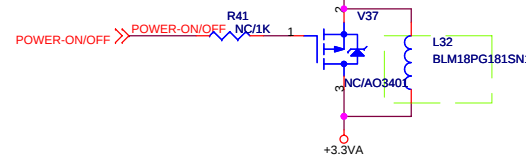
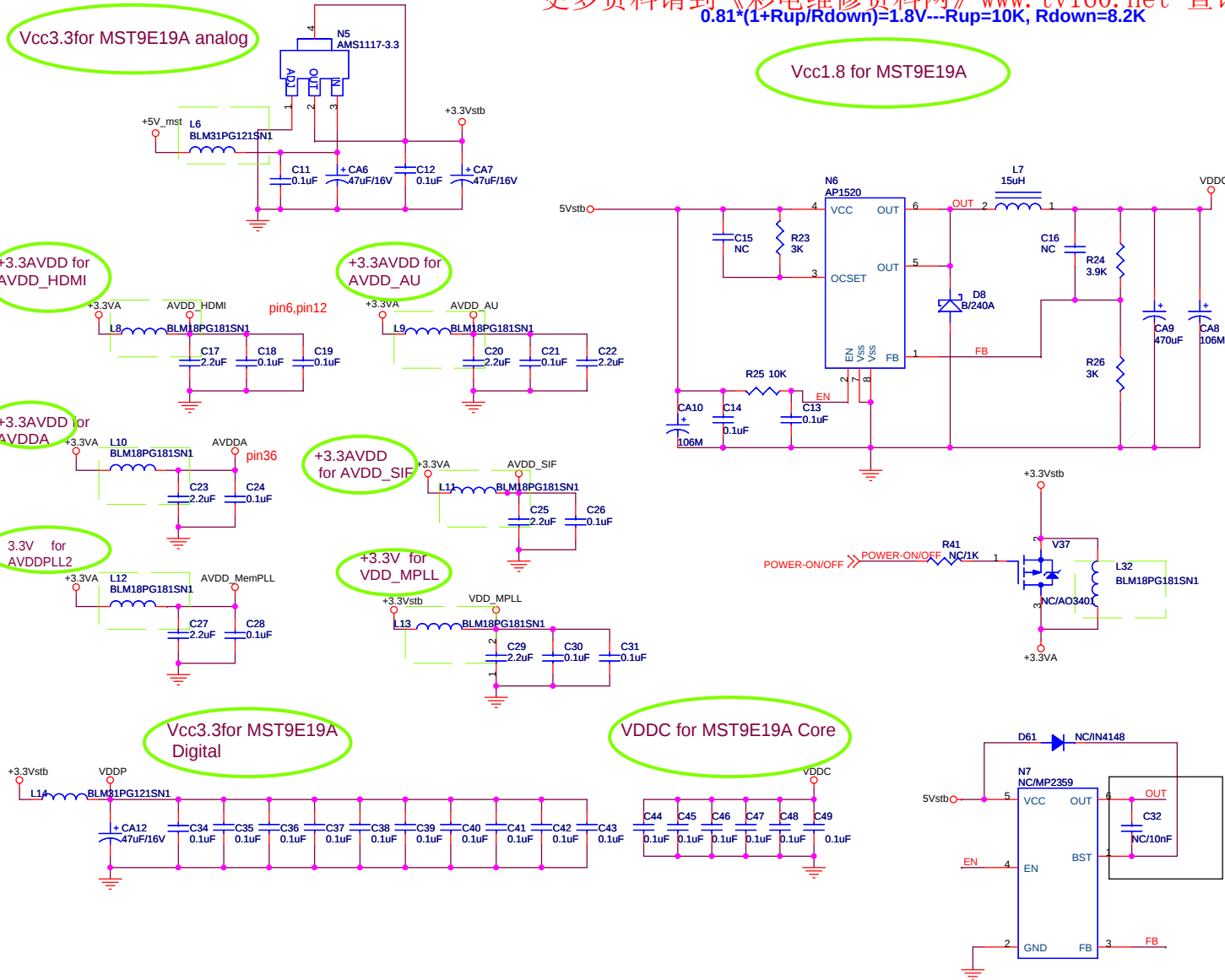
+3.3AVDD for AVDD_SIF

3.3V for AVDDPLL2

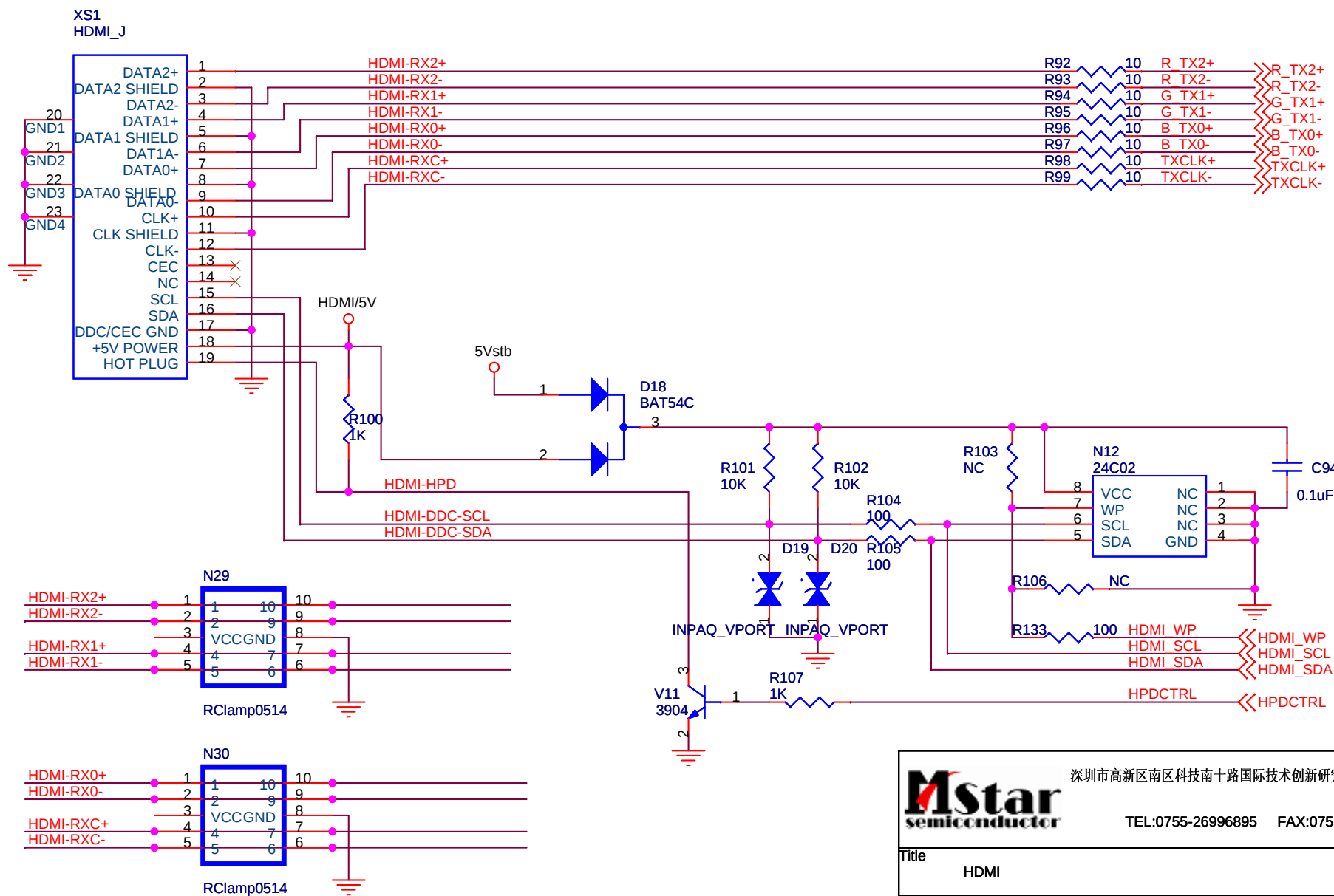
+3.3V for VDD_MPLL

Vcc3.3for MST9E19A Digital

VDDC for MST9E19A Core







Mstar semiconductor

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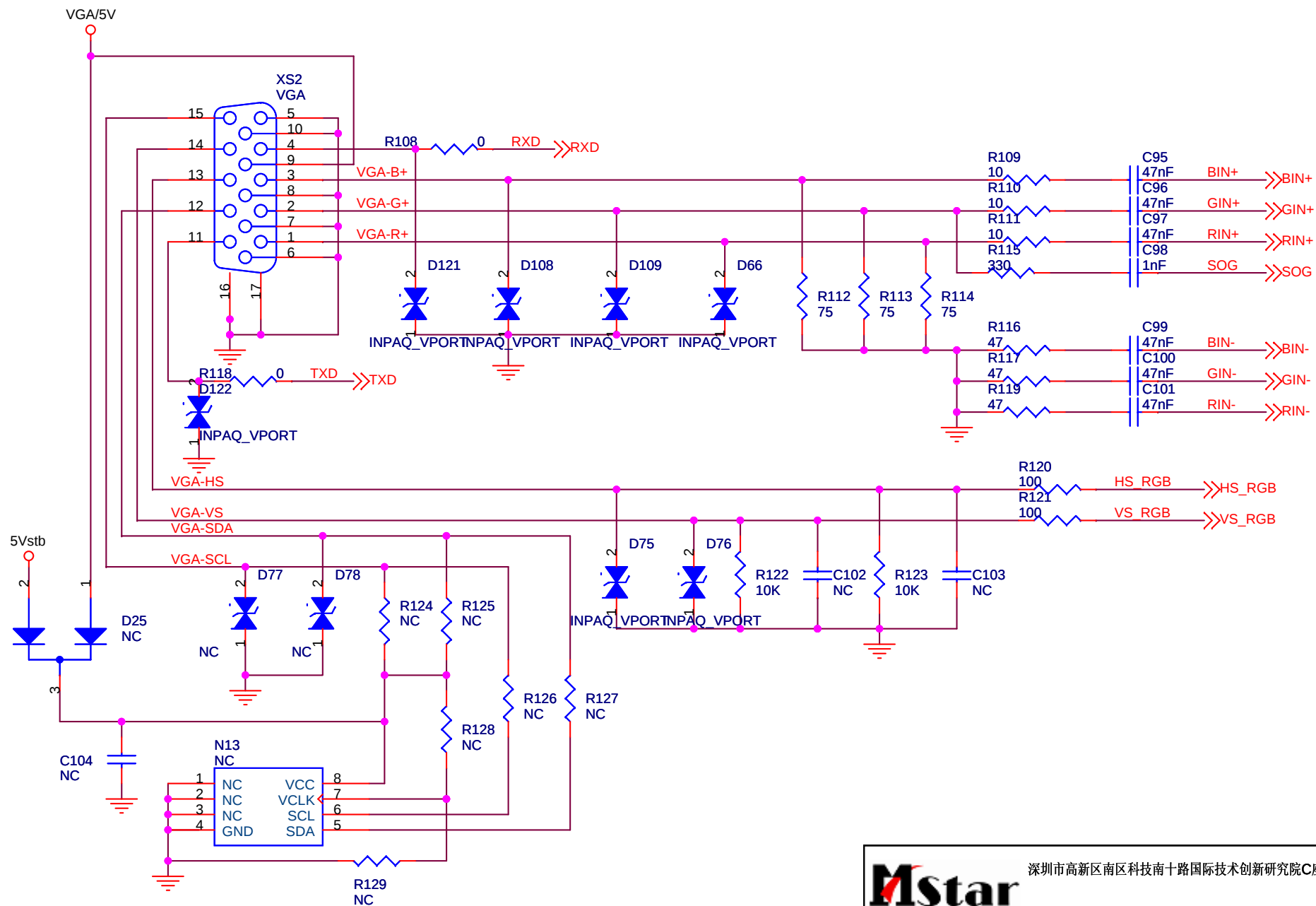
TEL:0755-26996895 FAX:0755-26996830

Title: HDMI

Size A Document Number: MST9E19A

Date: Wednesday, January 24, 2007 Sheet: 2

Rev: 1



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Title

VGA

Size

Document Number

MST9E19A

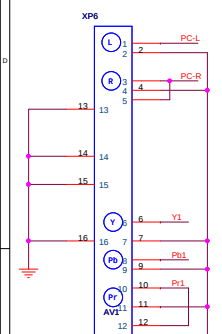
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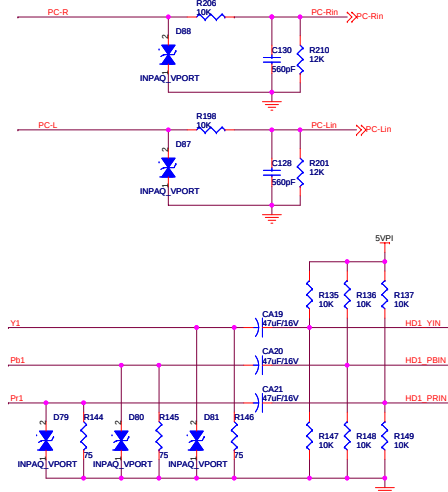
Wednesday, January 24, 2007

Sheet

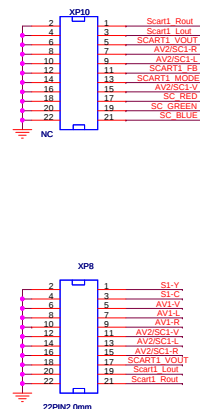
HDTV Input



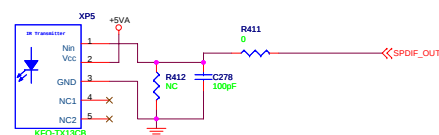
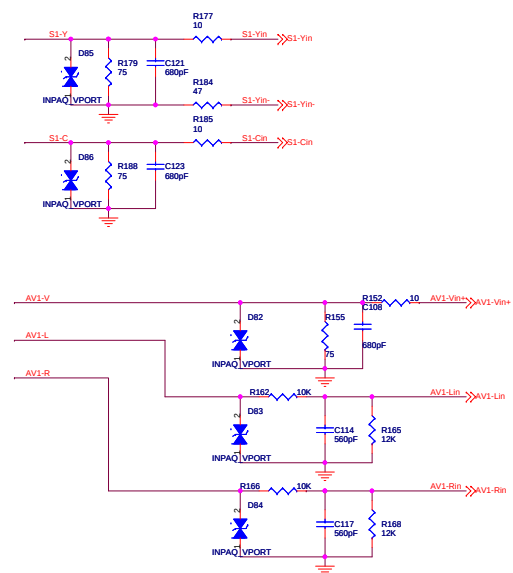
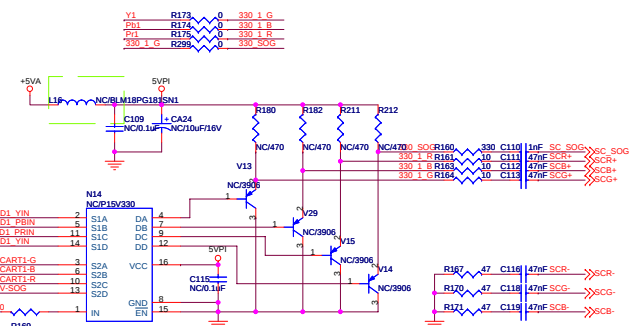
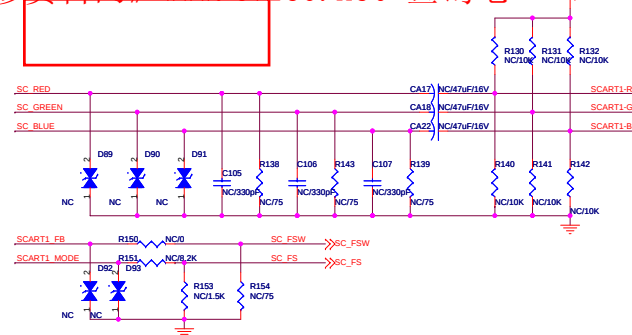
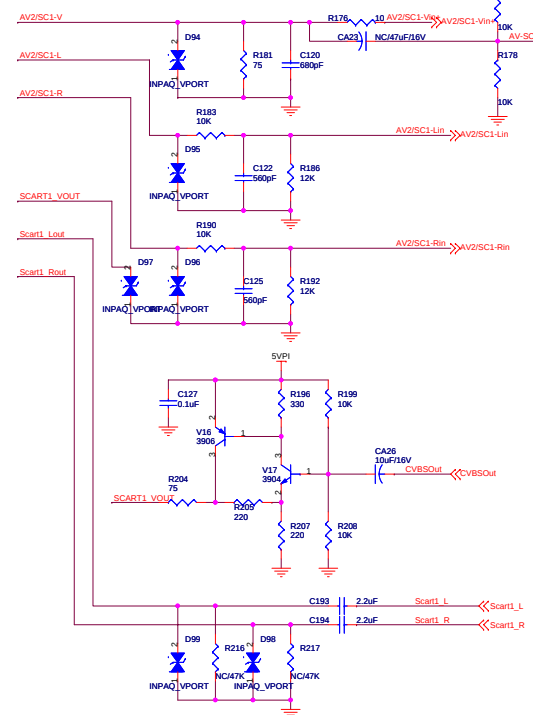
PC Audio Input



SCART1 Input



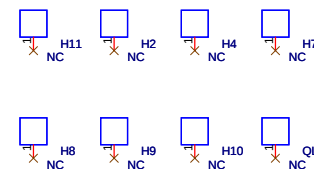
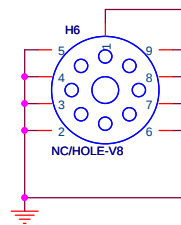
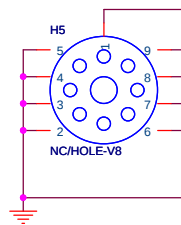
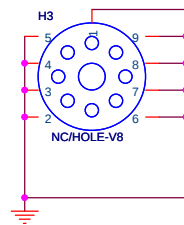
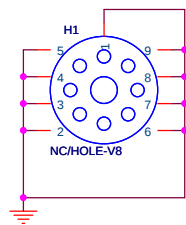
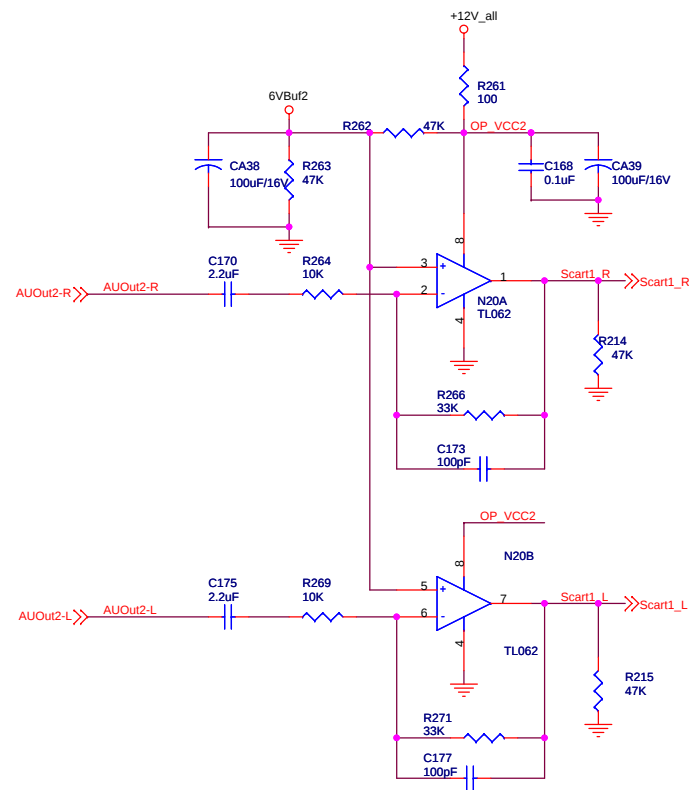
OR AV2&AVOUT



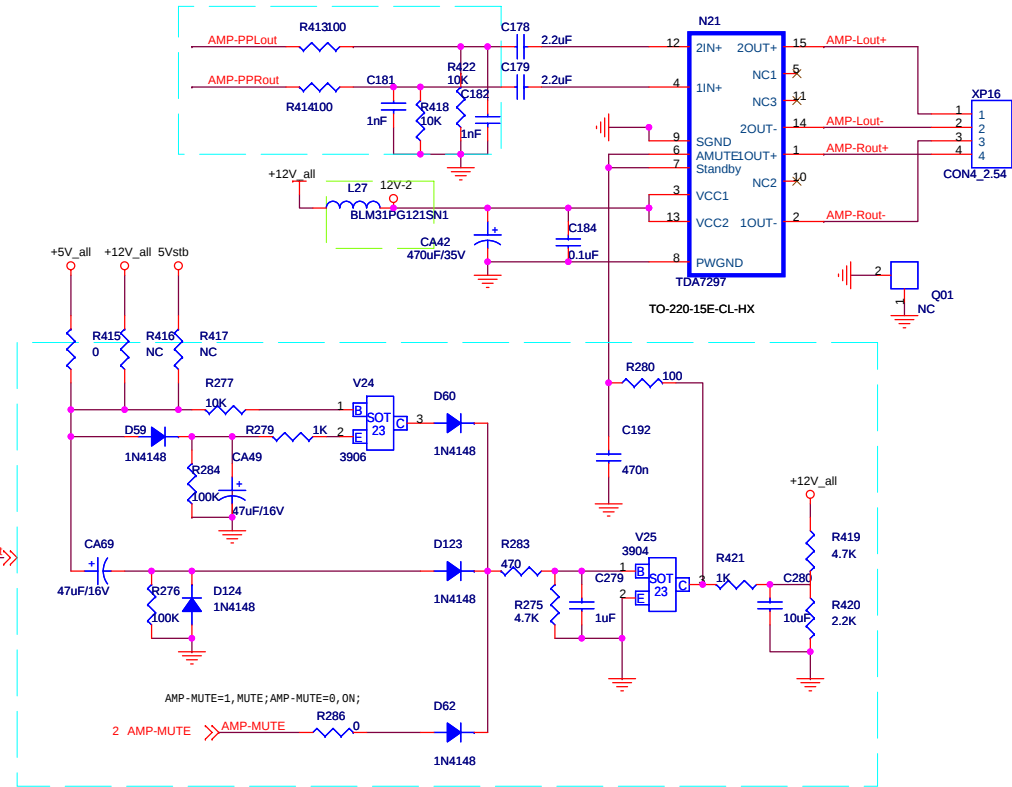
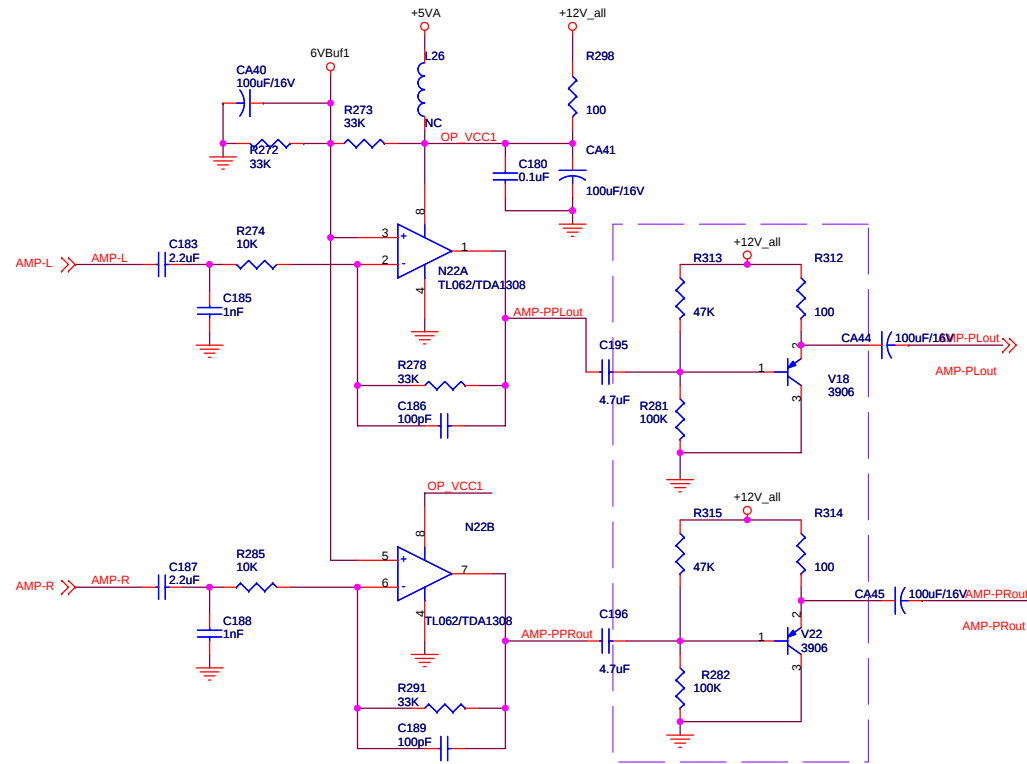
U23 PIN2 connected to GND L

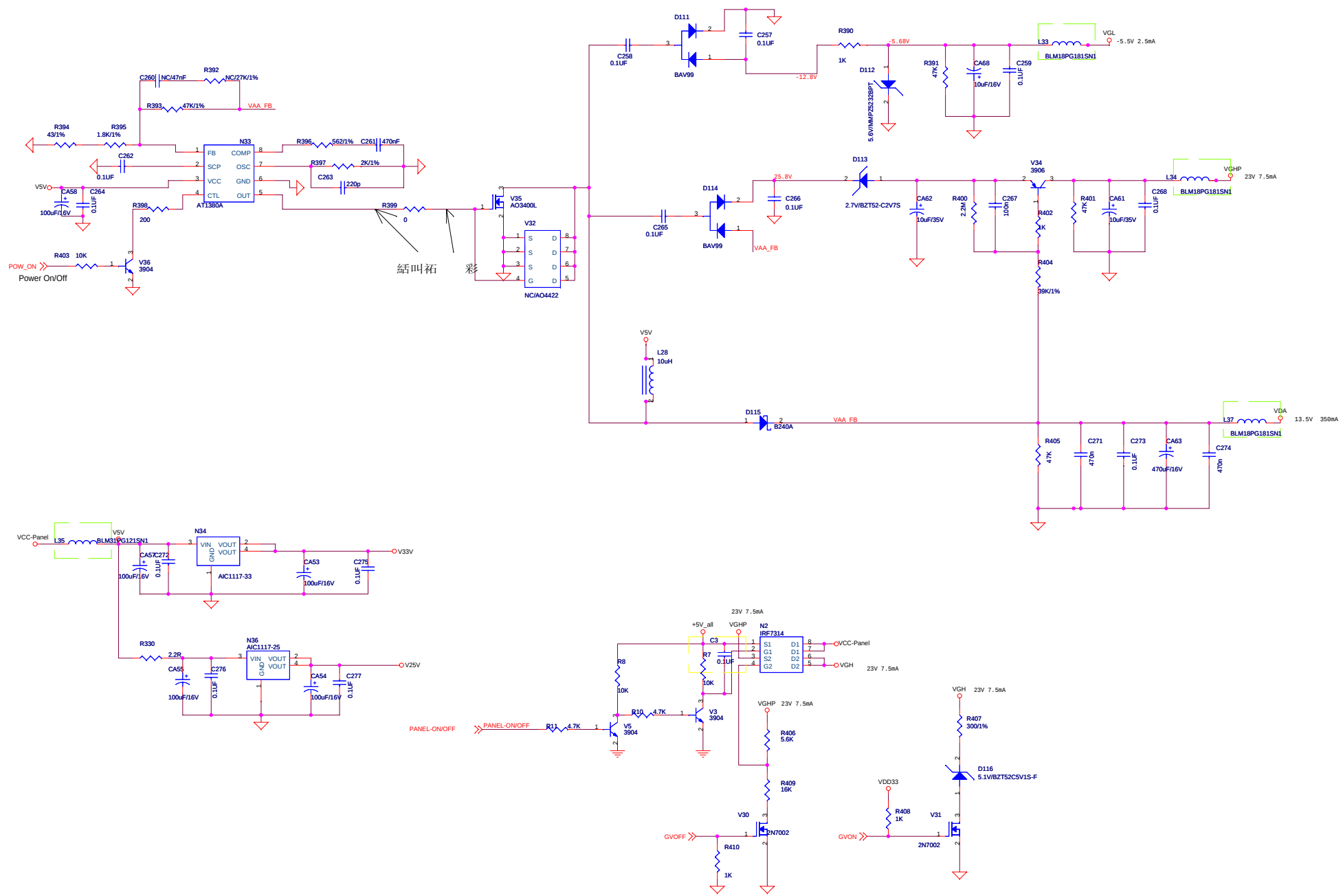


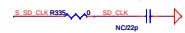
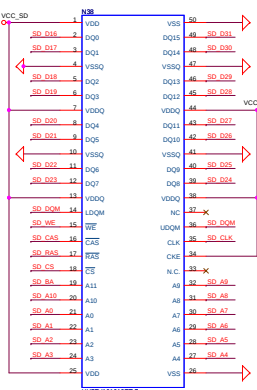
Audio OP(AUOUTL3/AUOUTR3; AUOUTL2/AUOUTR2)



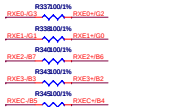
FOR TDA1308/PT2308 L26 FB,R298 NC;
FOR TL062 L26 NC,R298 0;







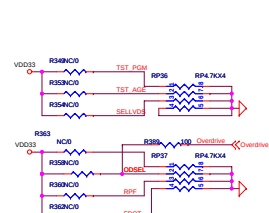
LVDS Power Filter



縹 AS



Debug port



T2C: EEPROM for LUT

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Title TDA1517			
Size Document Number			
Cust: M5T9E19A			
Date: Wednesday, January 24, 2007		Sheet 11 of 19	

