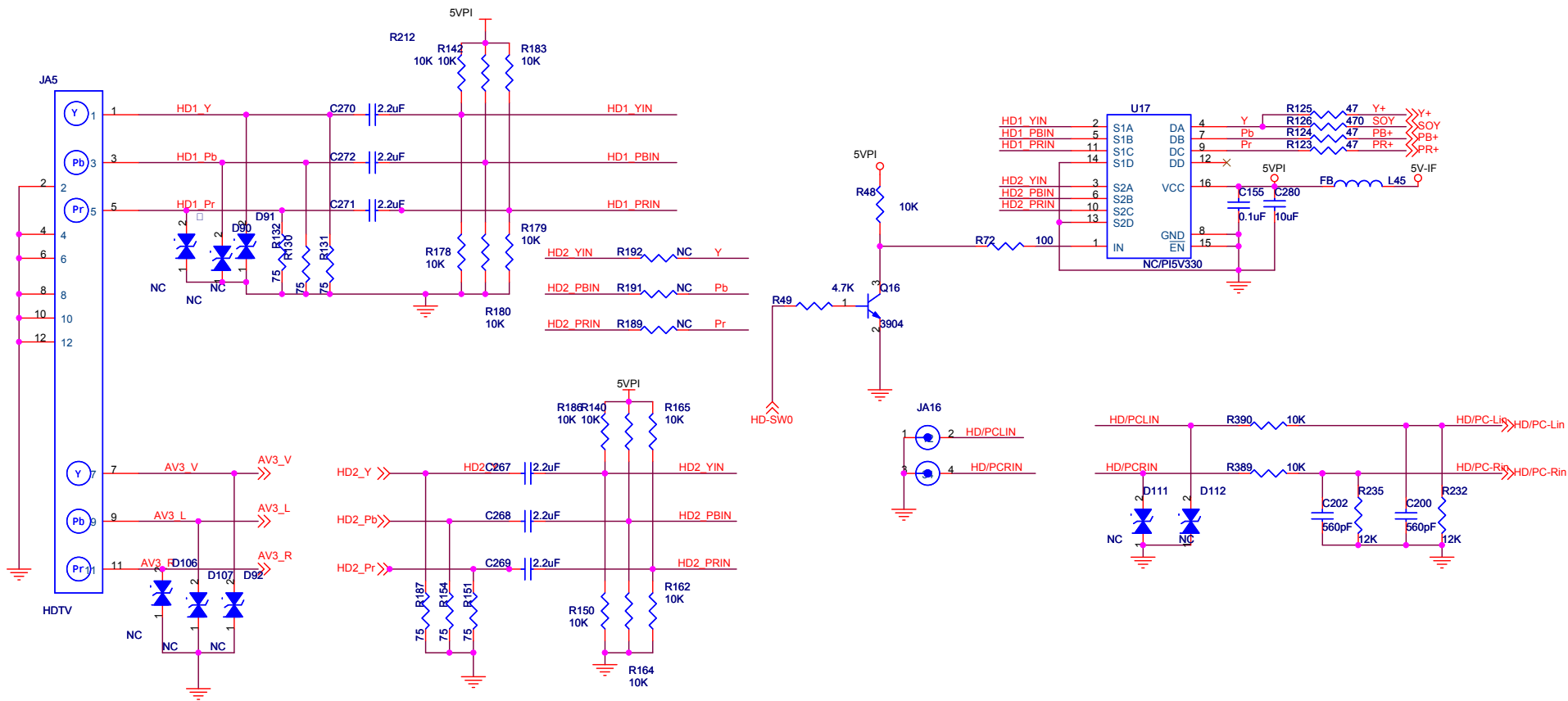
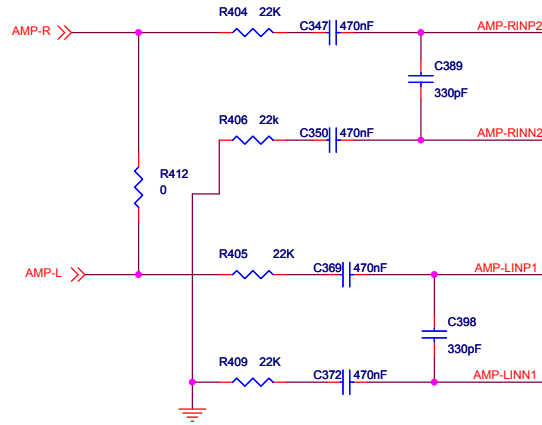
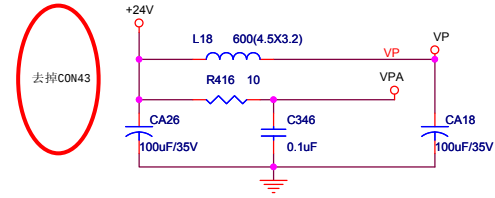




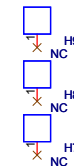
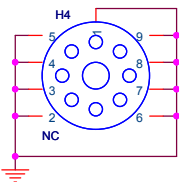
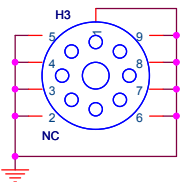
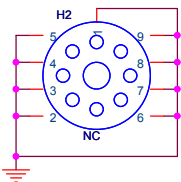
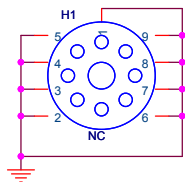
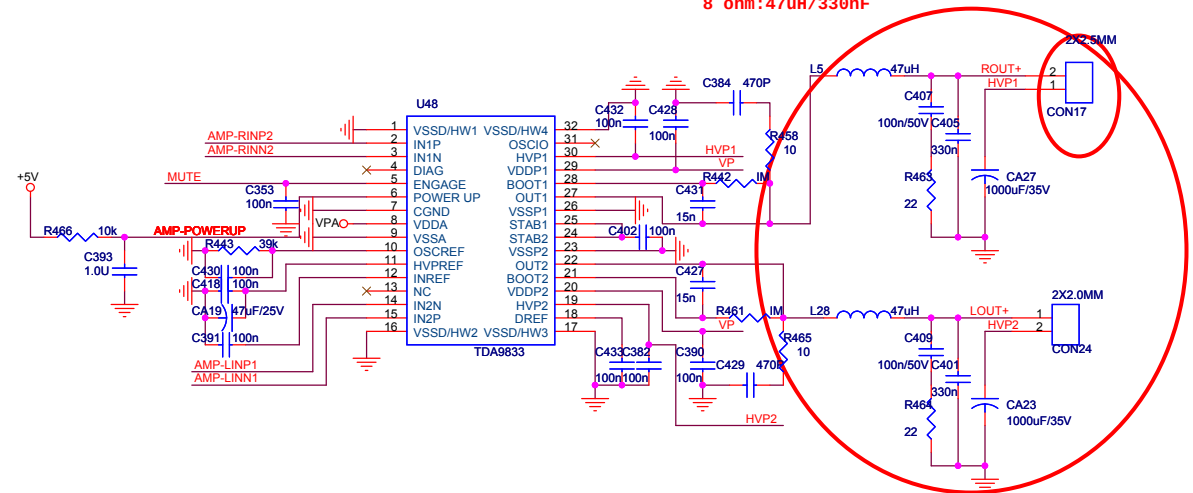
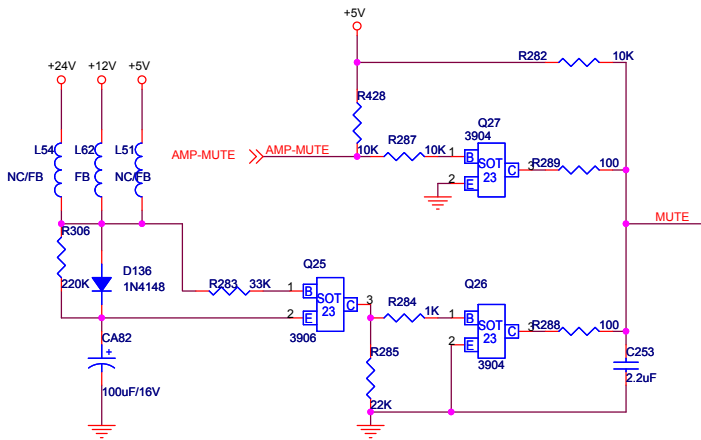
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C347&C350
4 ohm:1000nF
8 ohm:470nF

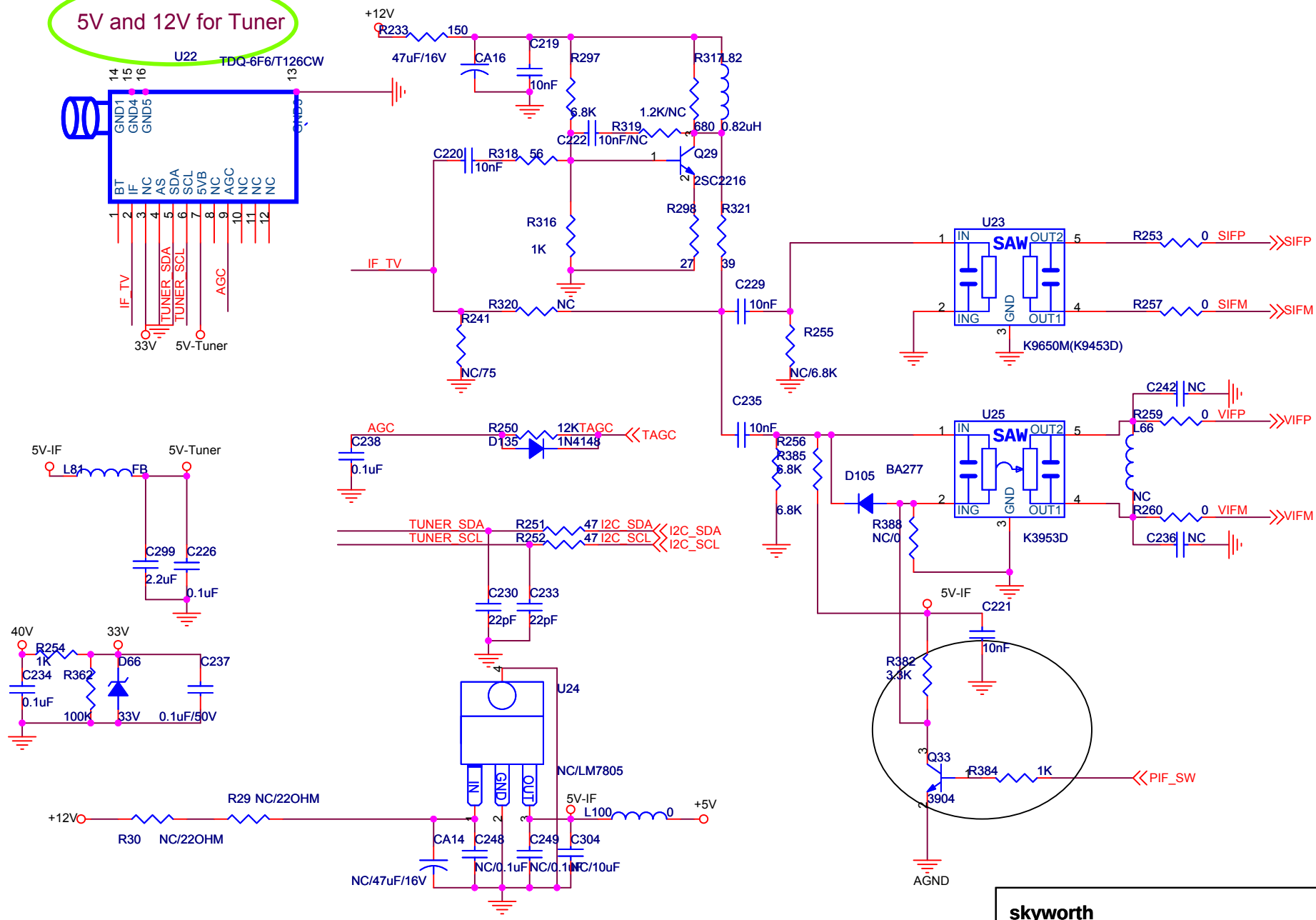


L5/C405 & L28/C401
4 ohm:22uH/680nF
8 ohm:47uH/330nF

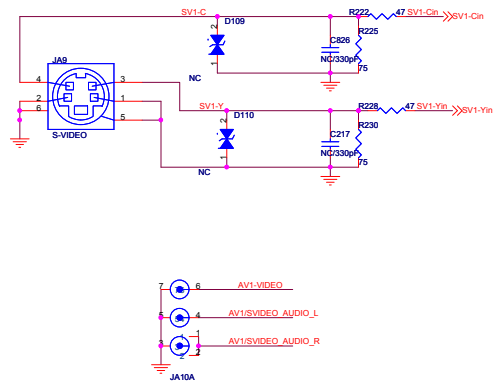


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5V and 12V for Tuner



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The schematic diagram illustrates the 74HC4052 multiplexer circuit. The IC is connected to a 5V supply and ground. The select lines X, Y, and Z are connected to the AV1/SVIDEO-Lin, AV2/Lin, and AV3/Lin inputs, respectively. The data inputs Y0-Y3 are connected to the AV1/SVIDEO-Rin, AV2/Rin, and AV3/Rin inputs. The data outputs A, B, and C are connected to the AV1/SVIDEO-Rin, AV2/Rin, and AV3/Rin inputs. The circuit also includes a 5V supply, a 10k resistor, and a 1uF capacitor.

A0-Sw1	A0-Sw0	Output
0	0	X0, Y0
0	1	X1, Y1
1	0	X2, Y2
1	1	X3, Y3

AV Input

The schematic diagram illustrates the AV input section of the TDA9984, showing three input channels: AV1/VIDEO, AV1/SVIDEO AUDIO, and AV2.

AV1/VIDEO Channel:

- Input: AV1-VIDEO (47V AV1-Vin+ and 47V AV1-Vin-)
- Components: Diode D123, Resistor R193, Capacitor C190 (NC/330pF), Resistor R216 (1K), Resistor R217 (47V AV1-Vin-).

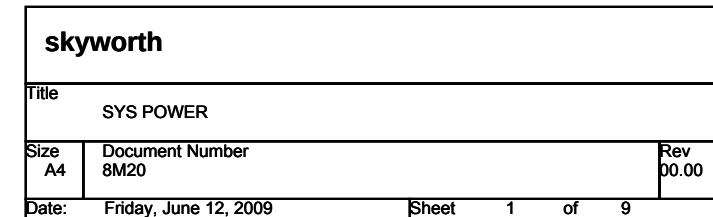
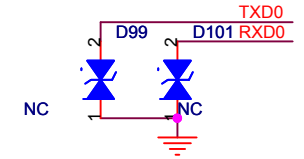
AV1/SVIDEO AUDIO Channel:

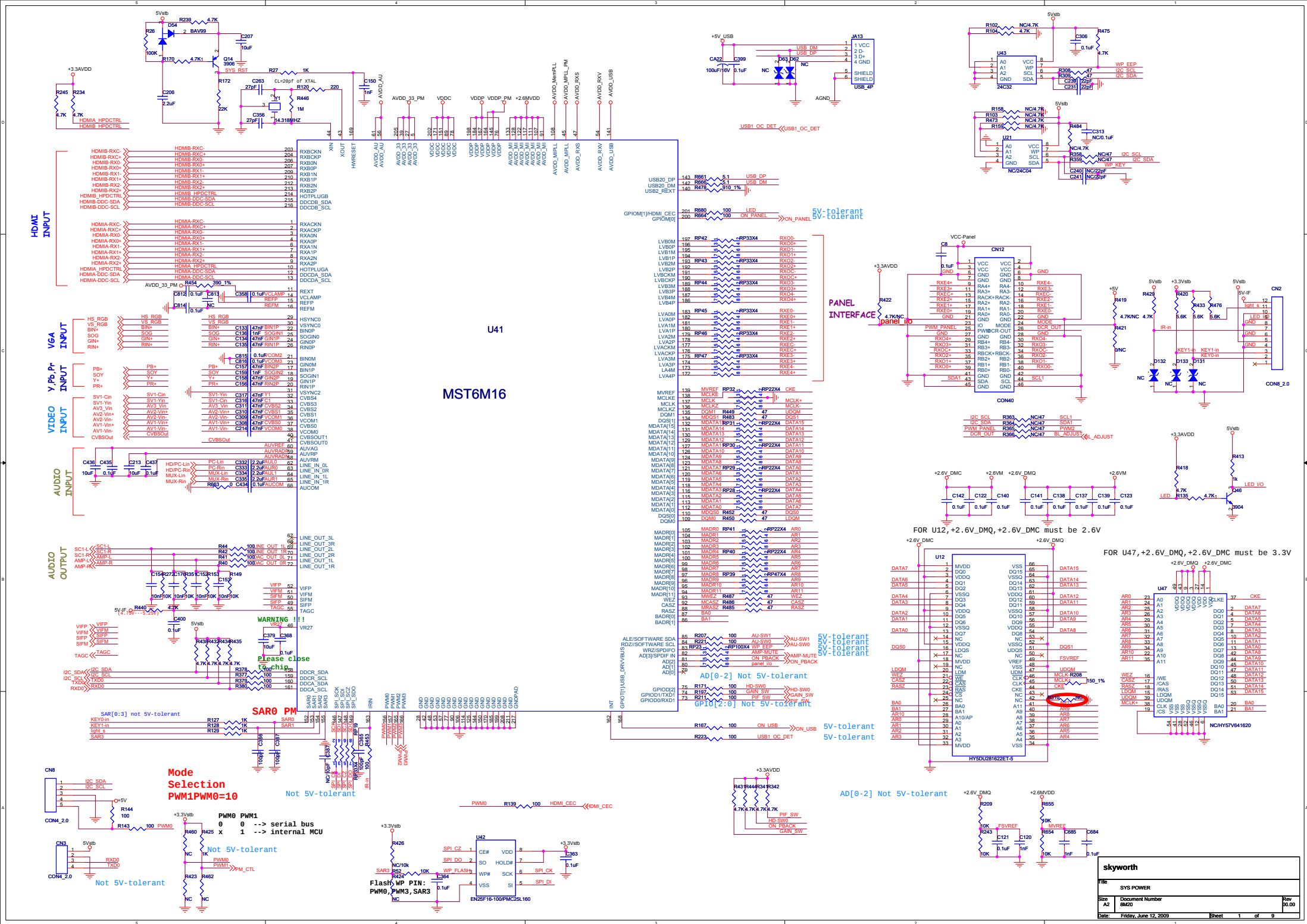
- Input: AV1/SVIDEO AUDIO (47V AV1-SVIDEO-Audio+ and 47V AV1-SVIDEO-Audio-)
- Components: Diode D116, Resistor R195, Capacitor C195 (560pF), Resistor R218 (12K), Resistor R219 (47V AV1-SVIDEO-Audio-).

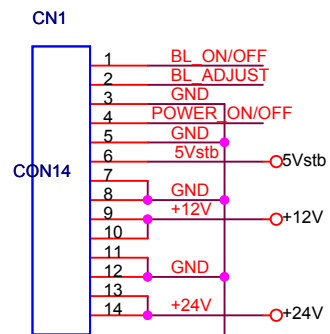
AV2 Channel:

- Input: AV2 (47V AV2-Vin+ and 47V AV2-Vin-)
- Components: Diode D117, Resistor R196, Capacitor C196 (560pF), Resistor R220 (12K), Resistor R221 (47V AV2-Vin-).

[illegible]

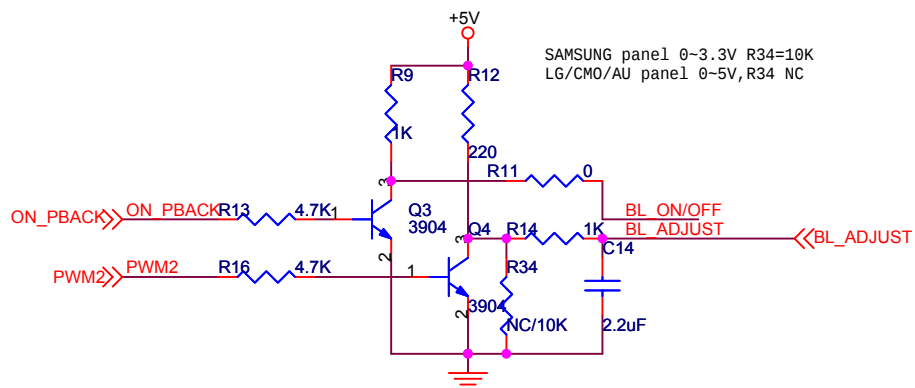






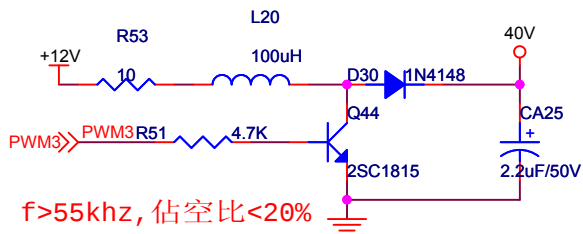
POWER ON:800MA
stb:100MA

Normal:L
Standby:H



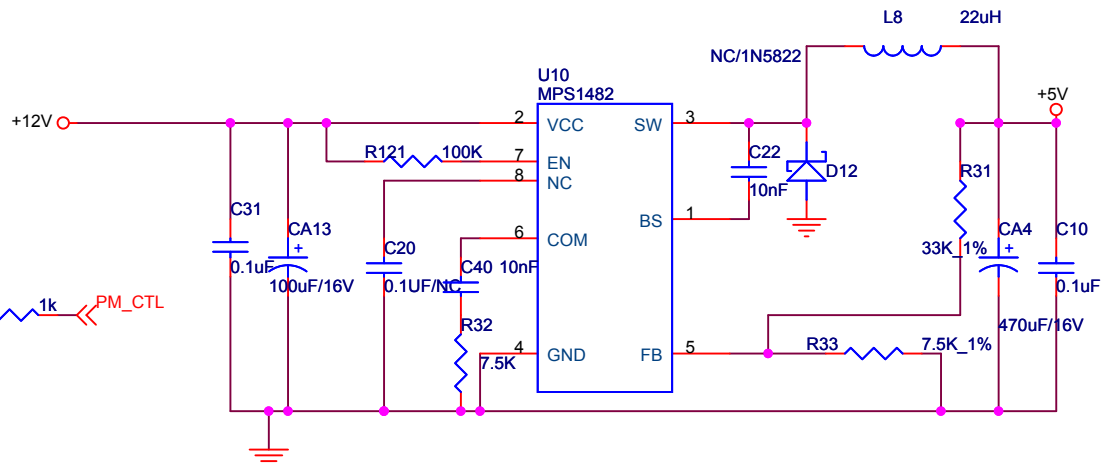
SAMSUNG panel 0~3.3V R34=10K
LG/CMO/AU panel 0~5V, R34 NC

WARNING !!!

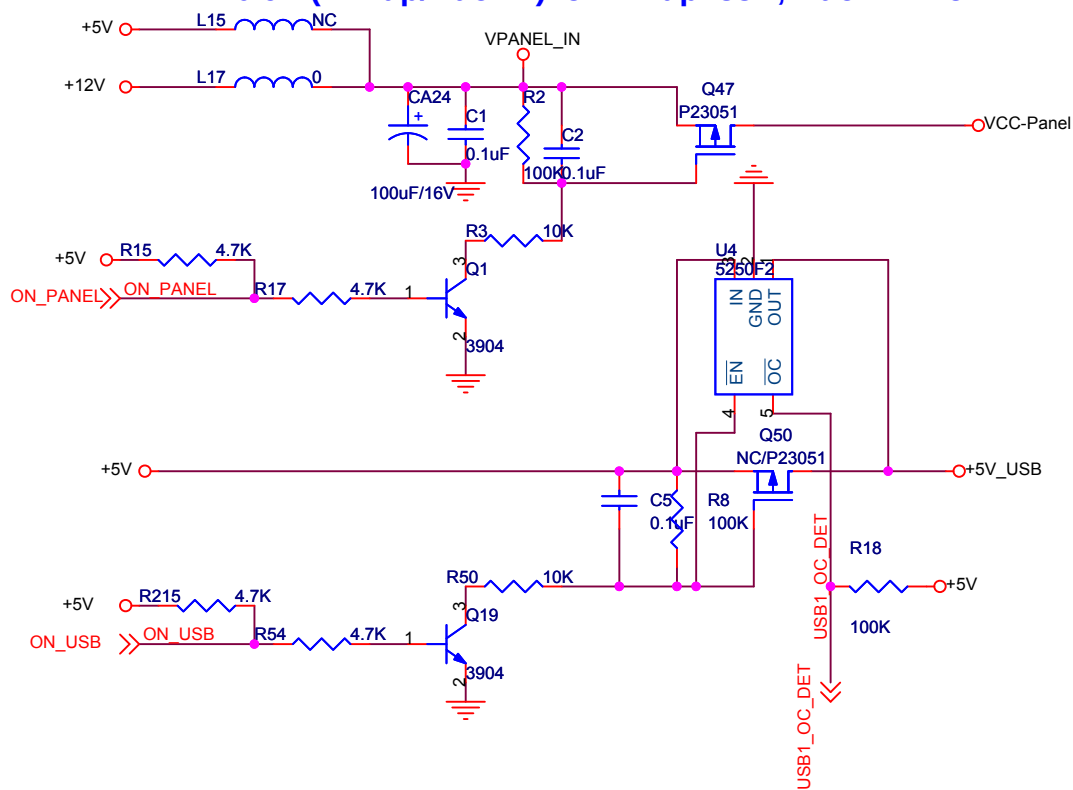


f>55khz, 佔空比<20%

WARNING !!!

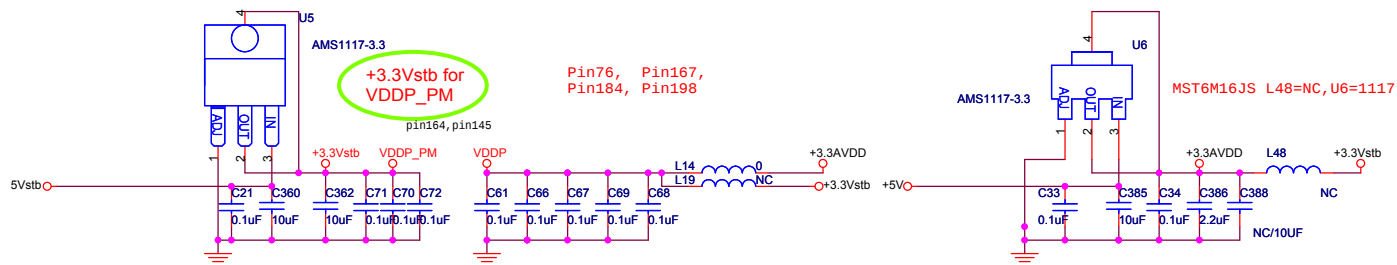


$0.92 \cdot (1 + R_{up}/R_{down}) = 5V$ --- $R_{up} = 33K$, $R_{down} = 7.5K$

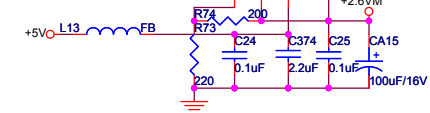


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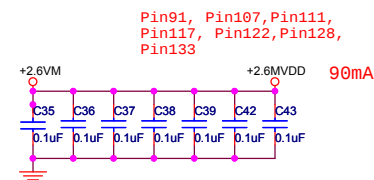
2.6V for MST6M16 and DDRAM



FOR use DDR, +2.6VM must be 2.6V, use u8, L13.

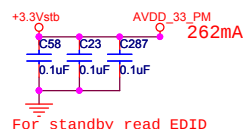
FOR use SDRAM, +2.6VM must be 3.3V, use L16

+2.6V for MST6M16

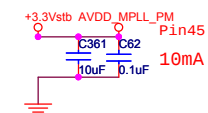


+3.3Vstb for AVDD_33

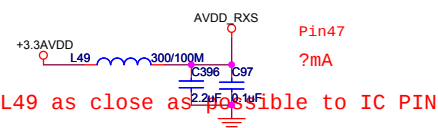
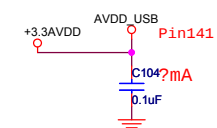
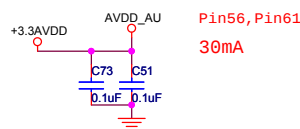
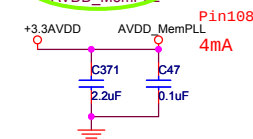
Pin5, Pin27, Pin39, Pin205



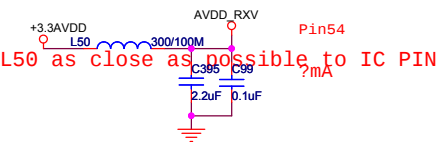
+3.3Vstb for VDD_MPLL



+3.3AVDD for AVDD_MemPLL

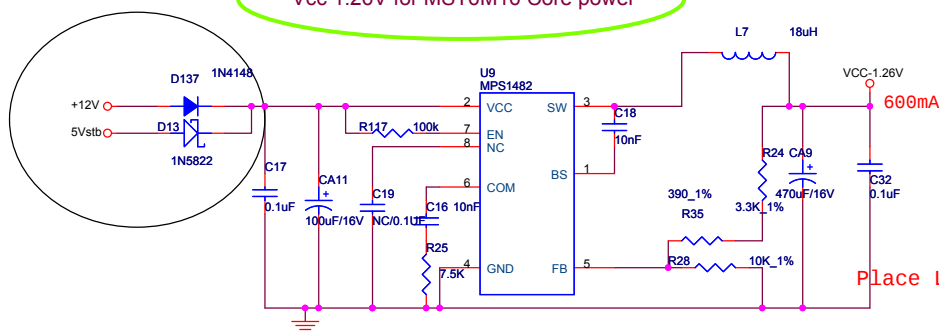


Place L49 as close as possible to IC PIN

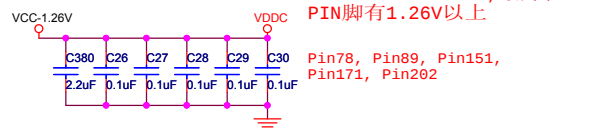


Place L50 as close as possible to IC PIN

Vcc 1.26V for MST6M16 Core power



$$0.92 \cdot (1 + R_{up}/R_{down}) = 1.26V \text{ --- } R_{up} = 3.7K, R_{down} = 10K$$



1.32V > VDDC > 1.26V, 最好在IC PIN脚有1.26V以上

注意: L7电流要大, 直流电阻要小, 保证VDDC到MST6M19GL的电压在1.26V

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