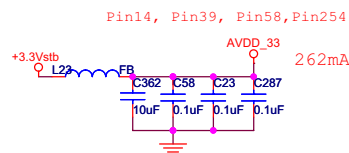
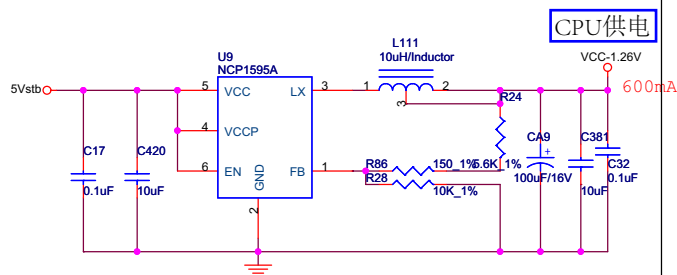


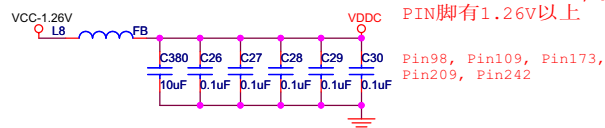
+3.3Vstb for AVDD_33



Vcc 1.26V for MST6X99GL Core power

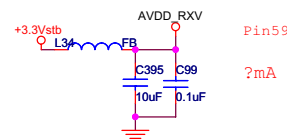
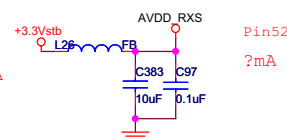
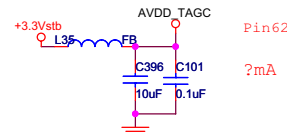
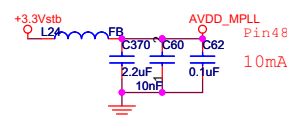


$$0.81 \cdot (1 + R_{up}/R_{down}) = 1.26V \text{---} R_{up} = 1.5K, R_{down} = 2.7K$$

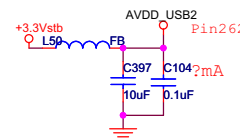
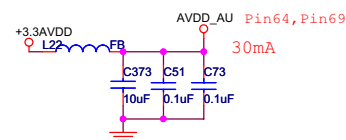
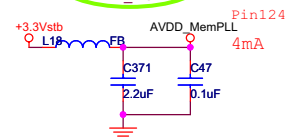


注意：L8电流要大，电阻要小，保证VDDC到MST6X99GL的电压在1.26V左右

+3.3Vstb for VDD_MPLL

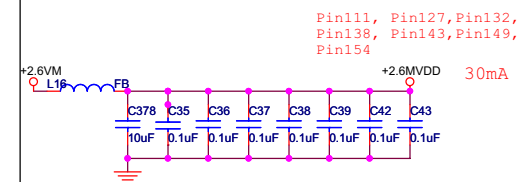


+3.3AVDD for AVDD_MemPLL

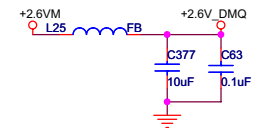
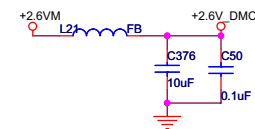


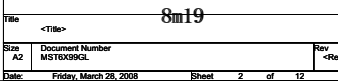
Update by Leo.Lin for Mst power management
--2007-12-10

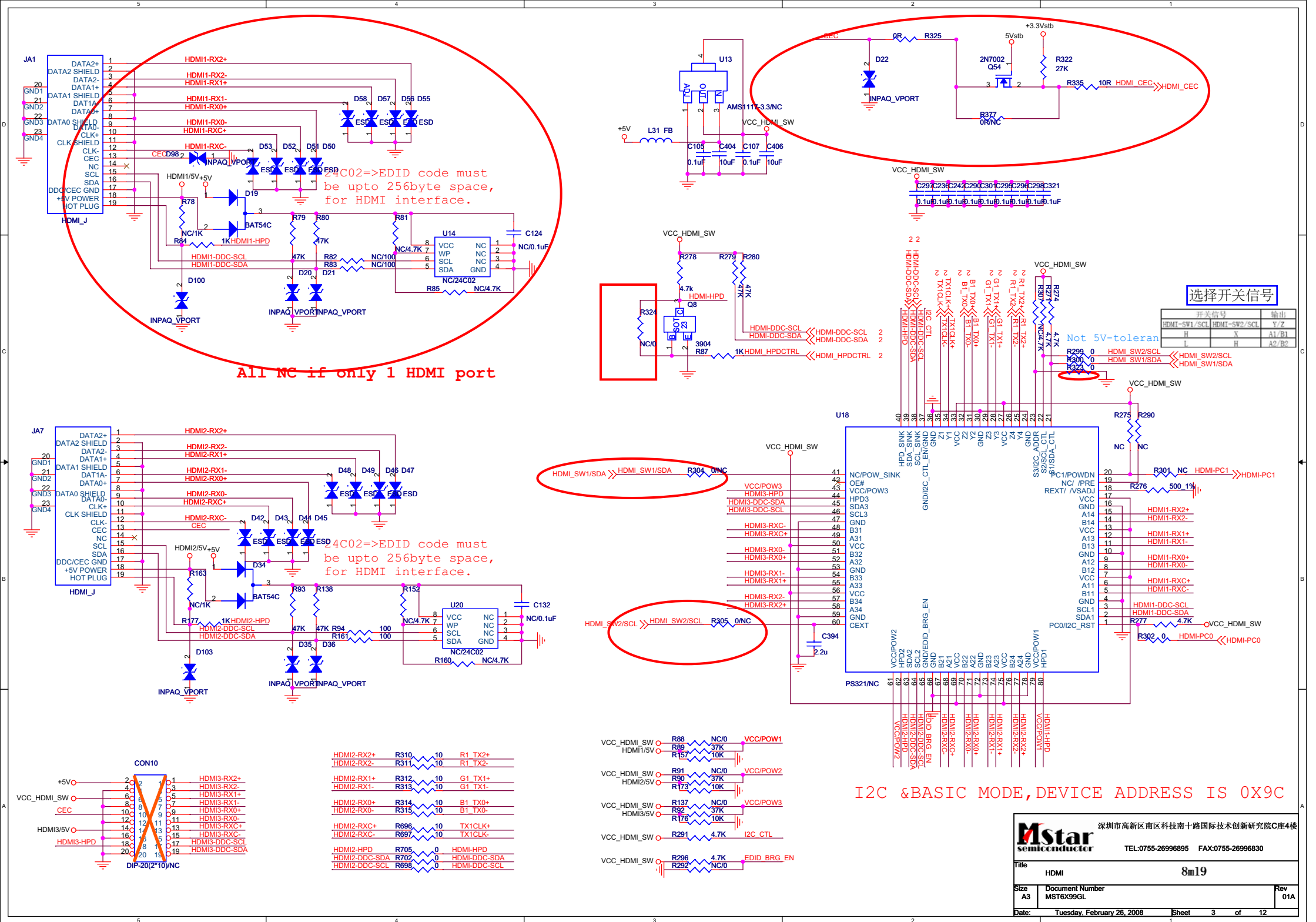
+2.6V for MST6X99GL

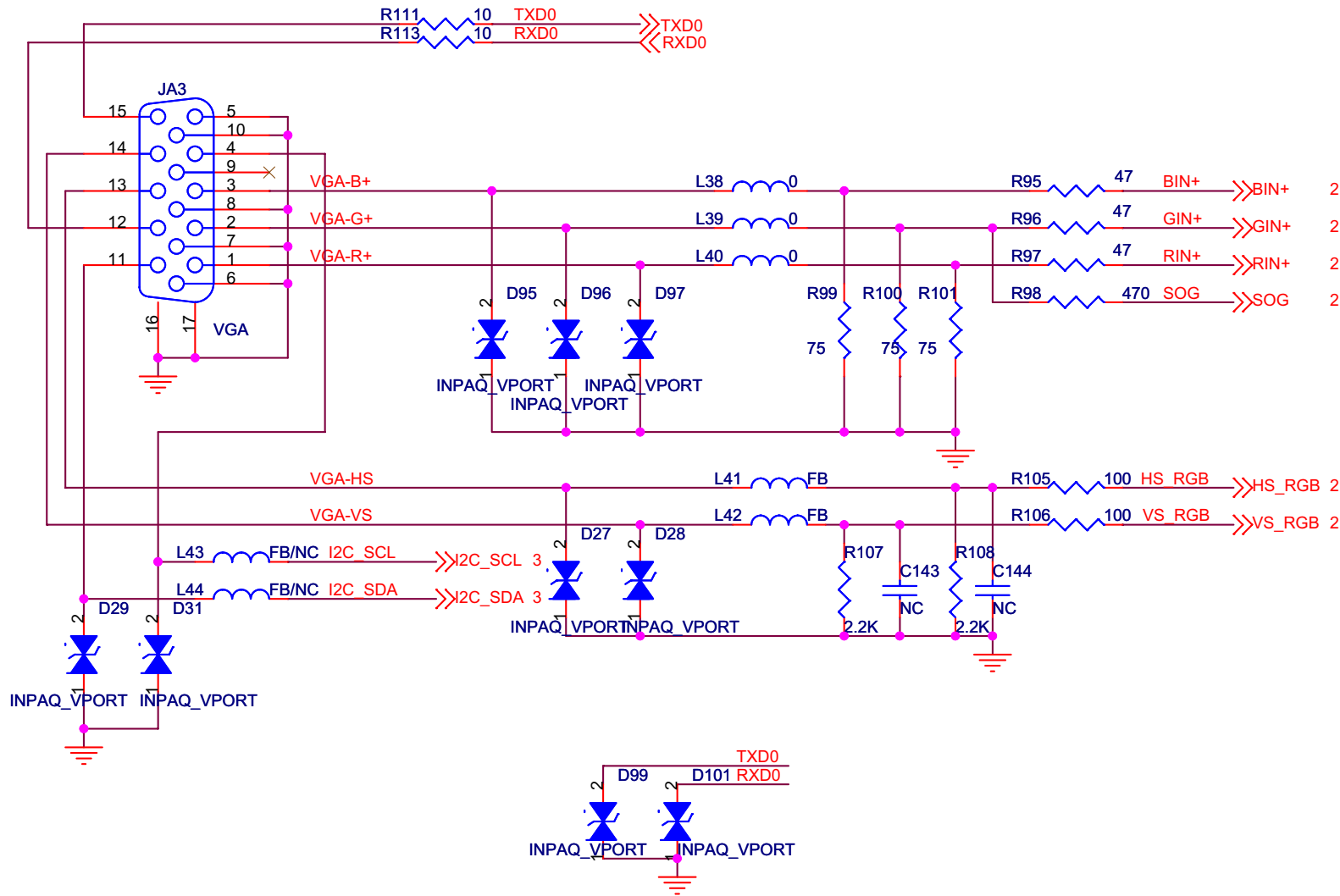


2.6V for DDRAM



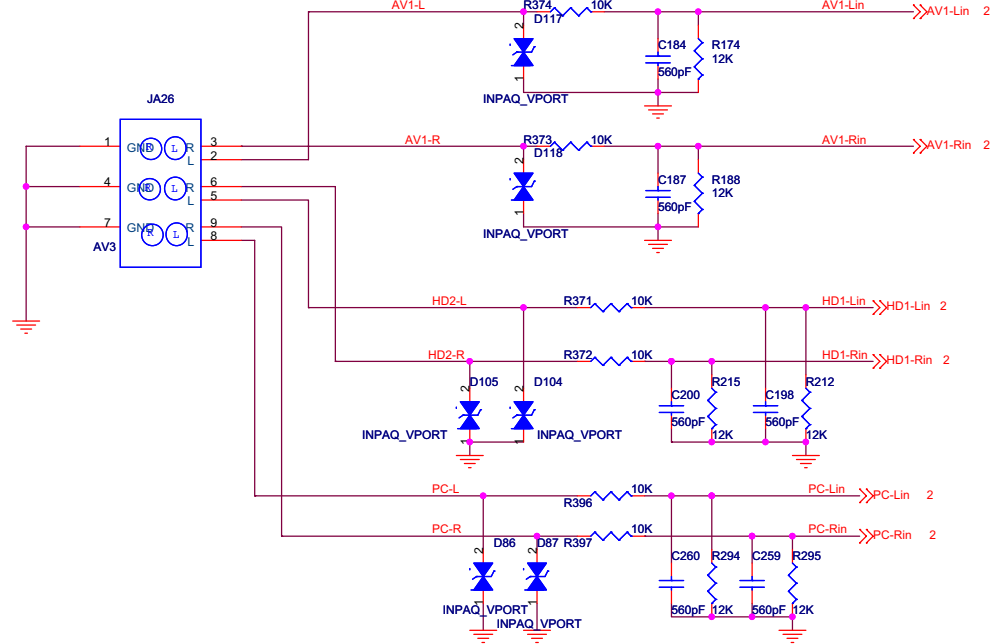
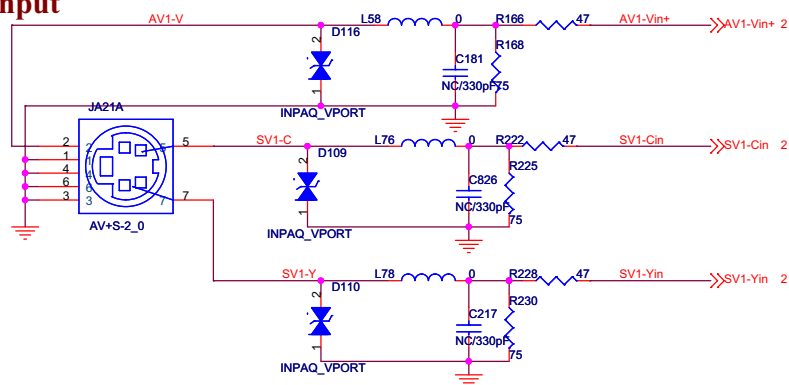




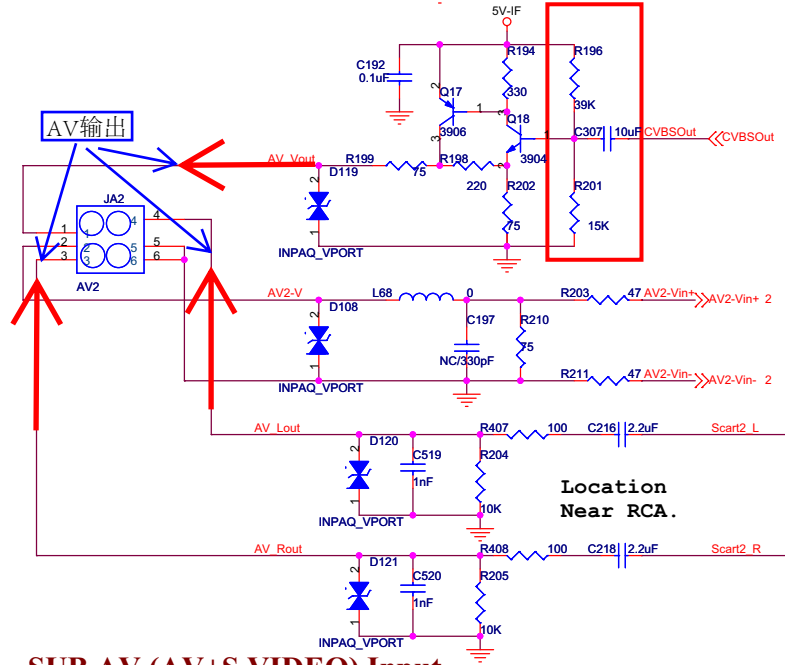


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Title VGA			
Size A	Document Number MST6X99GL	8m	Rev 01A
Date:	Saturday, February 23, 2008	Sheet	4 of 12

AV1 Input

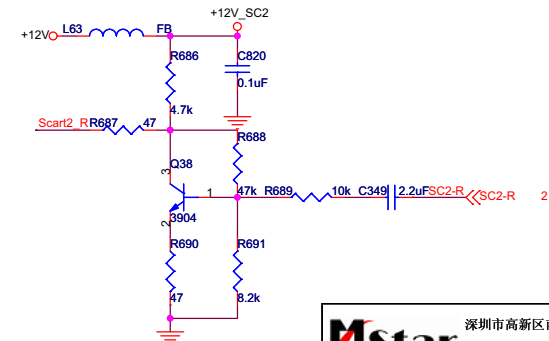
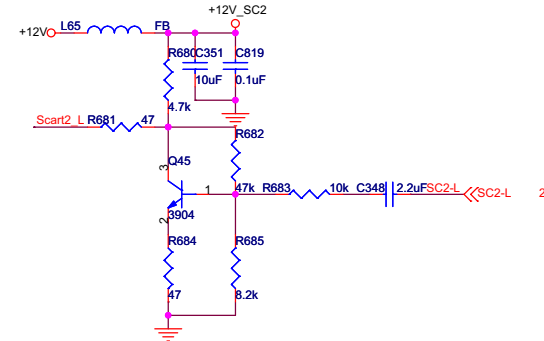
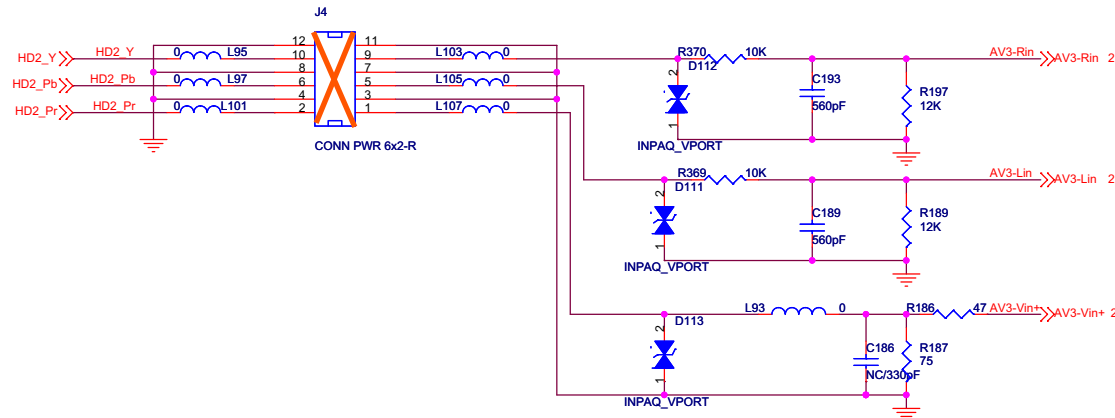


AV输出



Location
Near RCA.

AV3-----SUB AV (AV+S VIDEO) Input



Mstar
semiconductor

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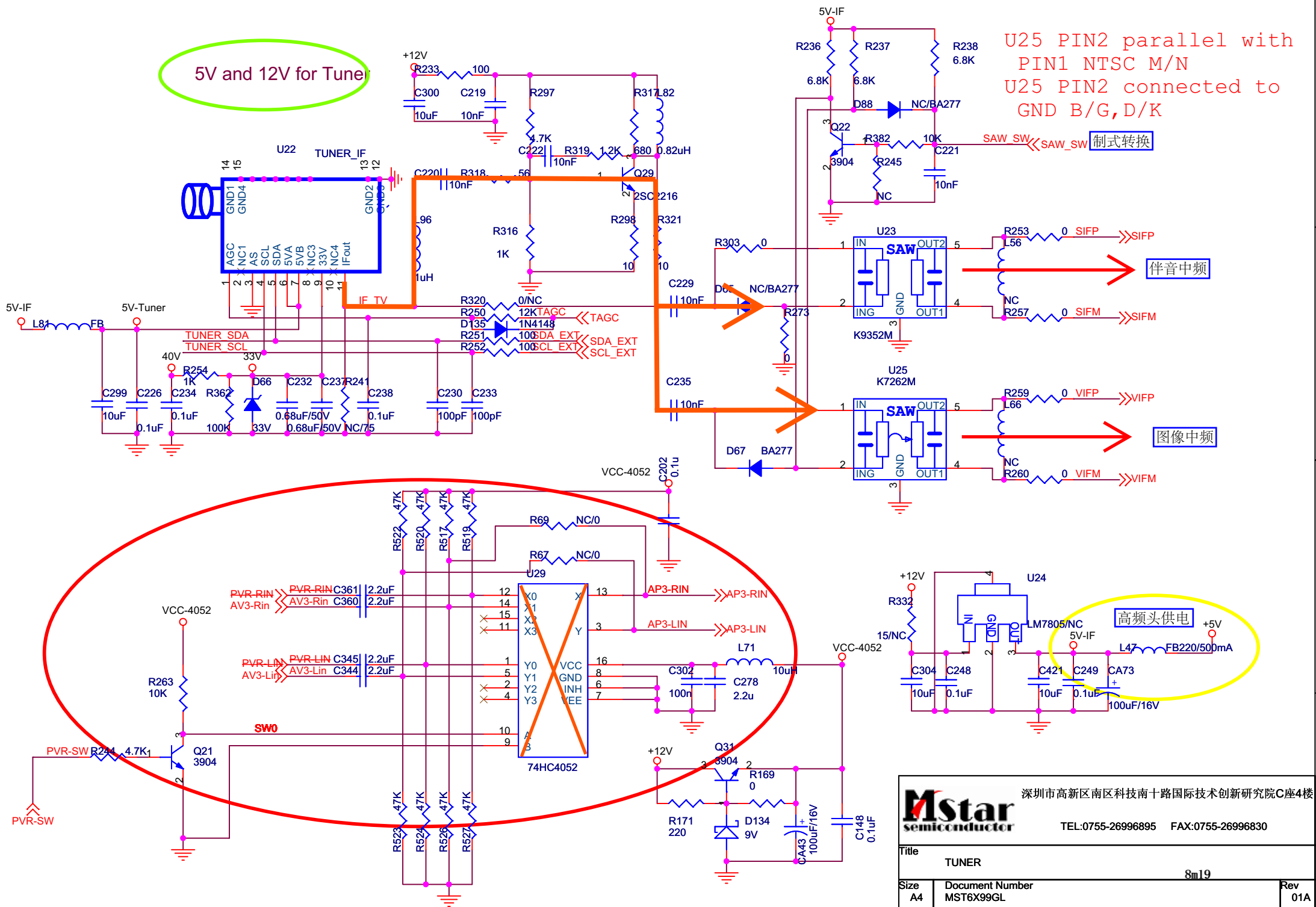
Title Video input 8m19

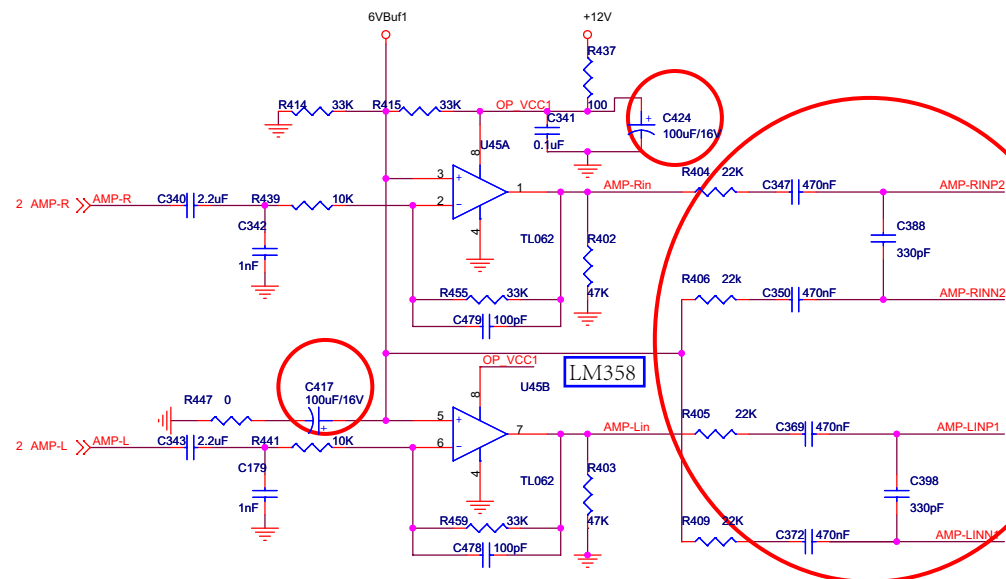
Size A3 Document Number MST6X99GL Rev 01A

Date: Saturday, February 23, 2008 Sheet 6 of 12

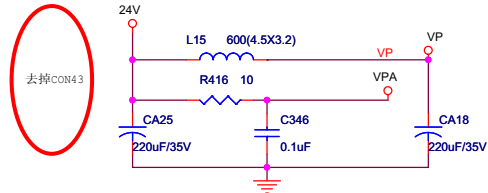
5V and 12V for Tune

U25 PIN2 parallel with
PIN1 NTSC M/N
U25 PIN2 connected to
GND B/G,D/K



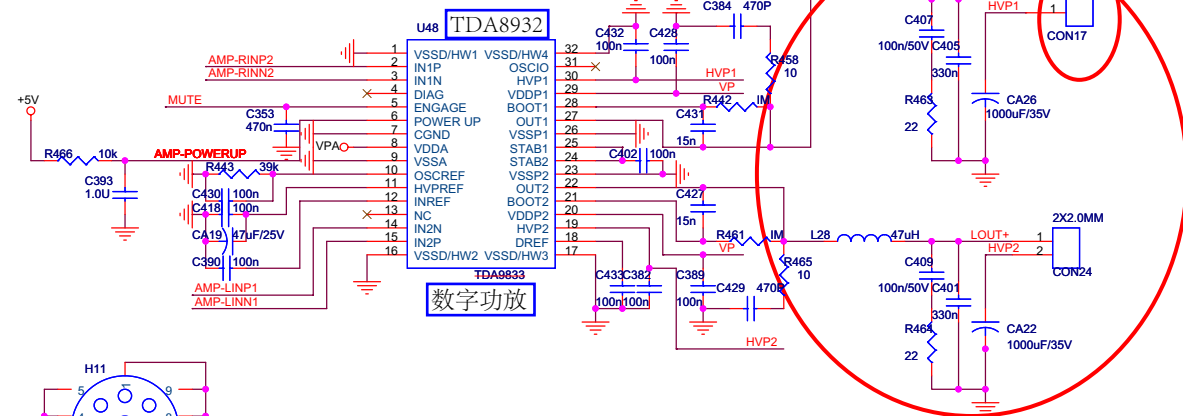
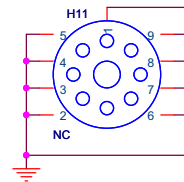
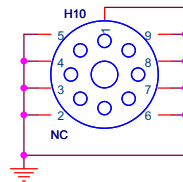
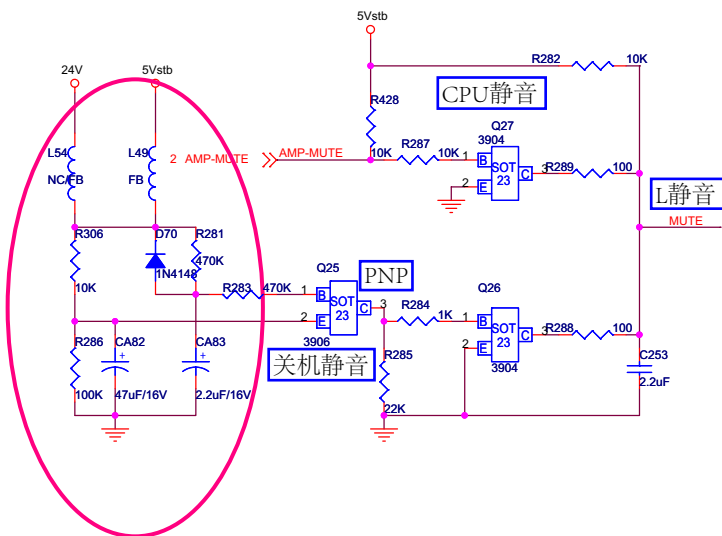


C347&C350
4 ohm:1000nF
8 ohm:470nF



去掉CON43

L5/C405 & L28/C401
4 ohm:22uH/680nF
8 ohm:47uH/330nF



2X2.5MM

CON17

2X2.0MM

CON24

