

GM21 LCD TV

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彩电维修资料网

TV160

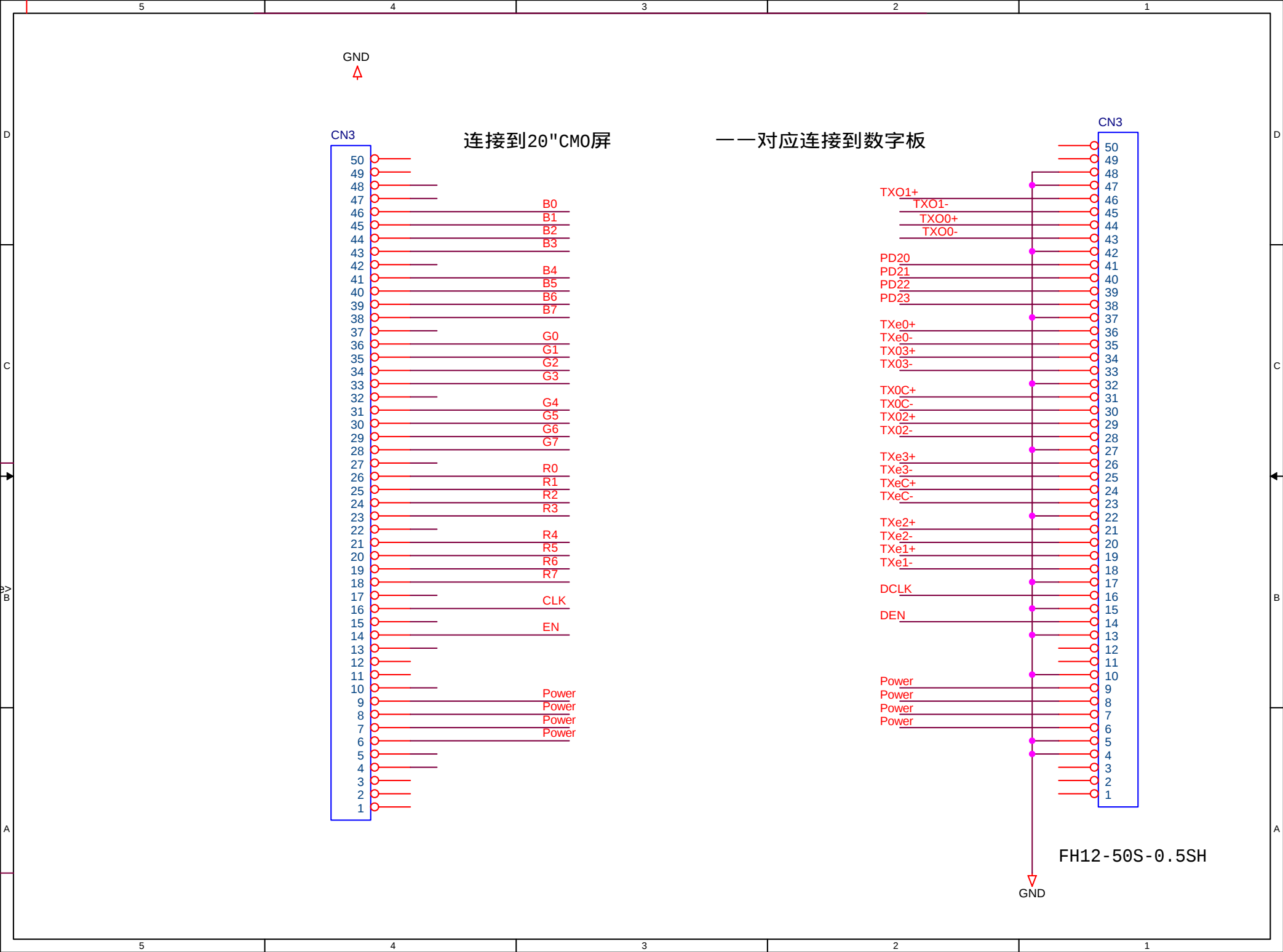
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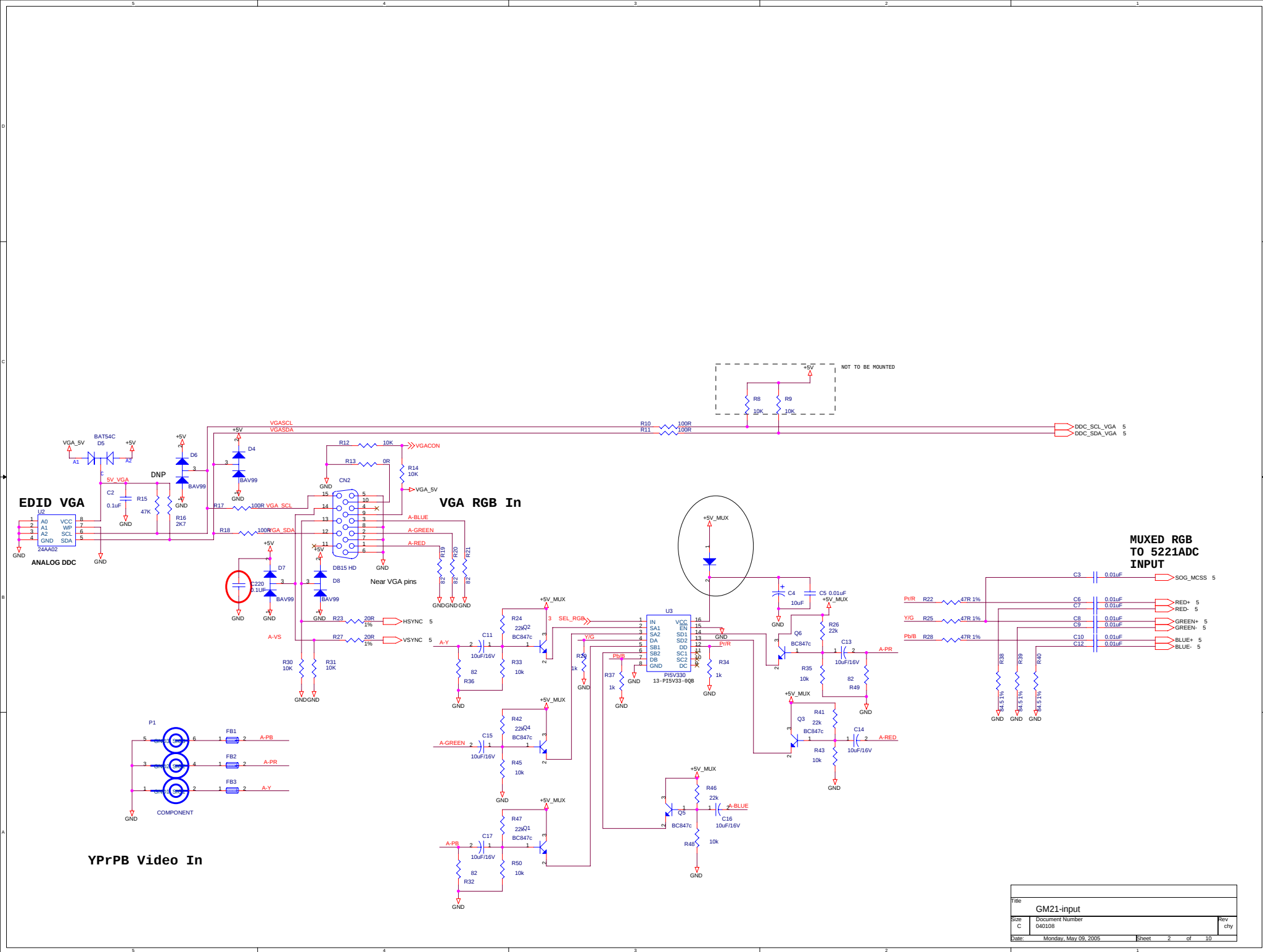
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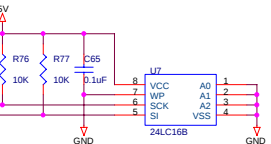
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Approval	Organization	Signature	Date
		CHY	04-05-17

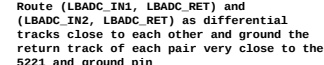
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The diagram shows the address bus configuration for the 74163 counter. The counter's T2 output is connected to the 'For_Update' input. The address bus lines R54 through R72 are each connected to a 10K resistor and an RMADDRn input of the 74163. The counter is powered by +5V and has its ground connected to the bottom rail.

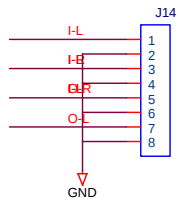


I2C
PWM
OUTPUT
FOR AMP
VOLUME,
BL
BRITNESS

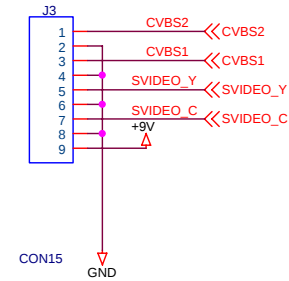
KEYBOARD
INPUT TO
LBW ADC

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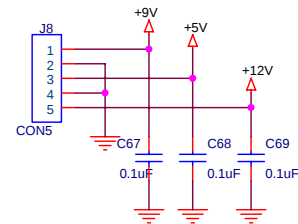
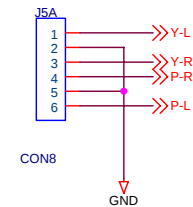
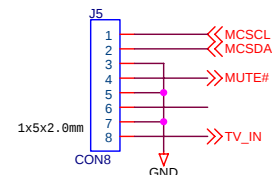
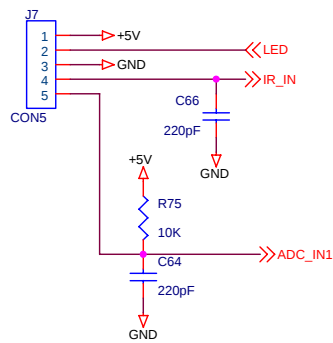
Keypad
INTERFACE



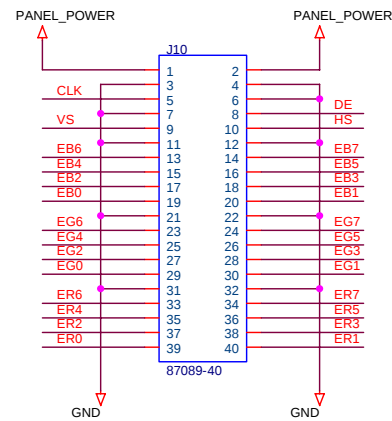
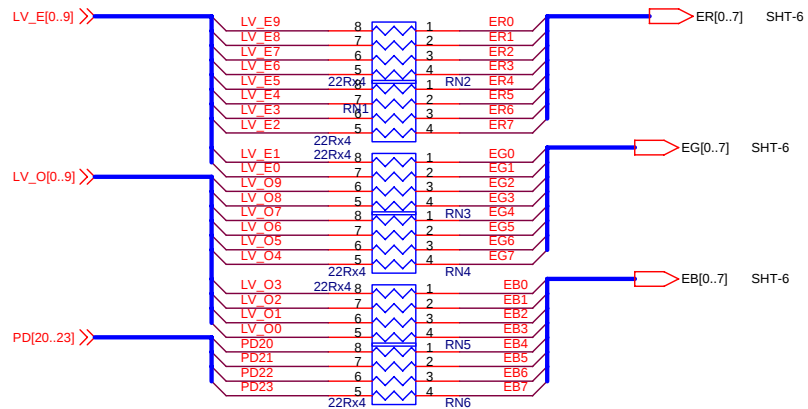
NV RAM
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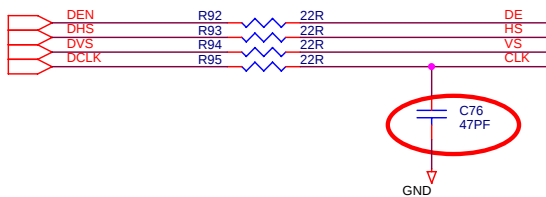
IR REMOTE SENSOR
INTERFACE



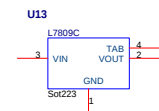
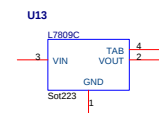
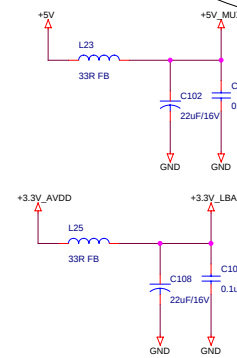
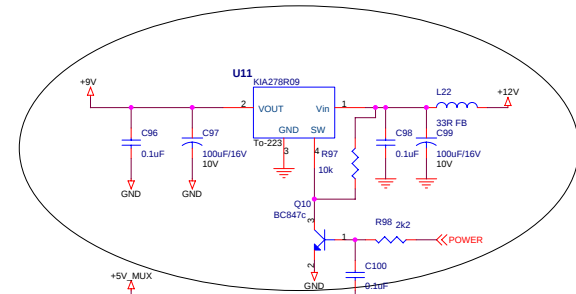
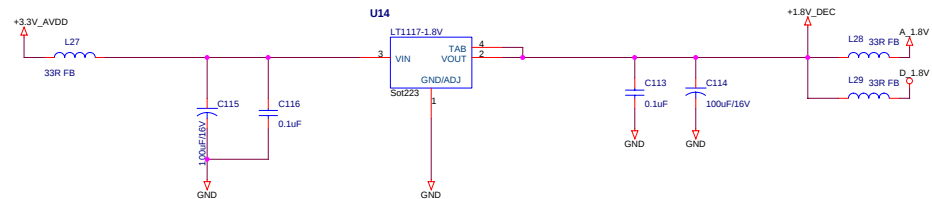
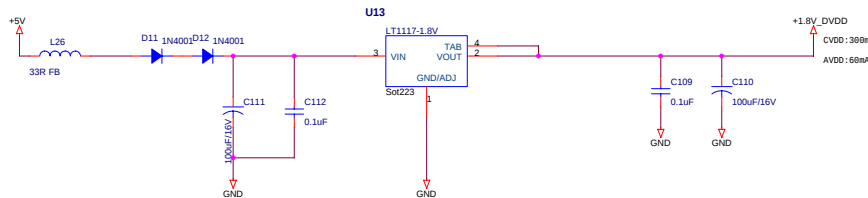
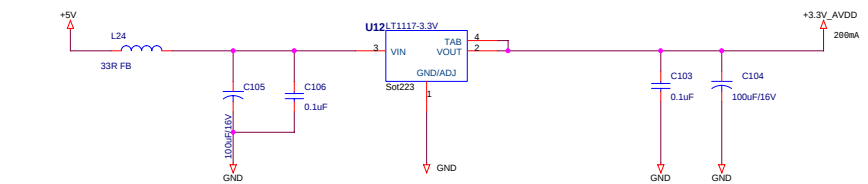
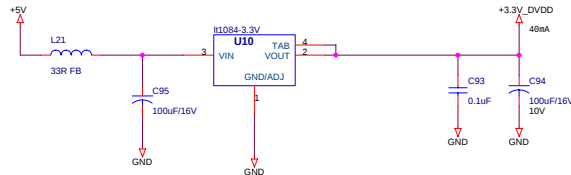
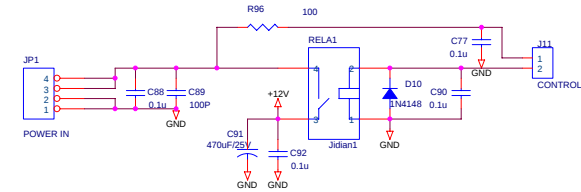
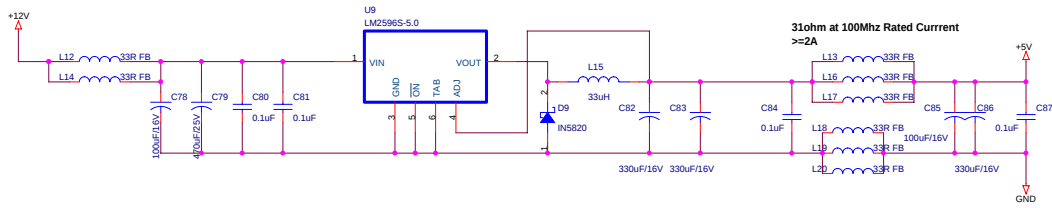
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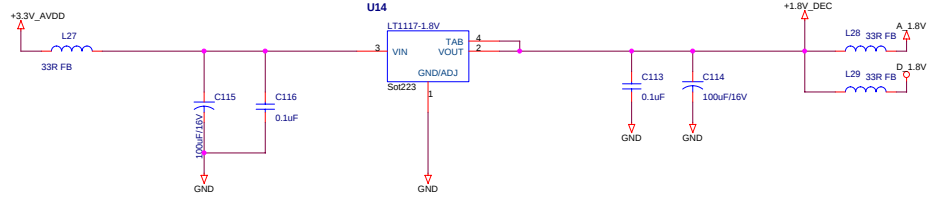
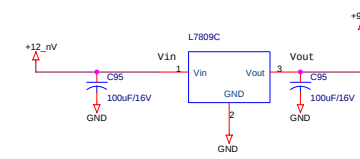
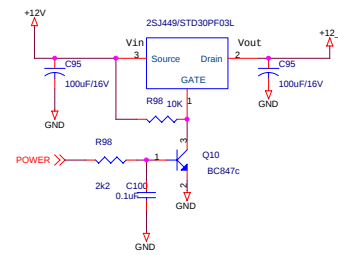
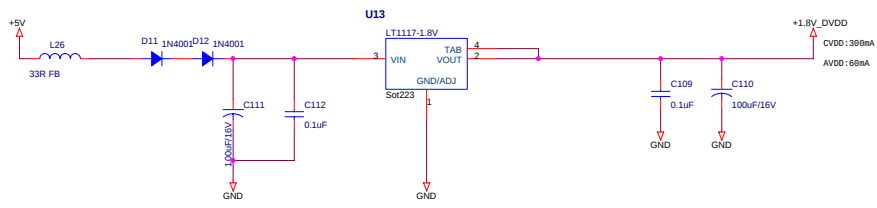
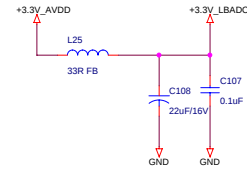
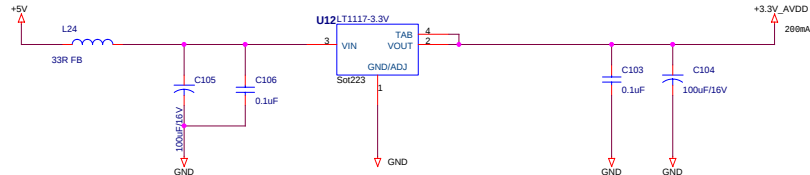
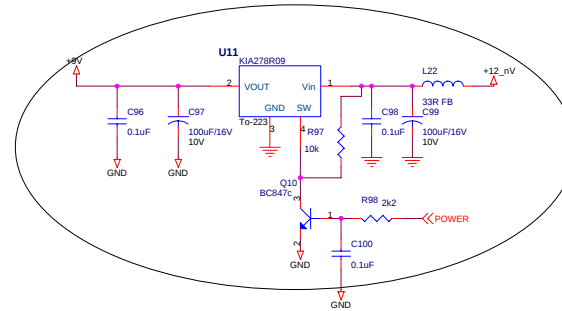
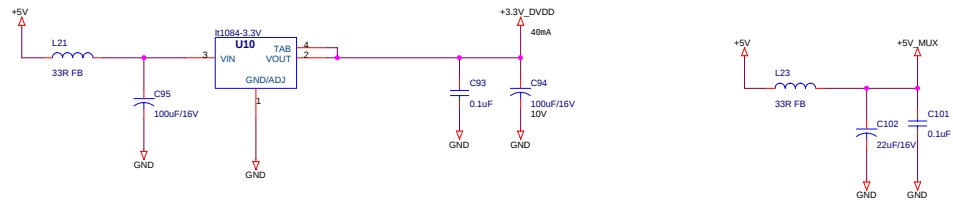
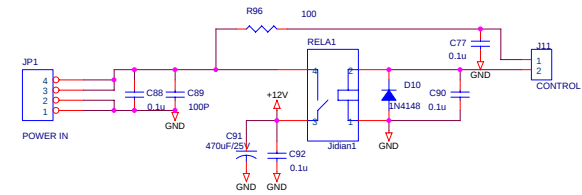
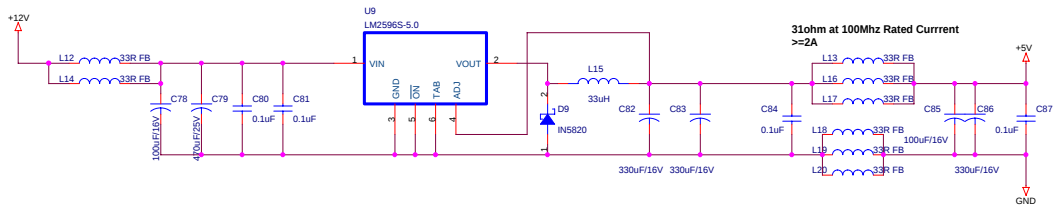
SHT-5,6 DEN
SHT-5,6 DHS
SHT-5,6 DVS
SHT-5,6 DCLK



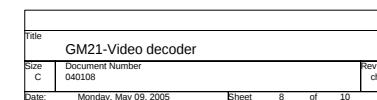
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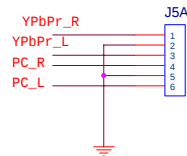
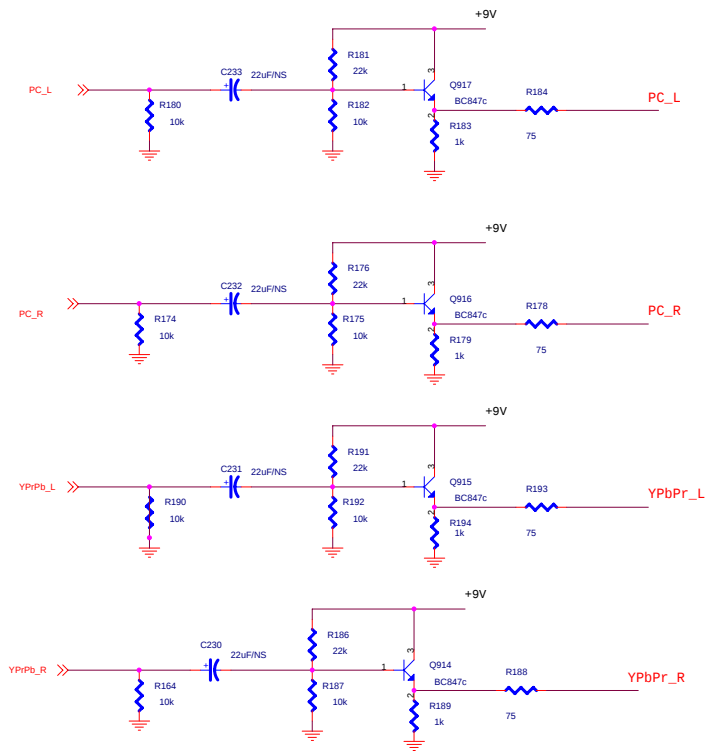


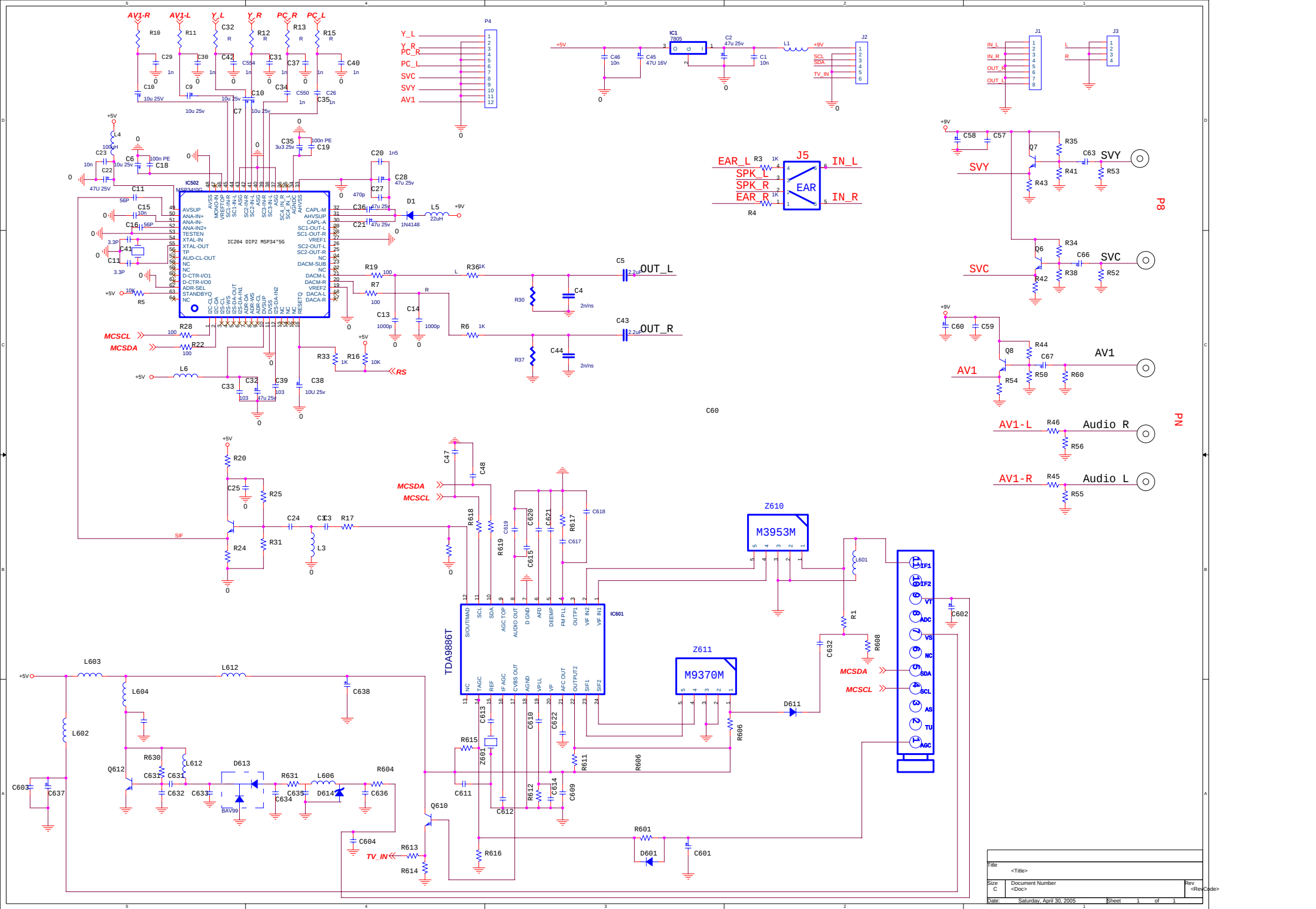
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