

GM21 LCD TV

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彩电维修资料网

TV160 彩电维修资料网

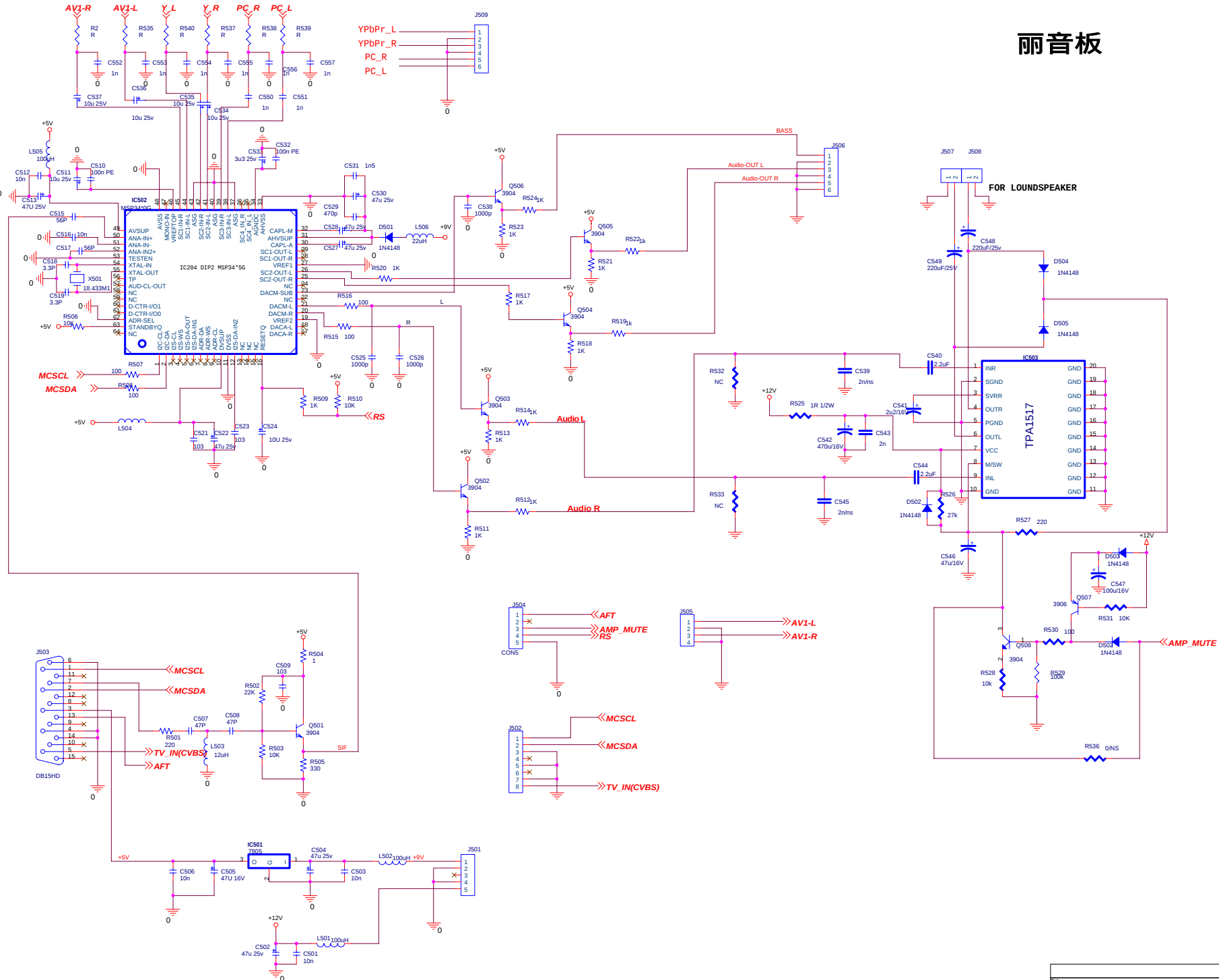
http://www.tv160.net

http://www.tv160.net

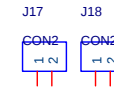
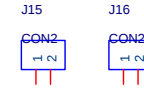
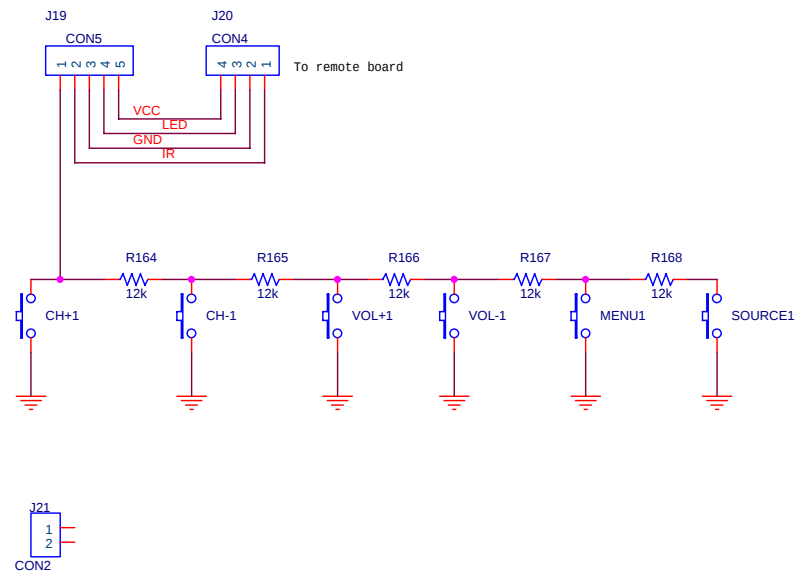
| Approval | Organization | Signature | Date     |
|----------|--------------|-----------|----------|
|          |              | CHY       | 04-05-17 |
|          |              |           |          |
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|                                  |                 |               |
|----------------------------------|-----------------|---------------|
| Title                            |                 |               |
| GM21                             |                 |               |
| Size                             | Document Number | Rev           |
| B                                | 040108          | chy           |
| Date: Tuesday, December 21, 2004 |                 | Sheet 1 of 10 |

# 丽音板



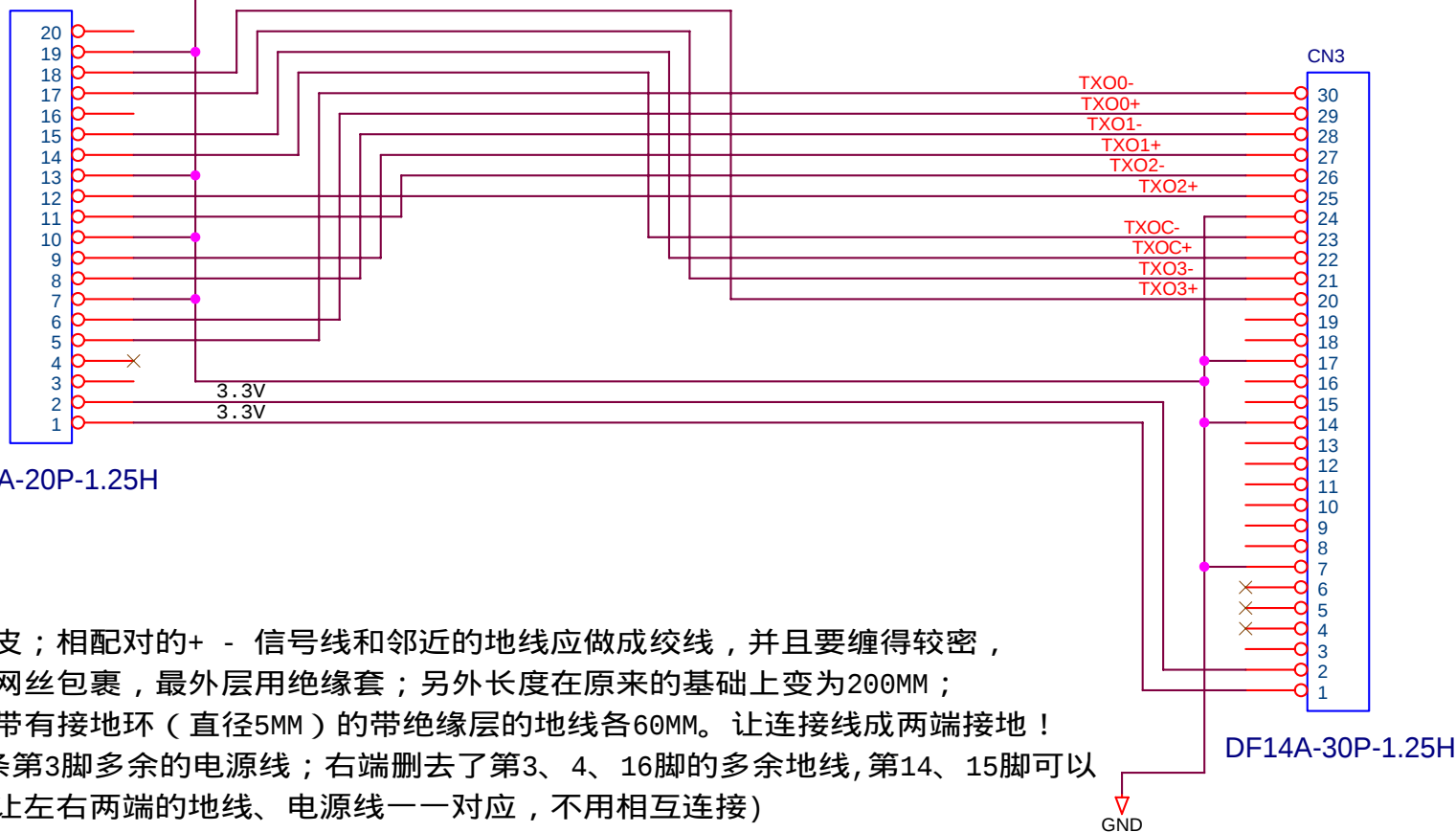
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| C     | <Doc>                     |  |  |
| Date  | Tuesday, January 04, 2005 |  |  |
| Sheet | 1 of 1                    |  |  |



|              |                            |       |             |
|--------------|----------------------------|-------|-------------|
| Title        |                            |       |             |
| GM21-key pad |                            |       |             |
| Size<br>B    | Document Number<br>040108  |       | Rev<br>city |
| Date:        | Tuesday, December 21, 2004 | Sheet | 1 of 10     |

连接到15" SAMSUNG屏

连接到数字板

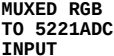


### 注意！

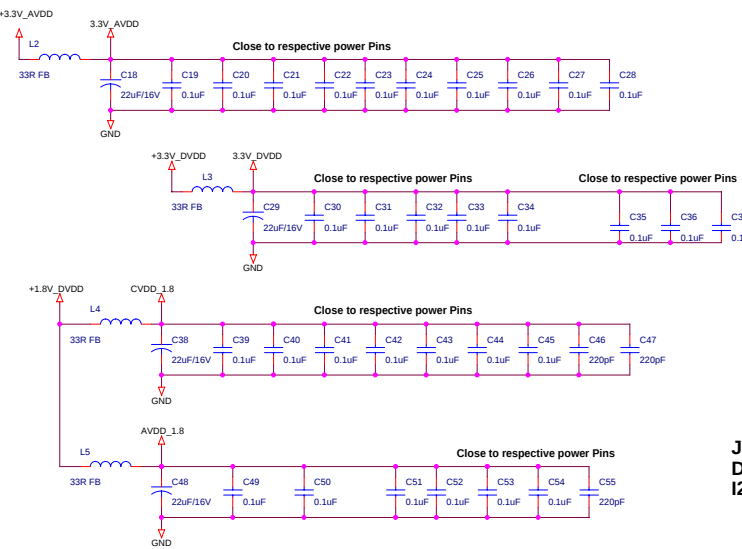
地线需要剥皮；相配对的+ - 信号线和邻近的地线应做成绞线，并且要缠得较密，外面用导电网丝包裹，最外层用绝缘套；另外长度在原来的基础上变为200MM；两端都引出带有接地环（直径5MM）的带绝缘层的地线各60MM。让连接线成两端接地！（右端删去了一条第3脚多余的电源线；右端删去了第3、4、16脚的多余地线，第14、15脚可以不缠地线；让左右两端的地线、电源线一一对应，不用相互连接）

长度：200MM；插头结构如上所示：DF14A-20P-1.25H和DF14A-30P-1.25H

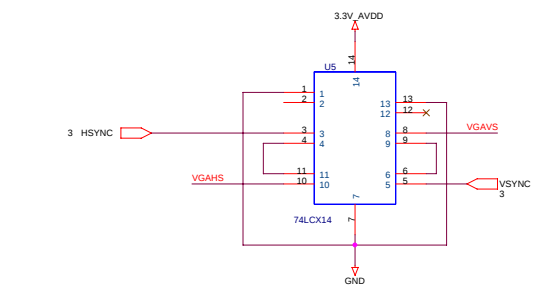
DVI test points right on the trace to avoid extra length

**EDID DVI**

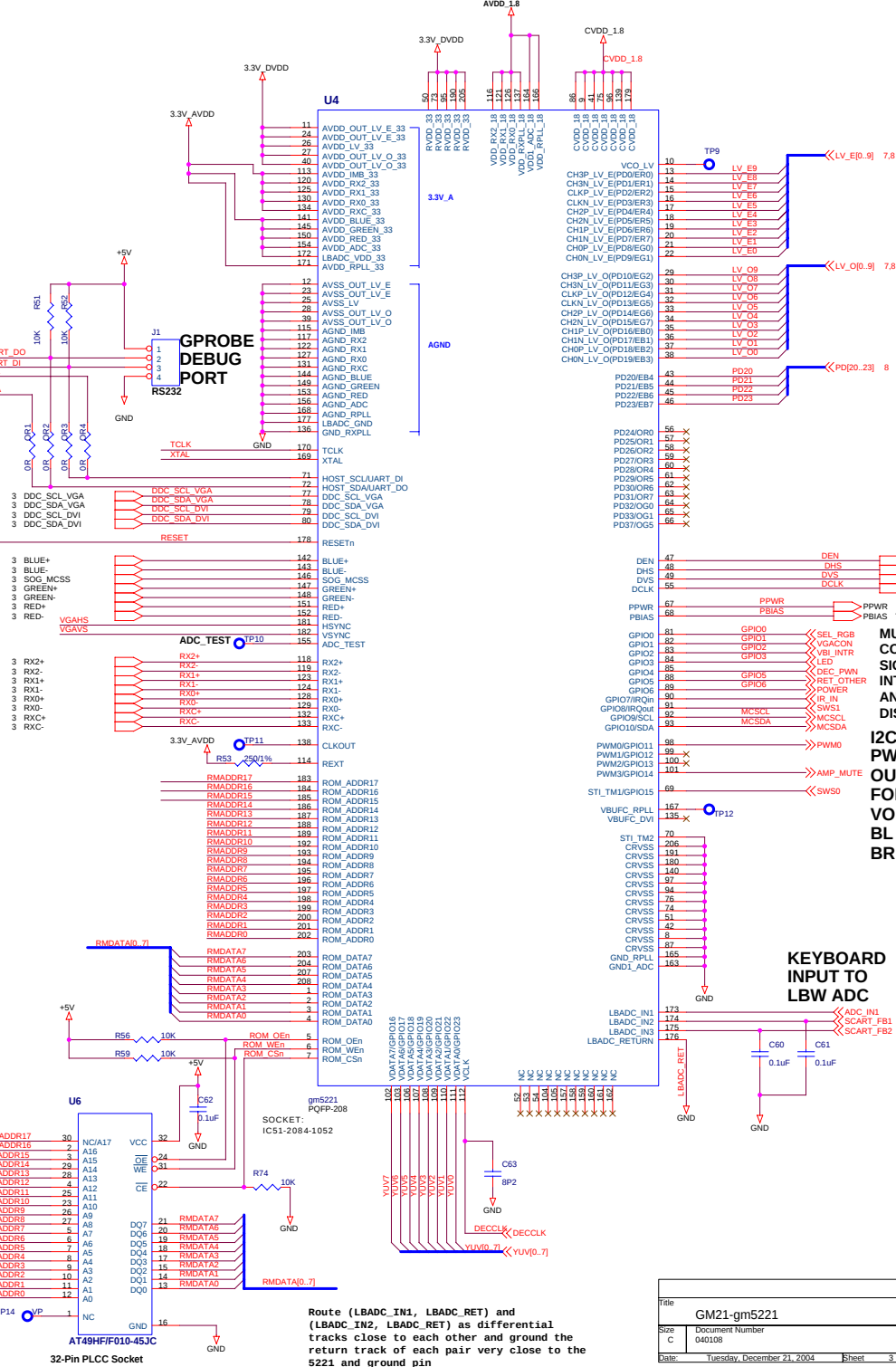
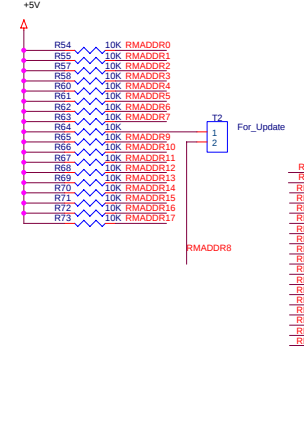
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| Title      |                            |       |         |
| GM21-input |                            |       |         |
| Size       | Document Number            |       | Rev     |
| C          | 040108                     |       | chy     |
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|                          |                                                                                                                                                                                                                                                    |
|--------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Rootstrap Signals</b> |                                                                                                                                                                                                                                                    |
| <b>RMADDR[6:0]</b>       | For I2C address [6:0] to JTAG bridge Host VF, to determine device address.                                                                                                                                                                         |
| <b>RMADDR[12:7]</b>      | User_Bits[5:0] for configuration setting                                                                                                                                                                                                           |
| <b>RMADDR[14:13]</b>     | OP_MODE[1:0]:<br>00 = Normal op. UART in 186 on system pins.<br>01 = DC to JTAG bridge.<br>10 = JTAG port, 5 wires.<br>11 = External parallel control bus using ROM Addr/Data                                                                      |
| <b>RMADDR15</b>          | SPI_LEN: 0 = parallel ROM (IF. 1 = SPI serial ROM and Cache control                                                                                                                                                                                |
| <b>RMADDR16</b>          | Initial state of OCM ROM:<br>0 = internal ROM on, and mapped to top 32K of OCM and OCM boot will be from internal ROM code.<br>1 = internal ROM off. The external ROM mapped to entire upper 512K of OCM address. OCM boot from external ROM code. |
| <b>RMADDR17</b>          | OSC_SEL: 0 = Xtal and internal oscillator.<br>1 = TTL oscillator (on TCLK pin)                                                                                                                                                                     |



**BOOTSTRAP OPTIONS SELECT (NOT POPULATED SHOULD BE HARD WIRED)**



**MUX SEL,VGA CONNECTOR SIGNAL,VBI INTR,LED,AMP AND AUDIO DISABLE**

**I2C PWM OUTPUT FOR AMP VOLUME, BL BRITNESS**

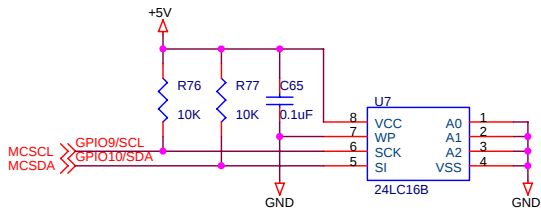
**KEYBOARD INPUT TO LBW ADC**

Route (LBADC\_IN1, LBADC\_RET) and (LBADC\_IN2, LBADC\_RET) as differential tracks close to each other and ground the return track of each pair very close to the 5221 and ground pin

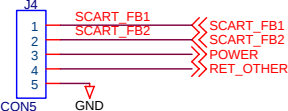
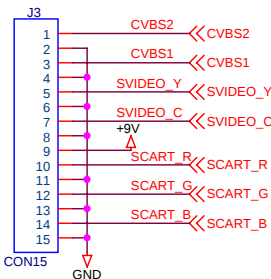
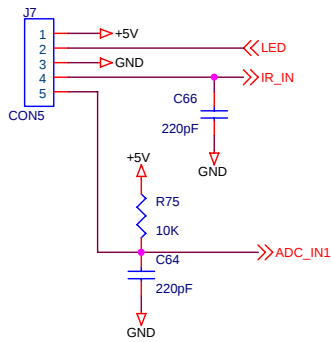
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|-------|----------------------------|---------------|
| Title |                            | GM21-gm5221   |
| Size  | Document Number            | 040108        |
| C     |                            |               |
| Date: | Tuesday, December 21, 2004 | Sheet 3 of 10 |

Keypad  
INTERFACE

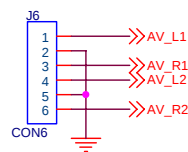
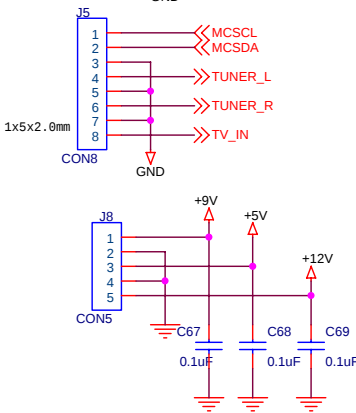
NV RAM  
INTERFACE



IR REMOTE SENSOR  
INTERFACE



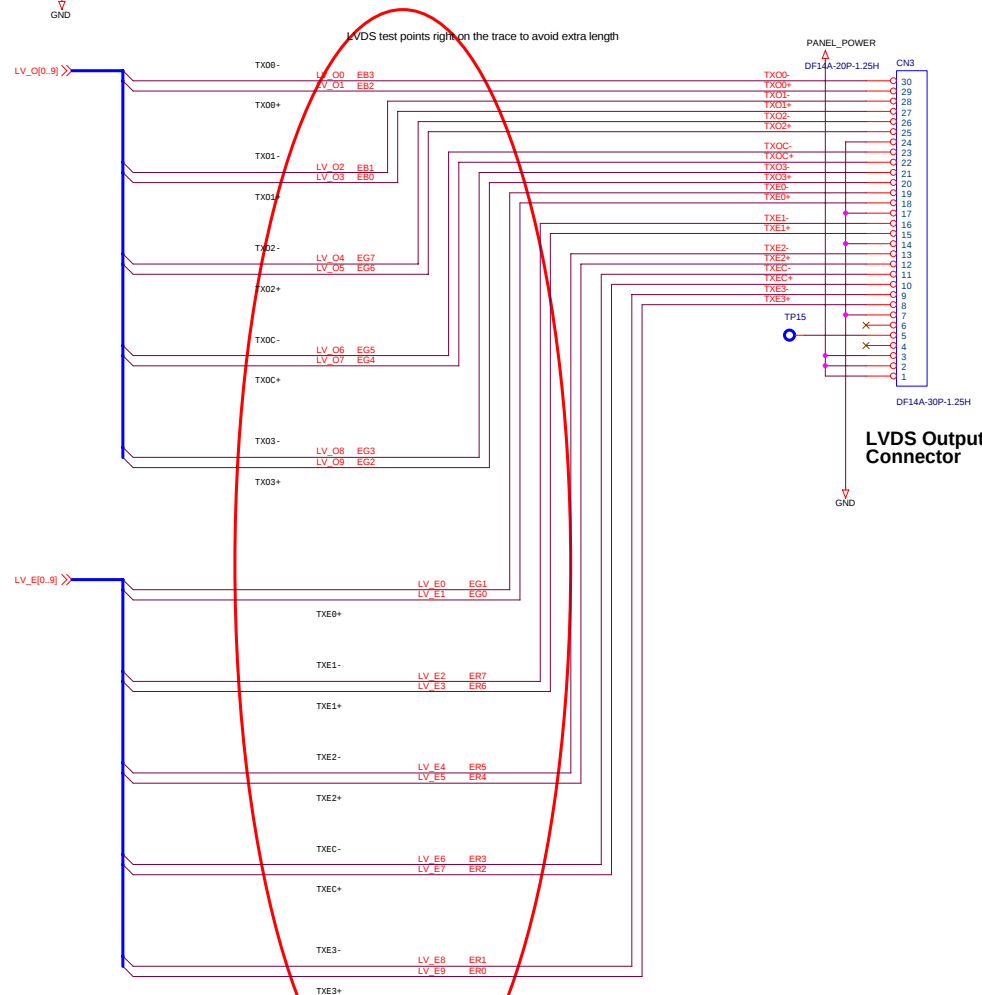
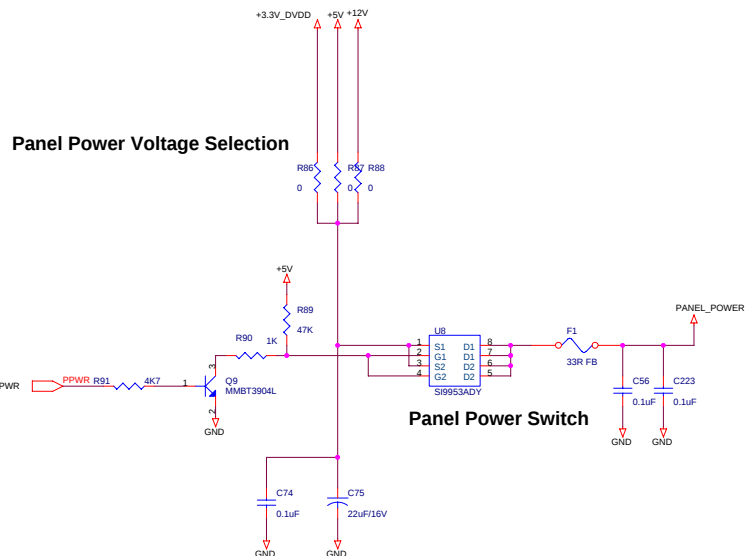
EXTERAL AV  
BOARD  
INTERFACE



|                  |                            |               |
|------------------|----------------------------|---------------|
| Title            |                            |               |
| GM21- connectors |                            |               |
| Size             | Document Number            | Rev           |
| B                | 040108                     | chy           |
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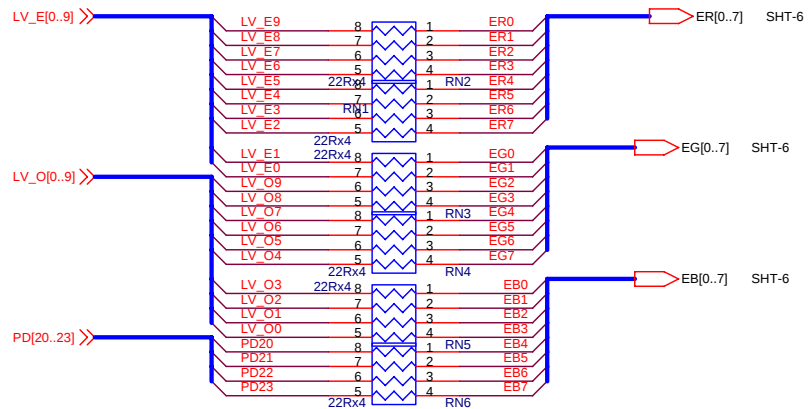


连接到15" SAMSUNG屏

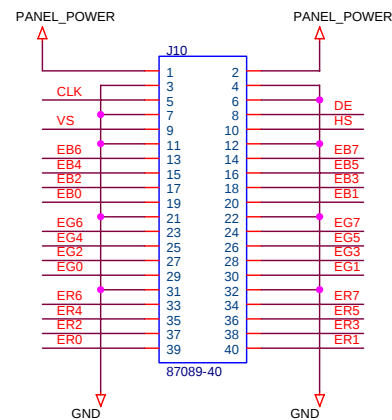
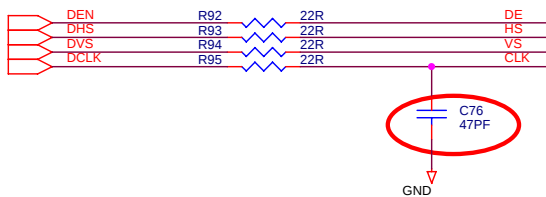


### LVDS Output Connector

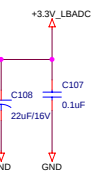
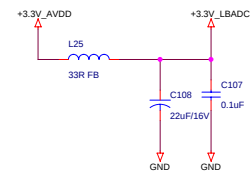
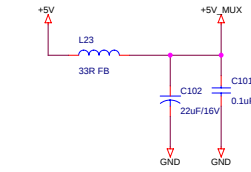
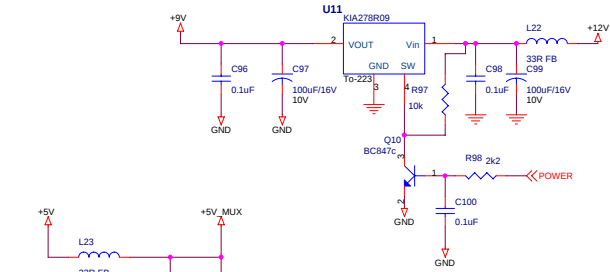
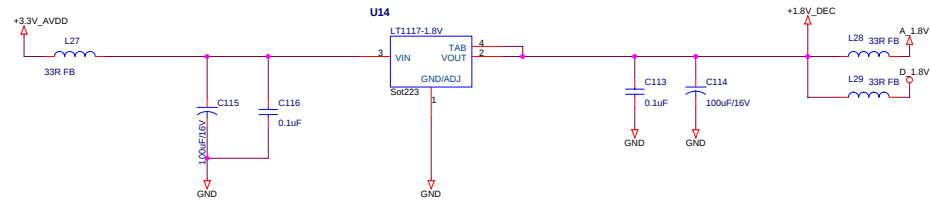
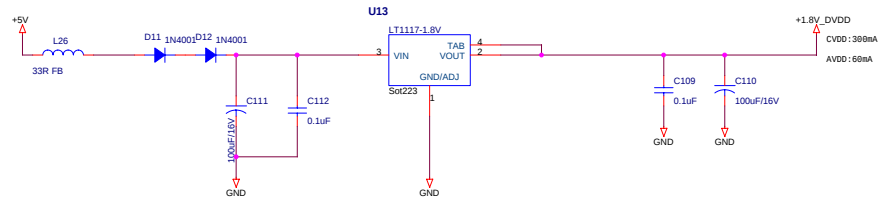
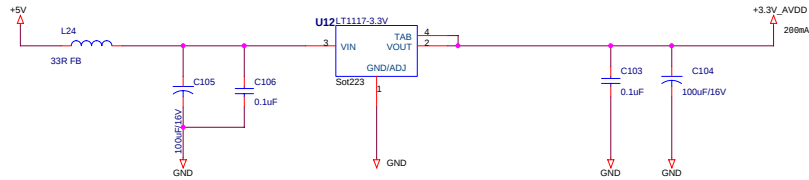
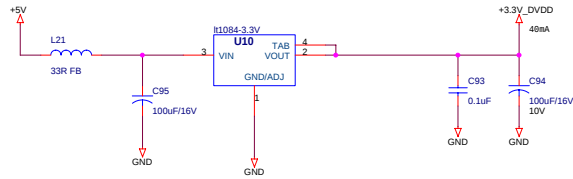
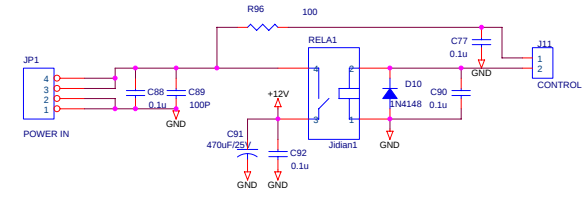
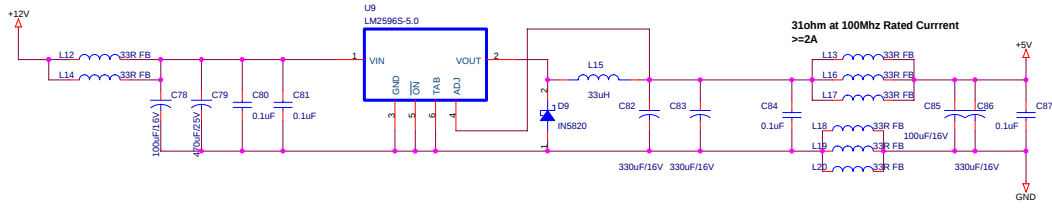
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| Title         |                            |       |         |
| GM21-LVDS out |                            |       |         |
| Size C        | Document Number            | Rev C |         |
|               | 040108                     |       |         |
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SHT-5,6 DEN  
SHT-5,6 DHS  
SHT-5,6 DVS  
SHT-5,6 DCLK

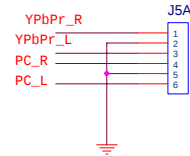
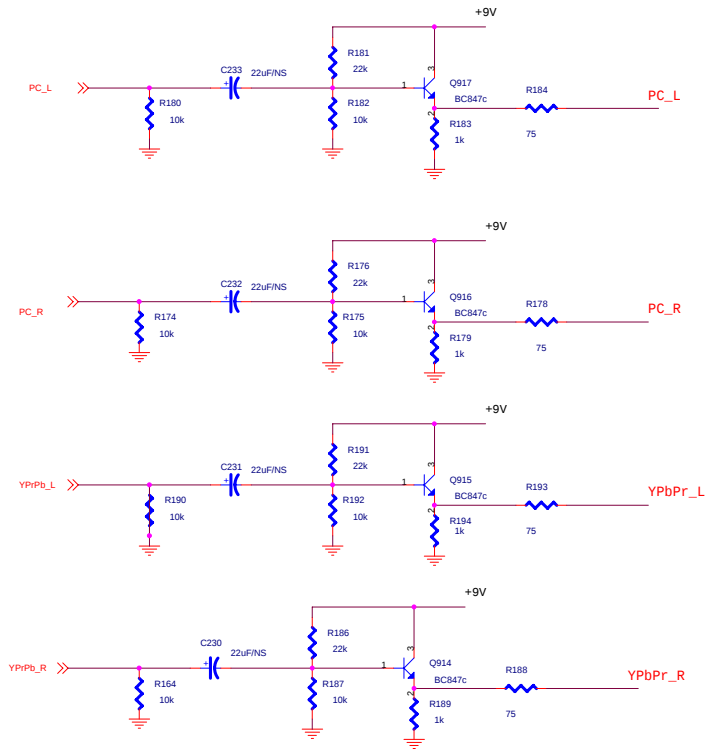


|              |                            |               |
|--------------|----------------------------|---------------|
| Title        |                            |               |
| GM21-TTL out |                            |               |
| Size         | Document Number            | Rev           |
| B            | 040108                     | chy           |
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|            |                            |               |
|------------|----------------------------|---------------|
| Title      |                            |               |
| GM21-power |                            |               |
| Size       | Document Number            | Rev<br>chy    |
| C          | 040108                     |               |
| Date:      | Tuesday, December 21, 2004 | Sheet 7 of 10 |





|            |                            |  |       |            |
|------------|----------------------------|--|-------|------------|
|            |                            |  |       |            |
| Title      |                            |  |       |            |
| GM21-Audio |                            |  |       |            |
| Size       | Document Number            |  |       | Rev<br>chy |
| C          | 040108                     |  |       |            |
| Date:      | Tuesday, December 21, 2004 |  | Sheet | 9 of 10    |