

# LCD TV 26T Digital

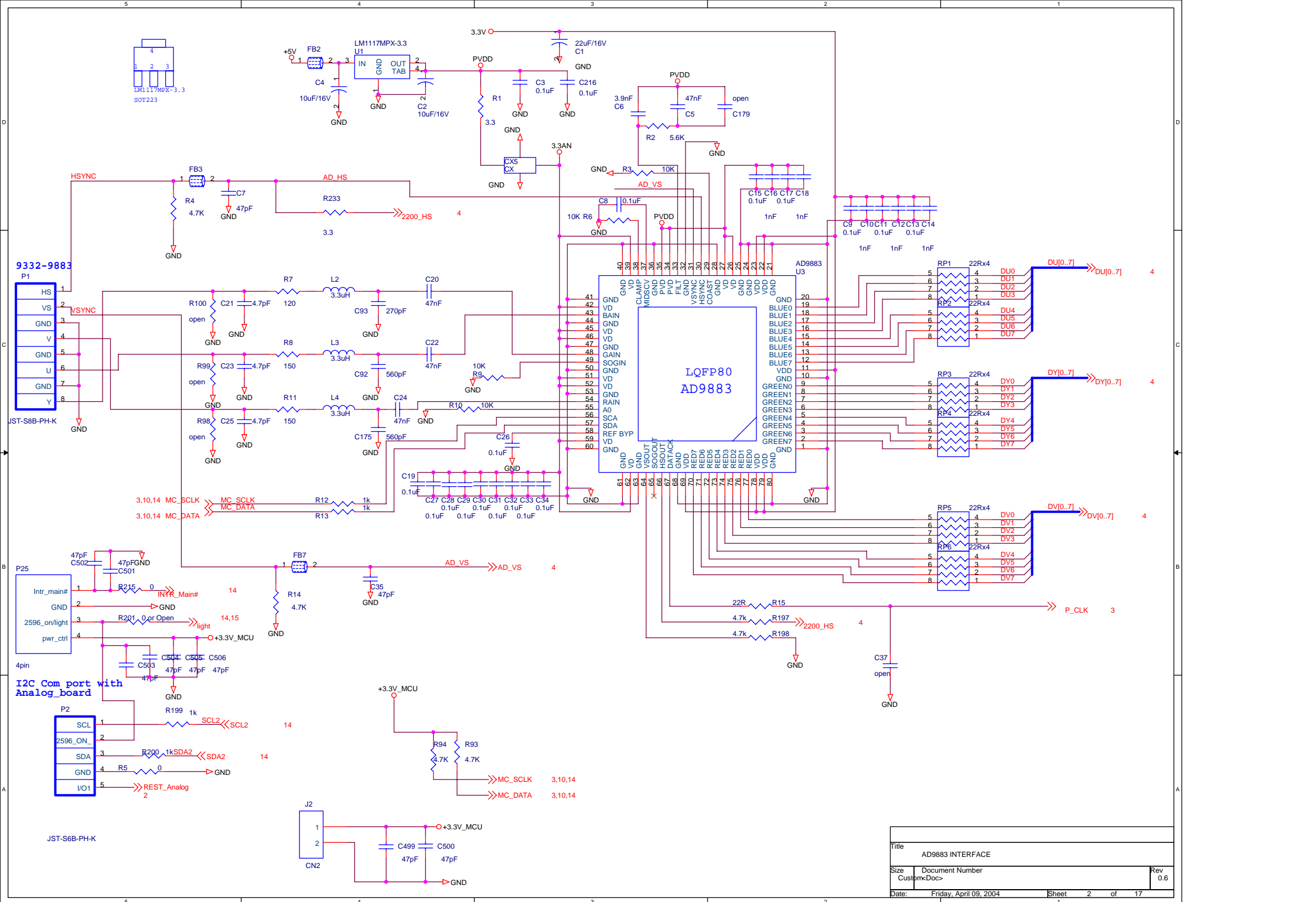
VER2.1                      2004.4.6

Status:V2.1    20040405

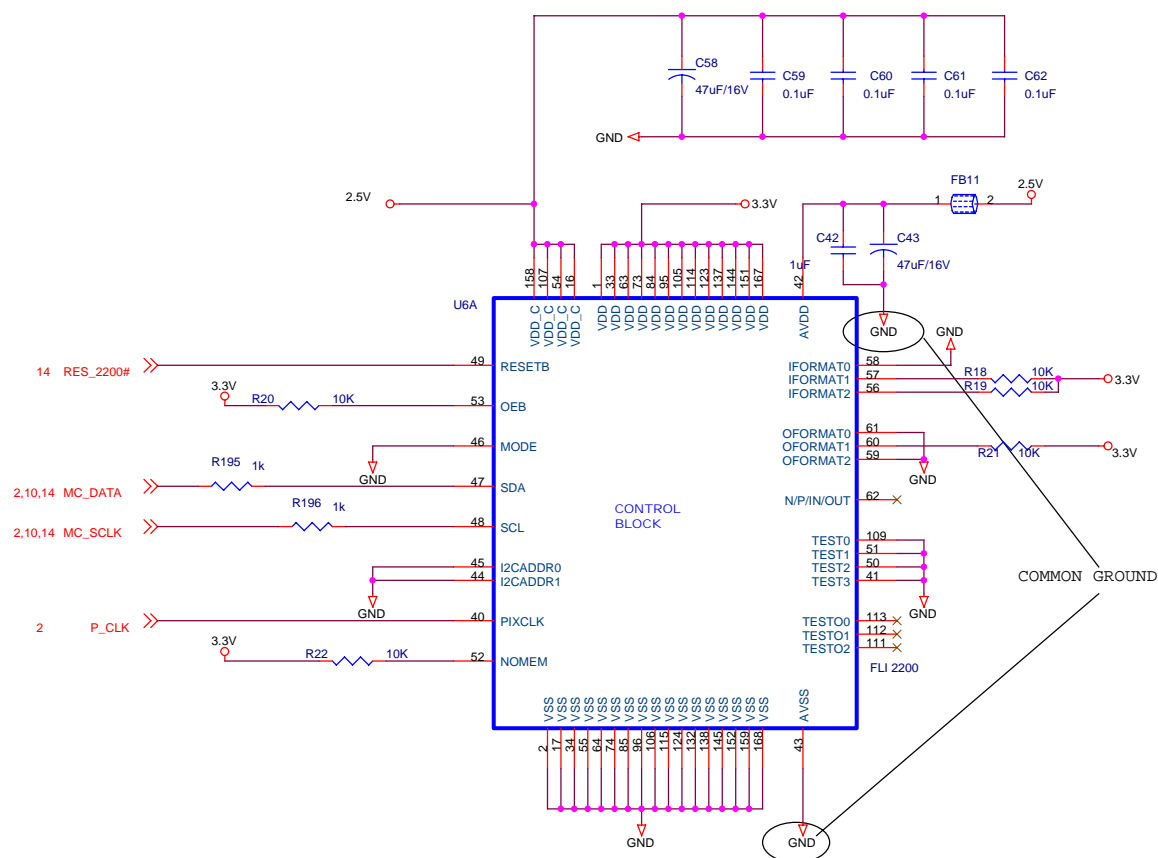
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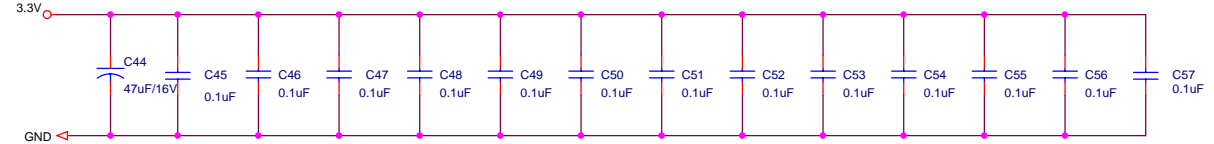
Title		
LCD26T		
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Custom:Doc>		<RevCode>
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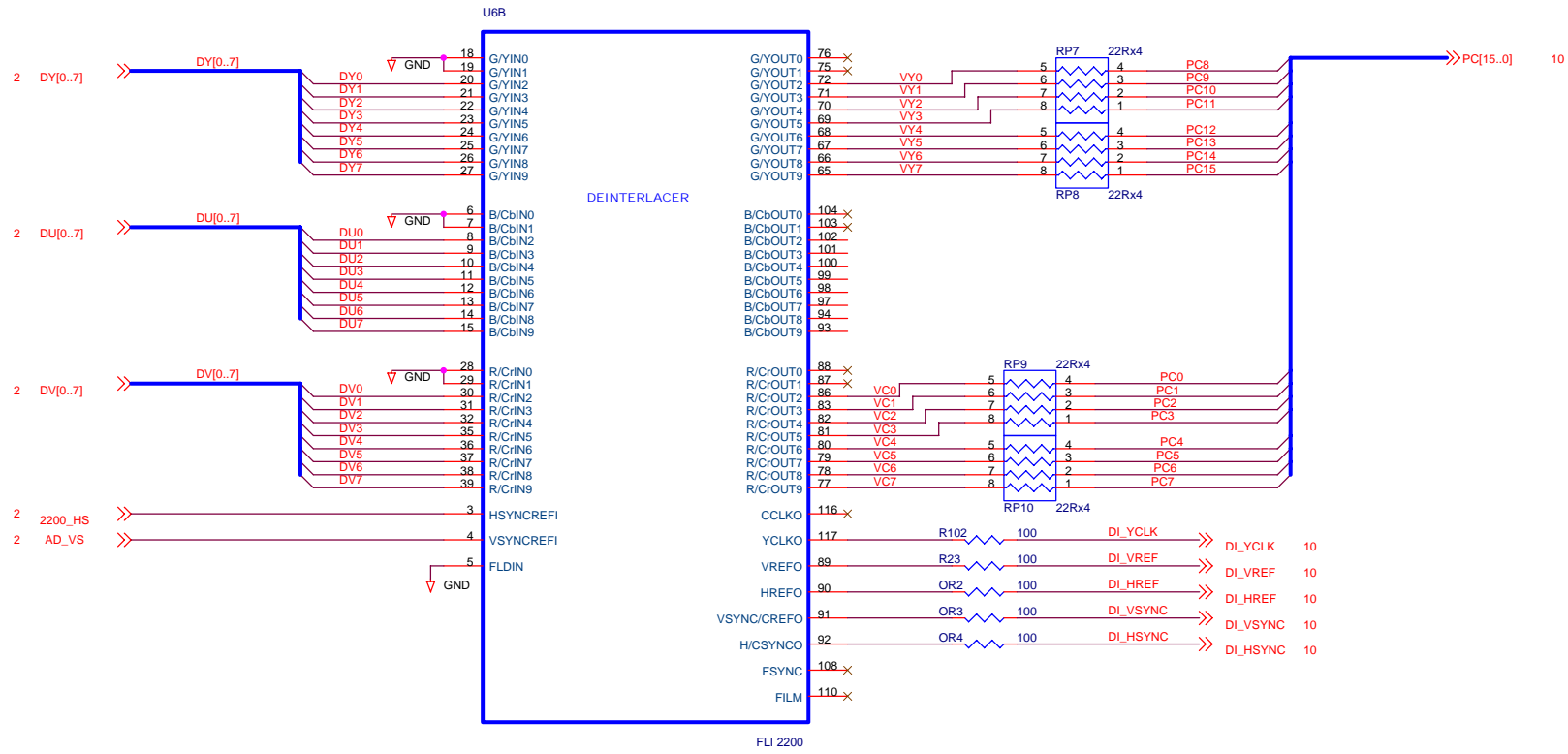
Title		
AD9883 INTERFACE		
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Custom:Doc>		0.6
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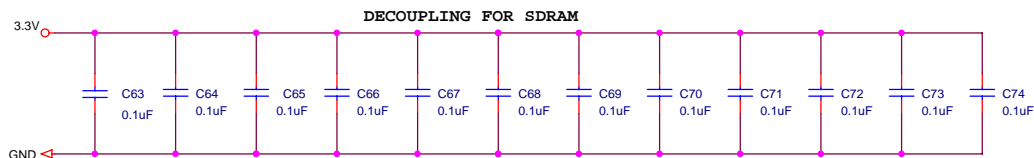
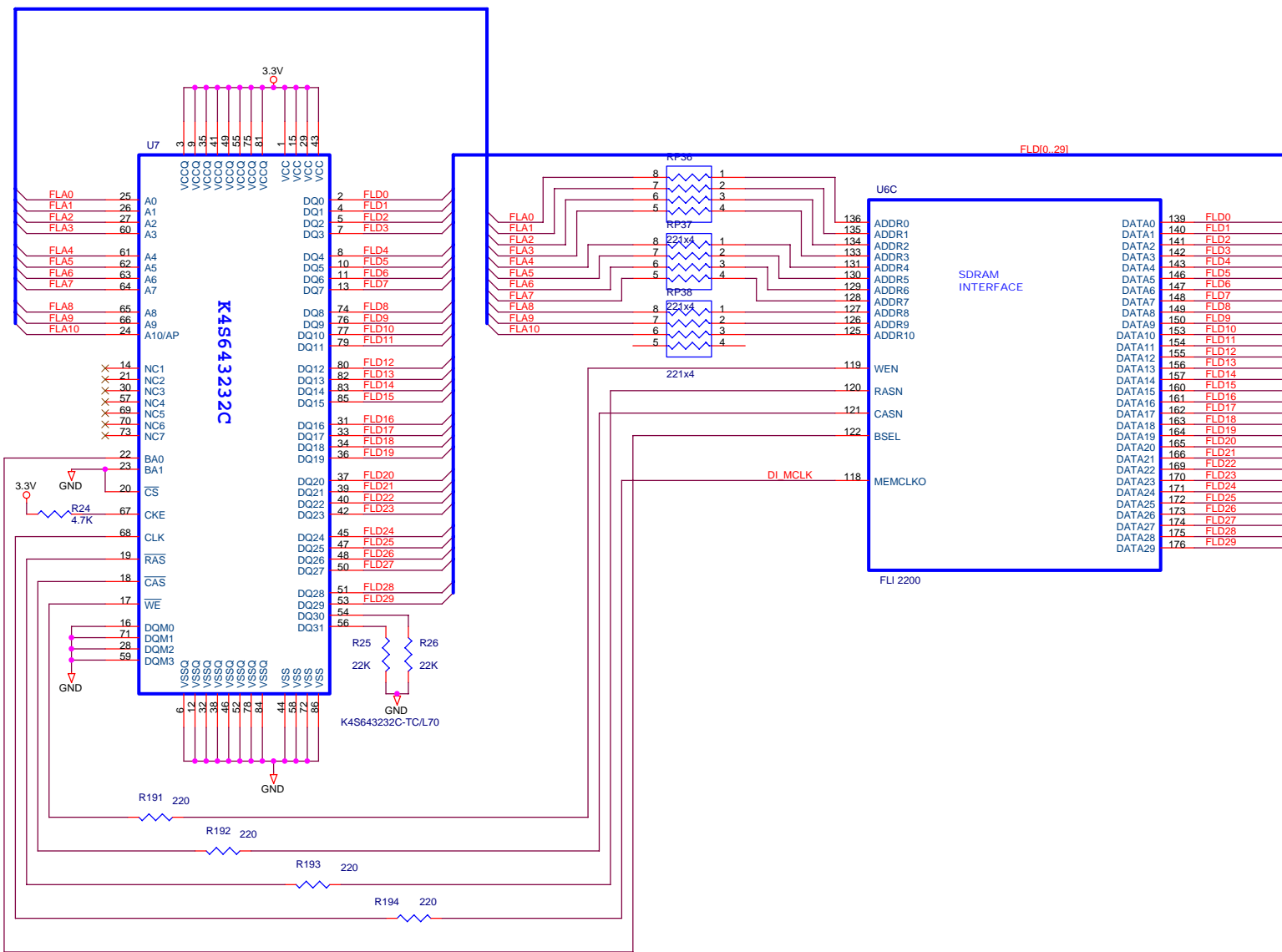
**DECOUPLING FOR FLI2200**



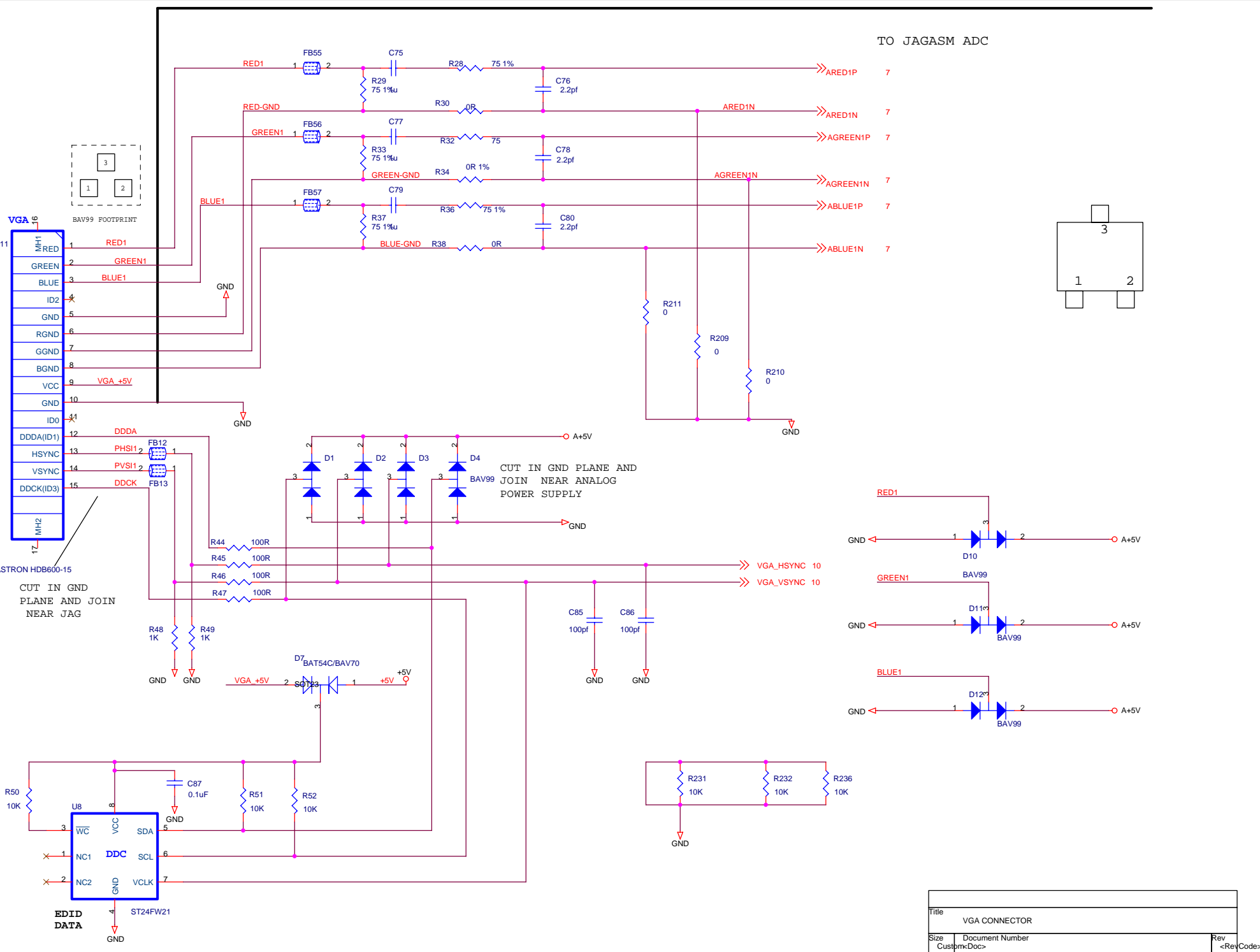
Title		
FLI2200 CONTROL INTERFACE		
Size	Document Number	Rev
Custom:Doc>		<RevCode>
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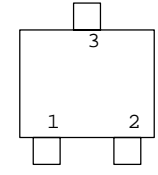
Title		FLI2200 INTERFACE	
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Title		
FLI2200 SDRAM INTERFACE		
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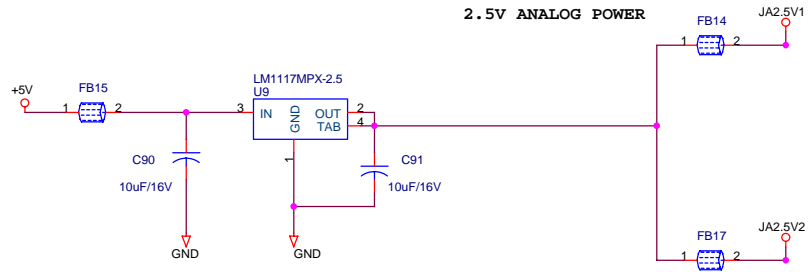
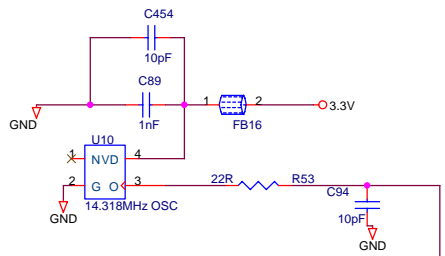
TO JAGASM ADC



ASTRON HDB600-15  
CUT IN GND PLANE AND JOIN NEAR JAG

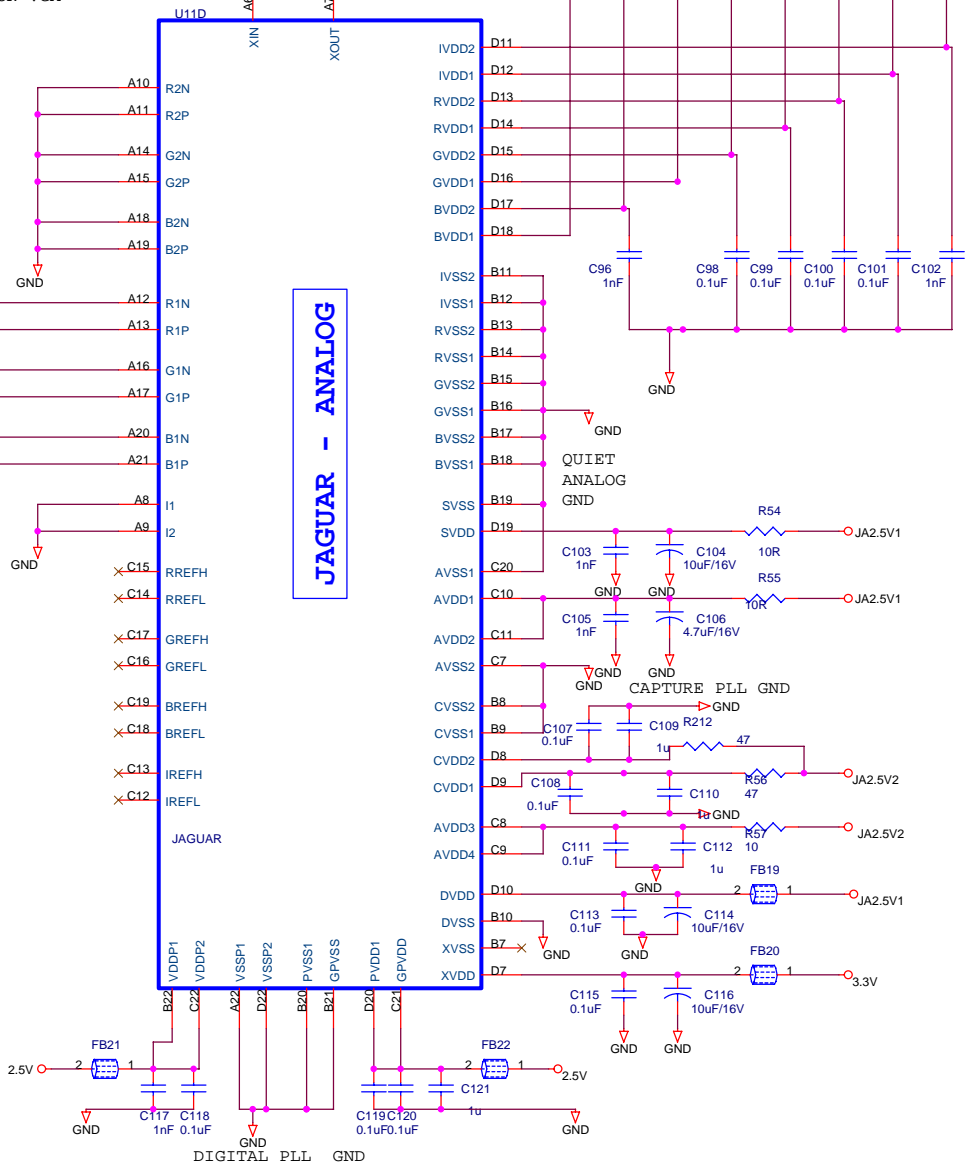
CUT IN GND PLANE AND JOIN NEAR ANALOG POWER SUPPLY

Title VGA CONNECTOR		
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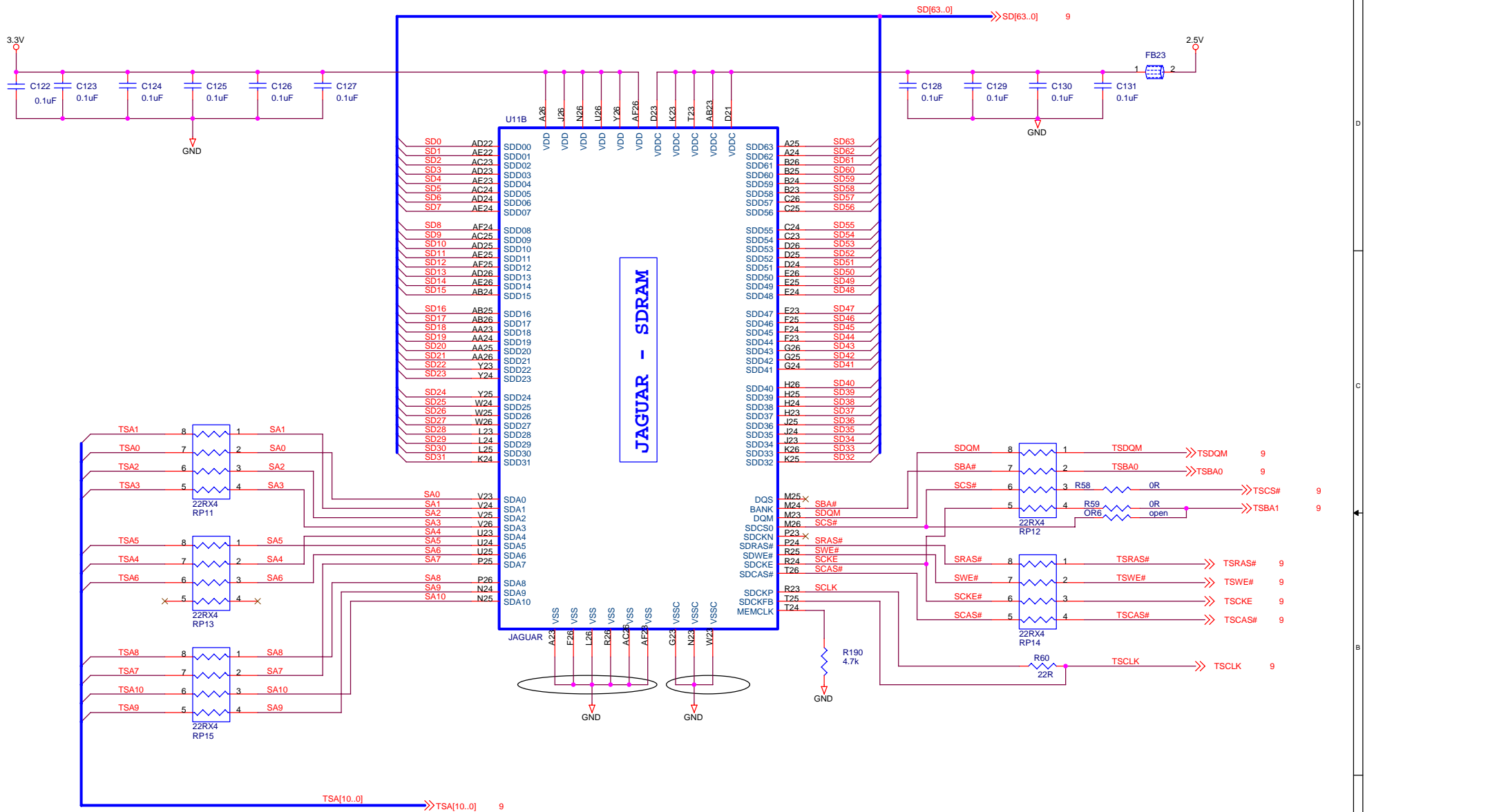


UNDER THESE LINES GND PLANE MUST BE CUT FROM VGA AS SHOWN ON VGA SHEET.

- 6 ARED1N
- 6 ARED1P
- 6 AGREEN1N
- 6 AGREEN1P
- 6 ABLUE1N
- 6 ABLUE1P

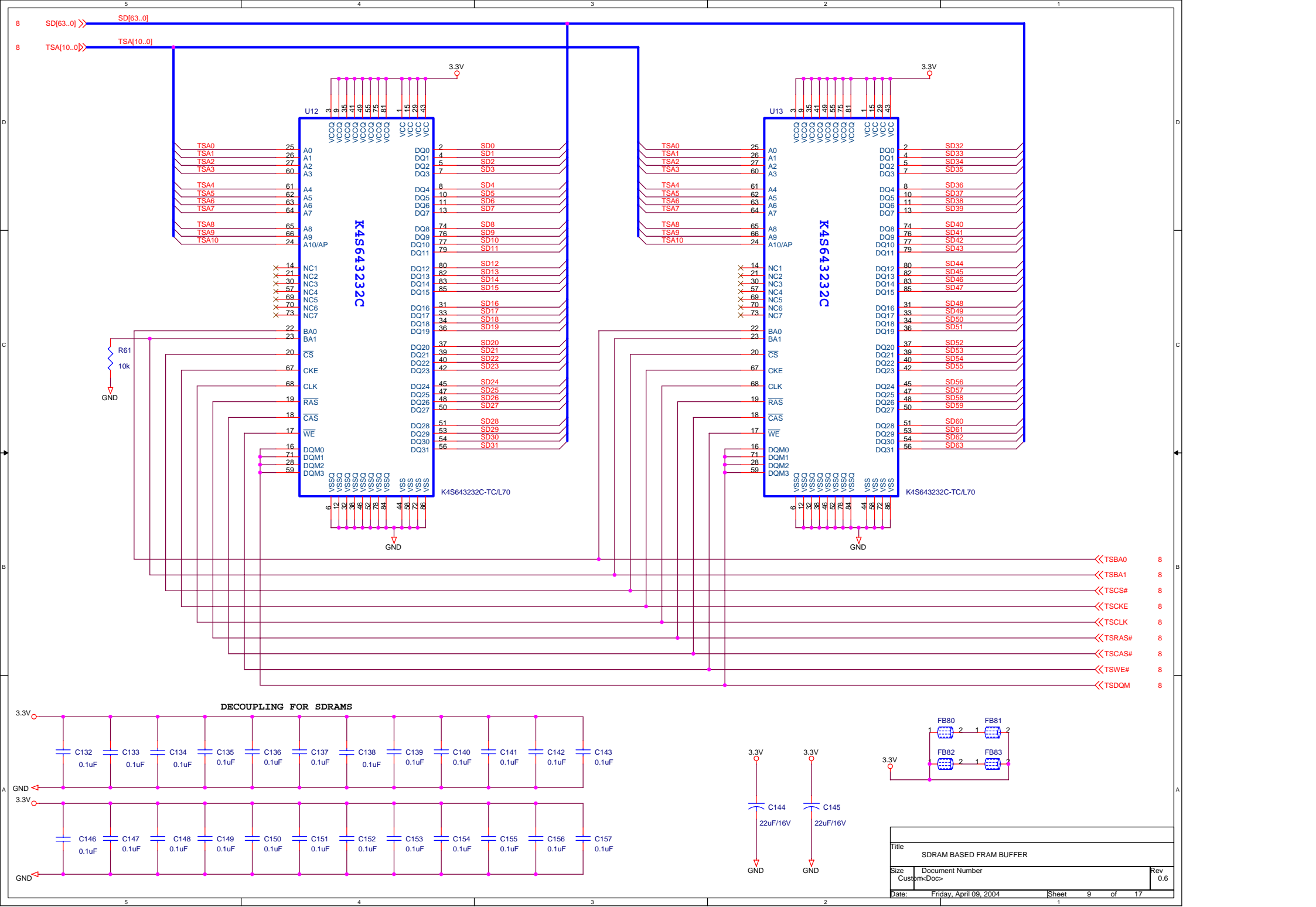


Title			JAGUAR ANALOG INTERFACE		
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Custom	Doc>				
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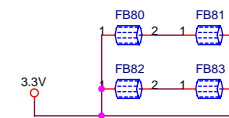
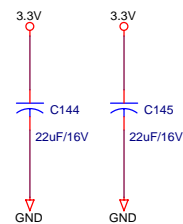
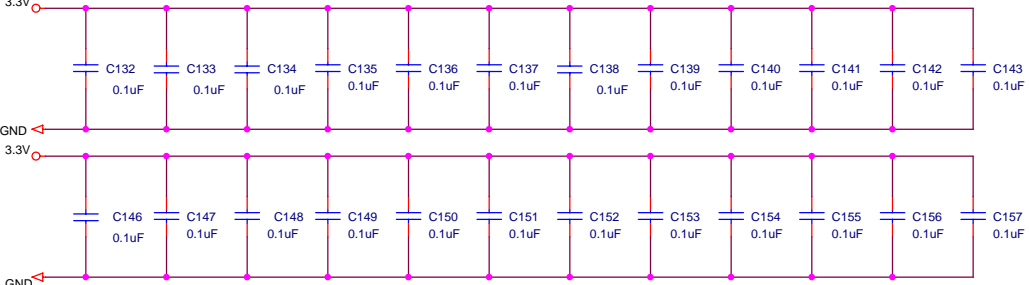


Title		
JAG SARAM INTERFACE		
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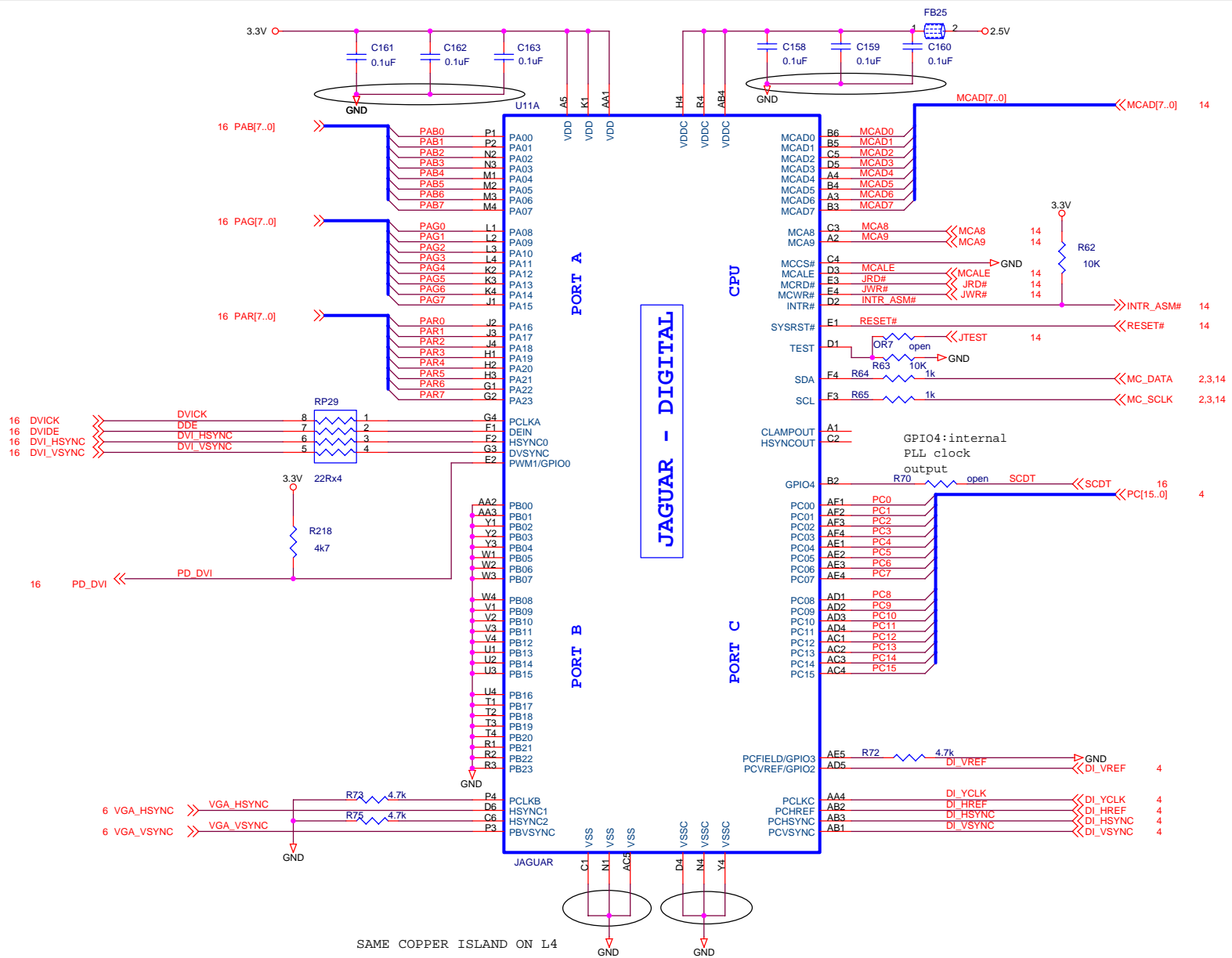




**DECOUPLING FOR SDRAMs**

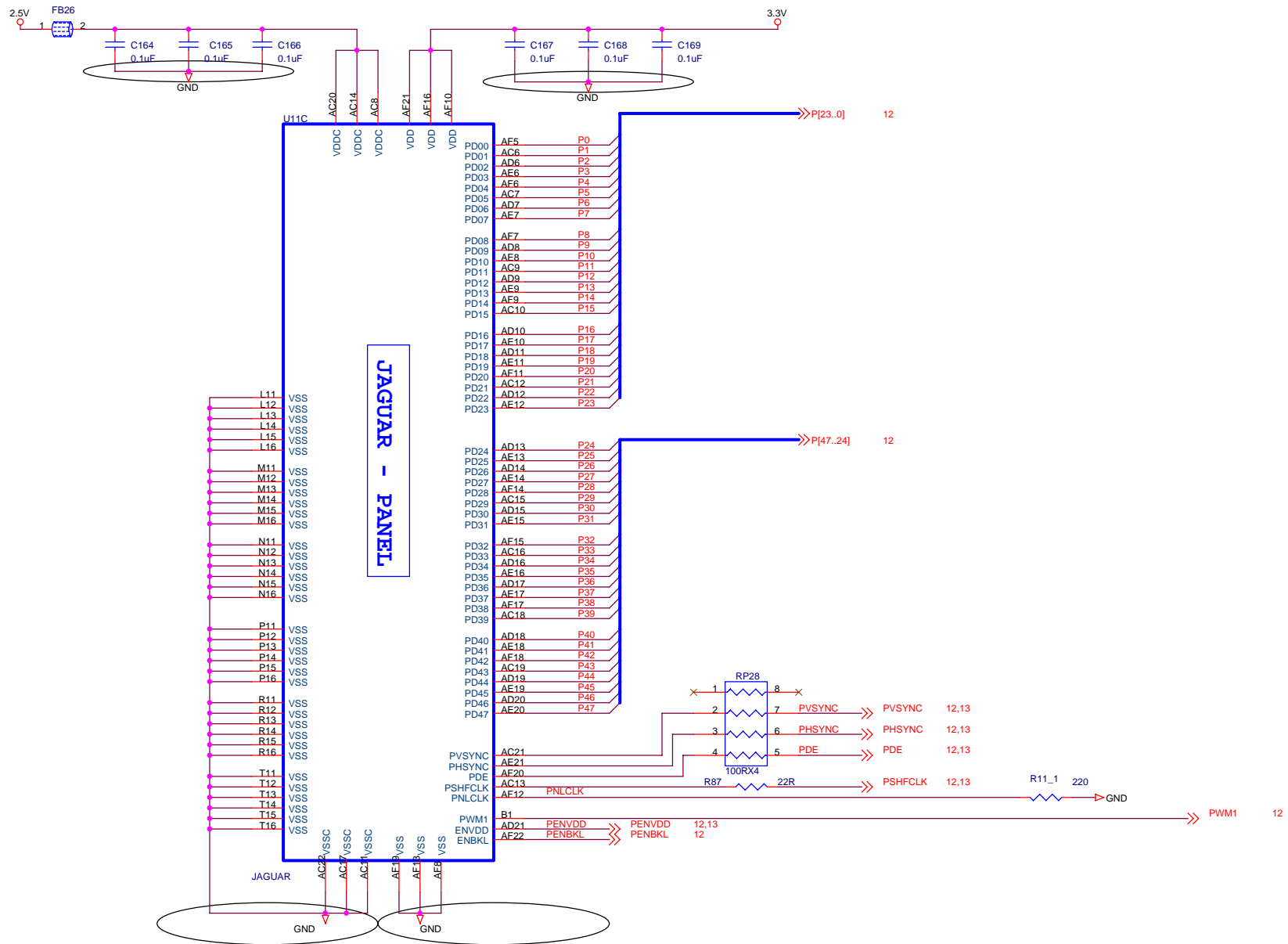


Title		
SDRAM BASED FRAM BUFFER		
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Custom:Doc>		0.6
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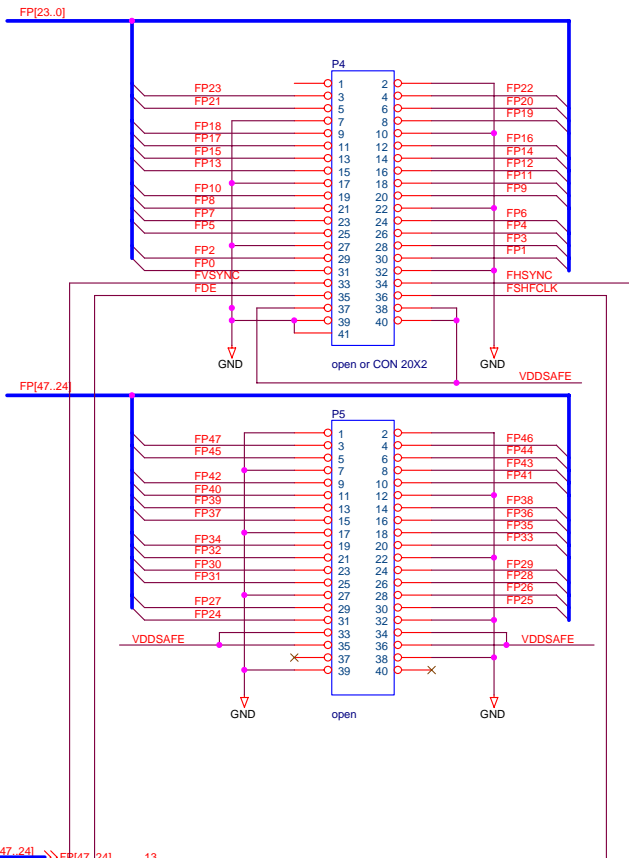
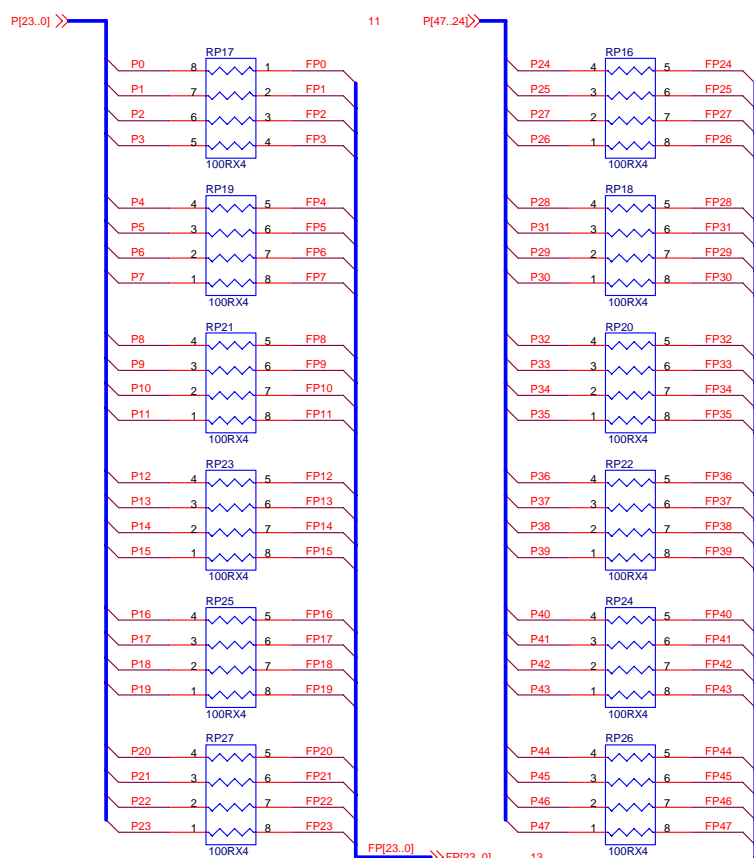


SAME COPPER ISLAND ON L4

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JAG DIGITAL INTERFACE		
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Title JAG PANEL INTERFACE		
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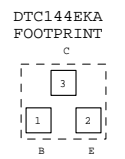
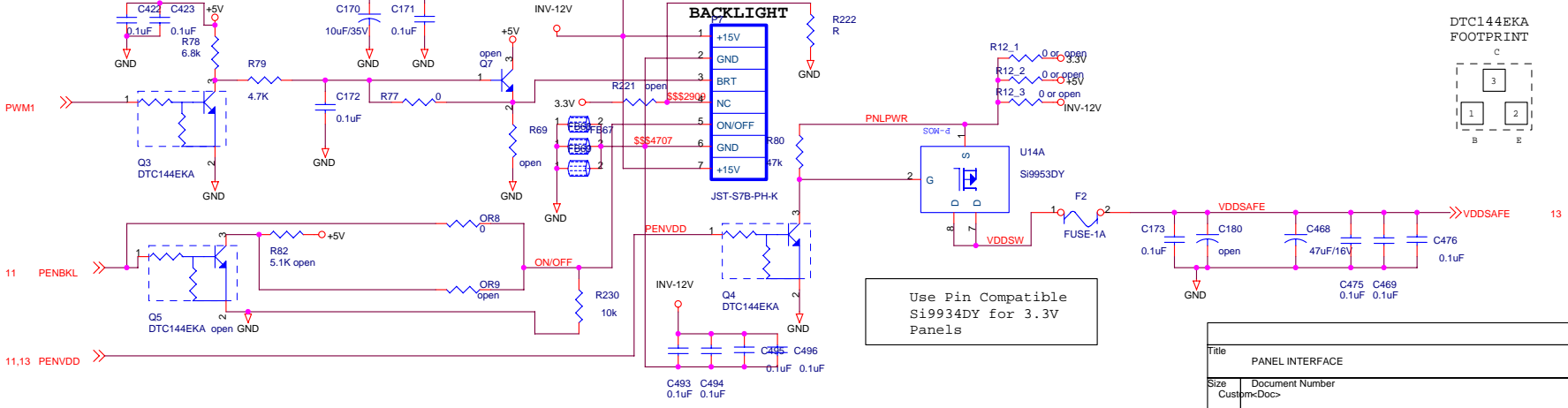
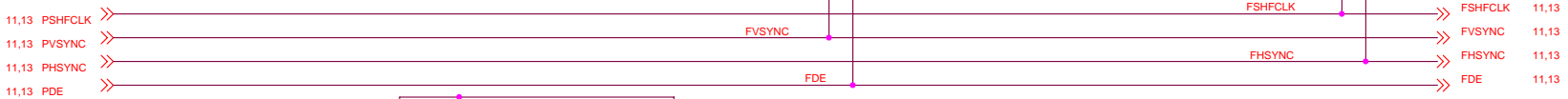


OUTPUT IS 24BIT :

- P0-P7(B0-B7)
- P8-P15(G0-G7)
- P16-P23(R0-R7)

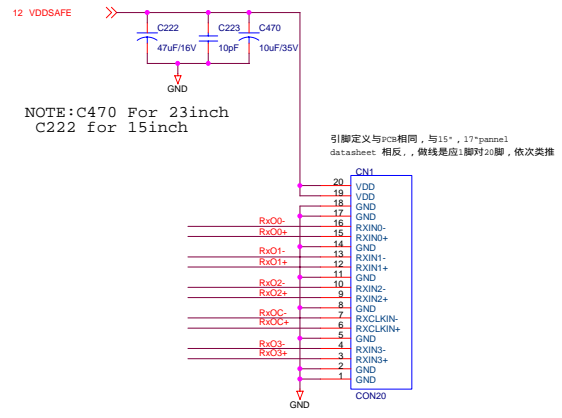
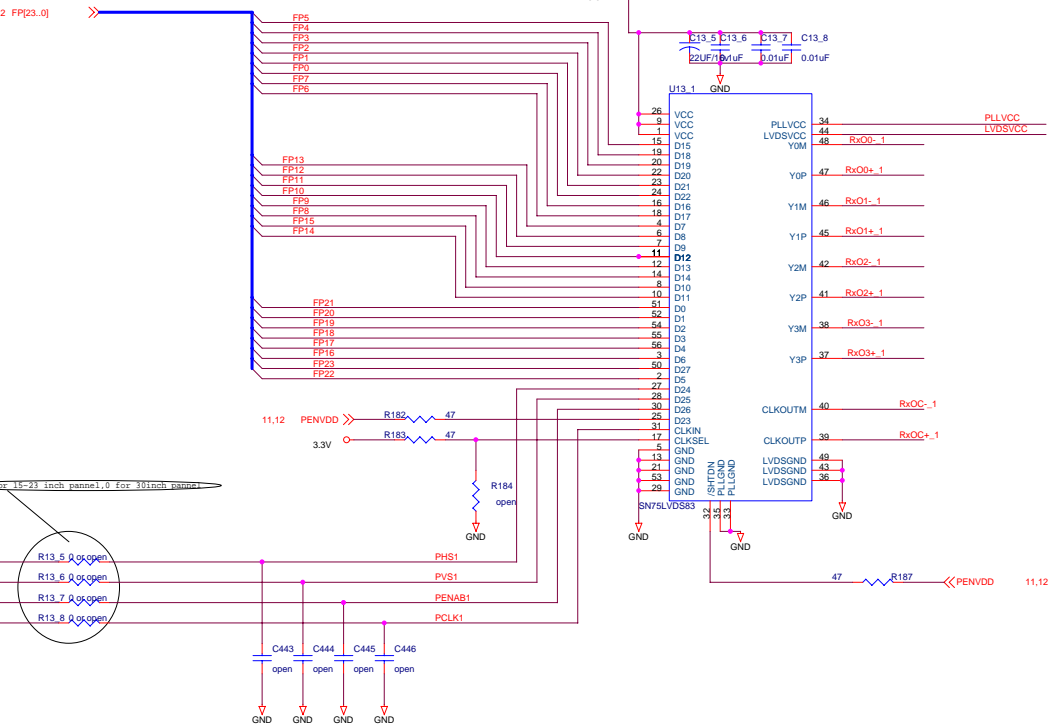
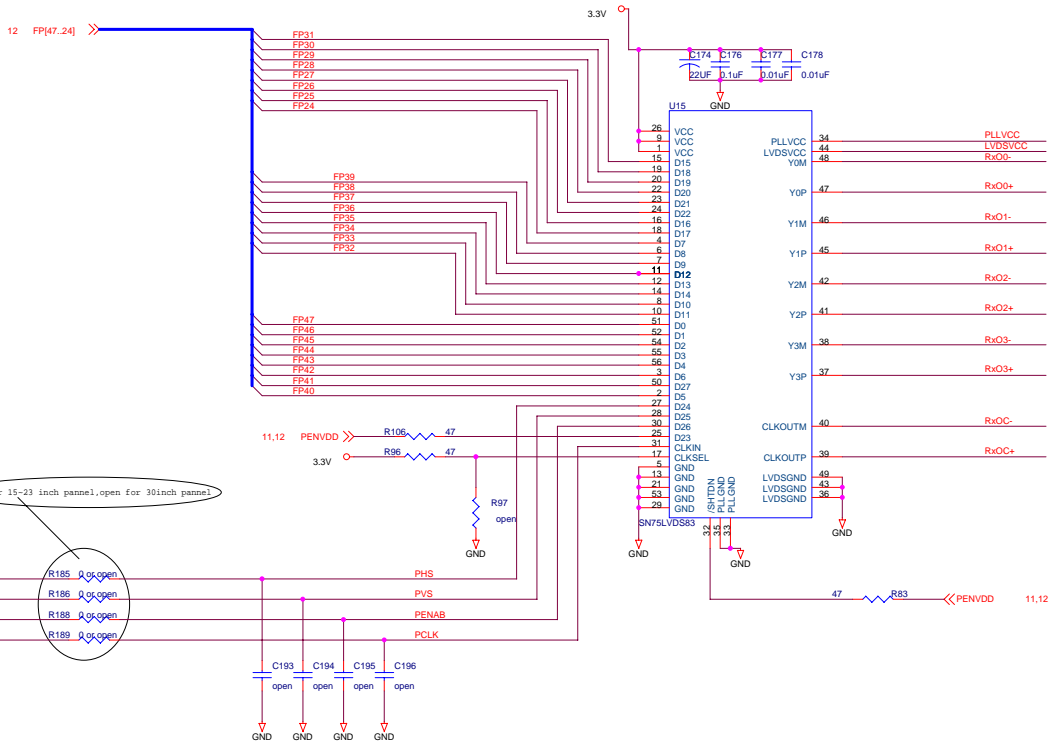
OUTPUT IS 48BIT :

- P0-P7(FB0-FB7)
- P8-P15(SB0-SB7)
- P16-P23(PB0-PB7)
- P24-P31(SG0-SG7)
- P32-P39(PRO-PR7)
- P40-P47(SR0-SR7)

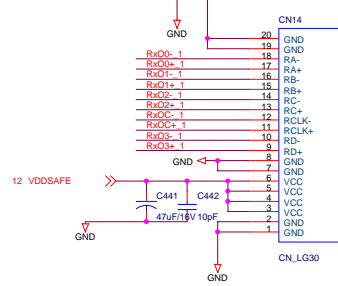
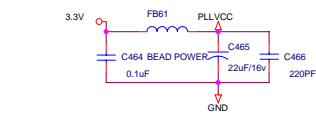
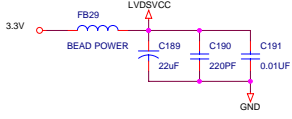
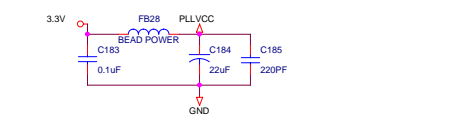


Use Pin Compatible Si9934DY for 3.3V Panels

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Custom:Doc<		12	0.6
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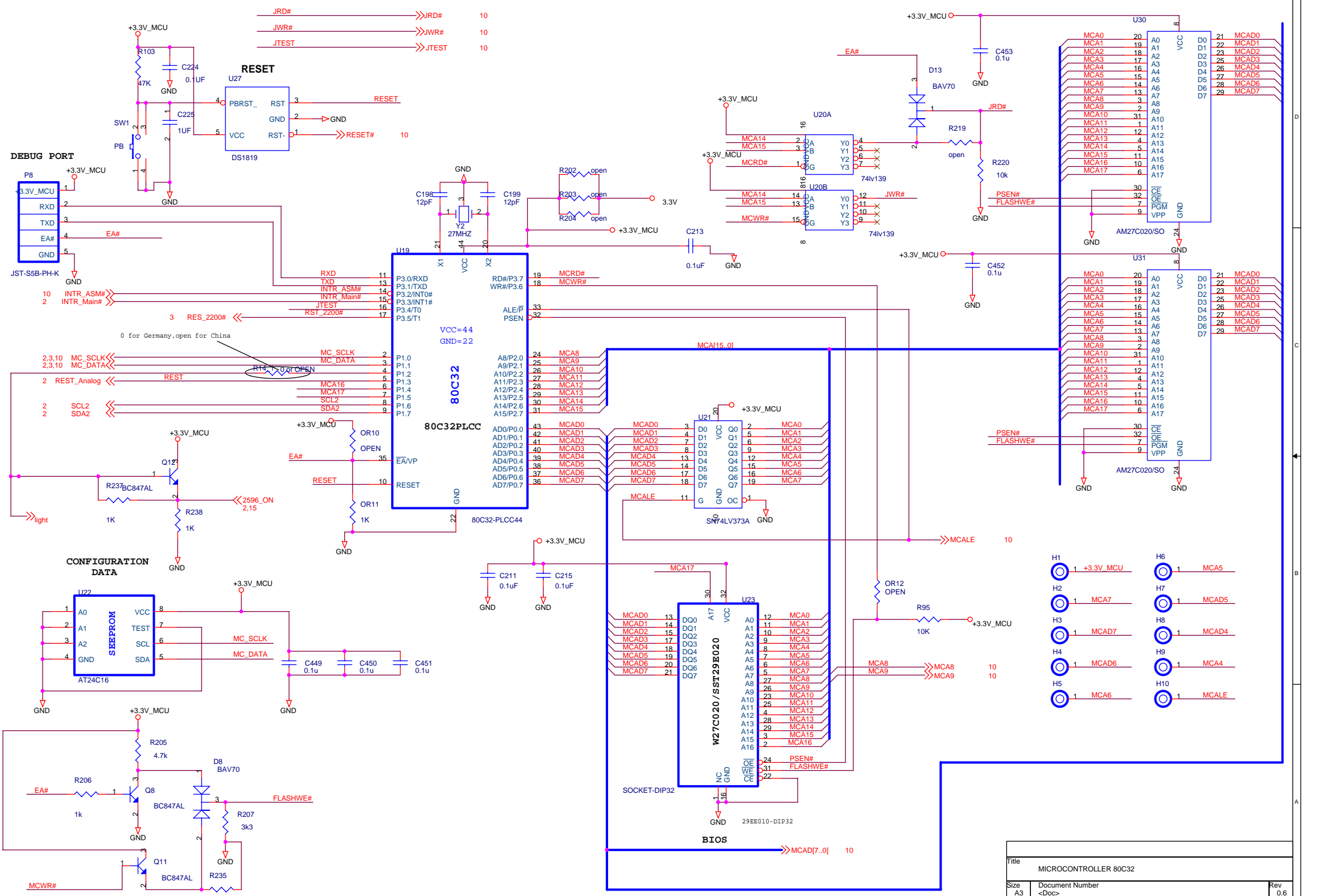


GND3 CONNECT TO LCD'S METAL FRAME

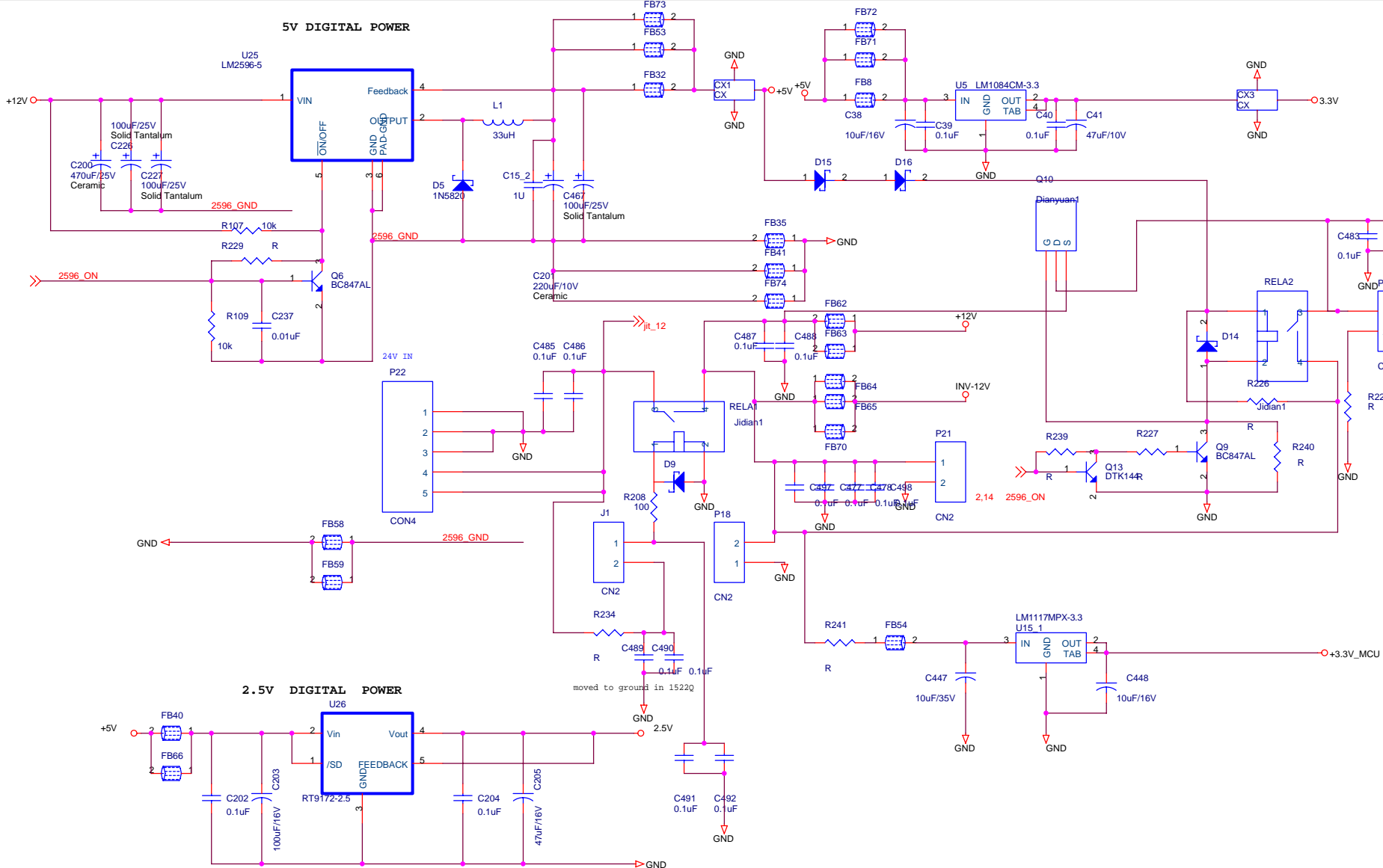


GND3 CONNECT TO LCD'S METAL FRAME

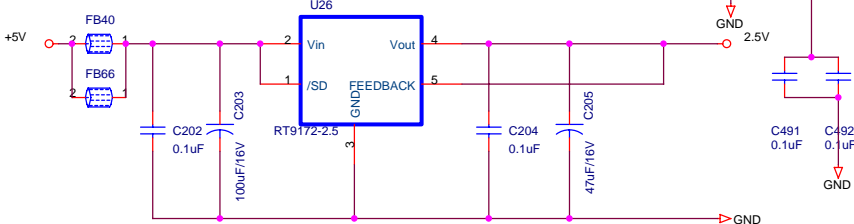
Title			
LVDS PANEL			
Size	Document Number		Rev
C	<Doc>		<Rev Code>
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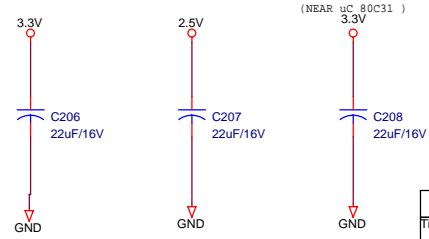
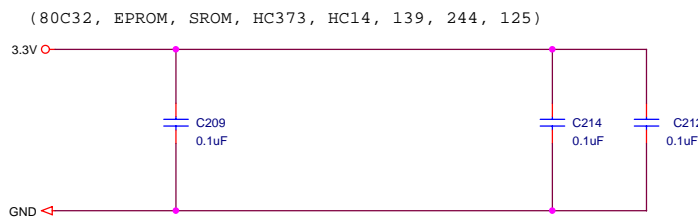
Title		
MICROCONTROLLER 80C32		
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A3	<Doc>	0.6
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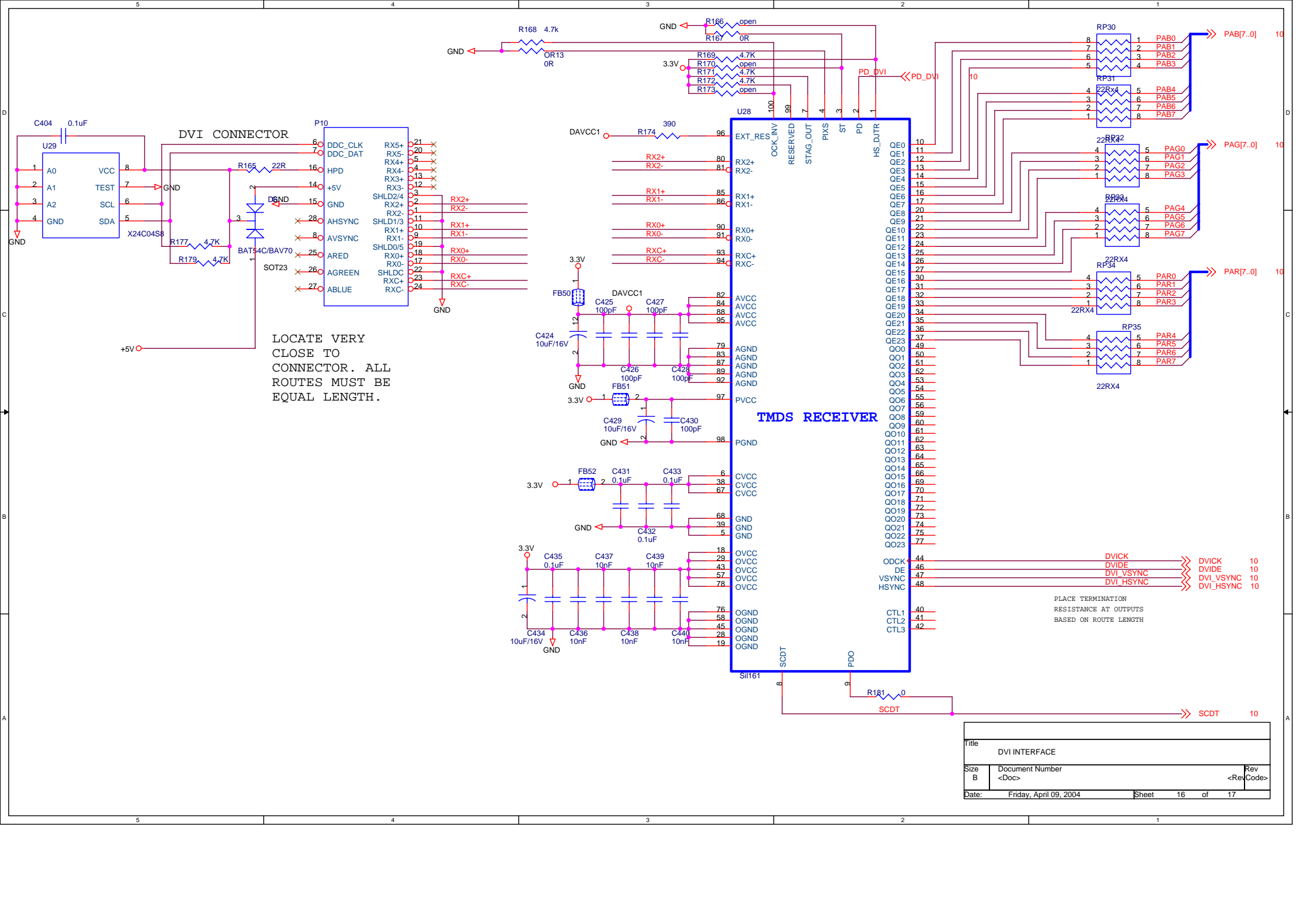
**2.5V DIGITAL POWER**



**DECOUPLING FOR MISC ICs**



Title		
POWER AND DECAP		
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Custom:Doc>		0.6
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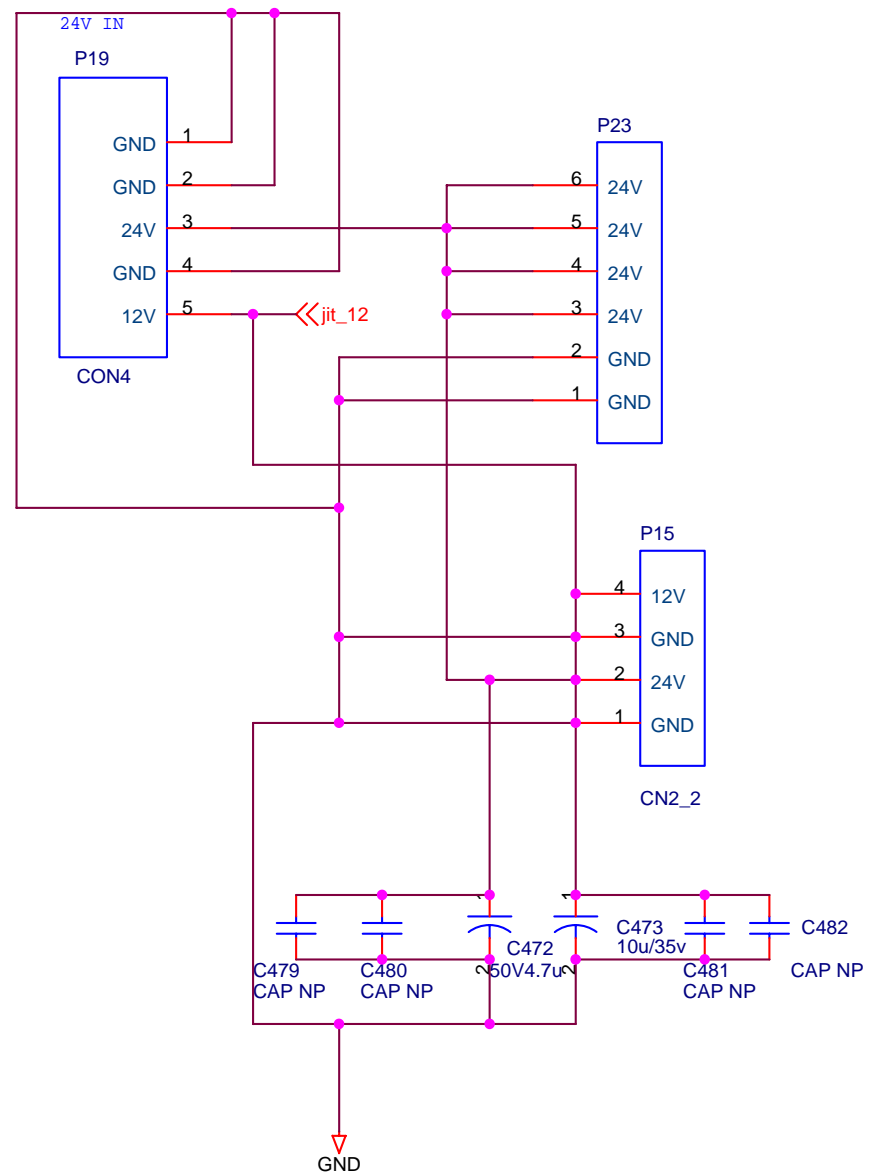
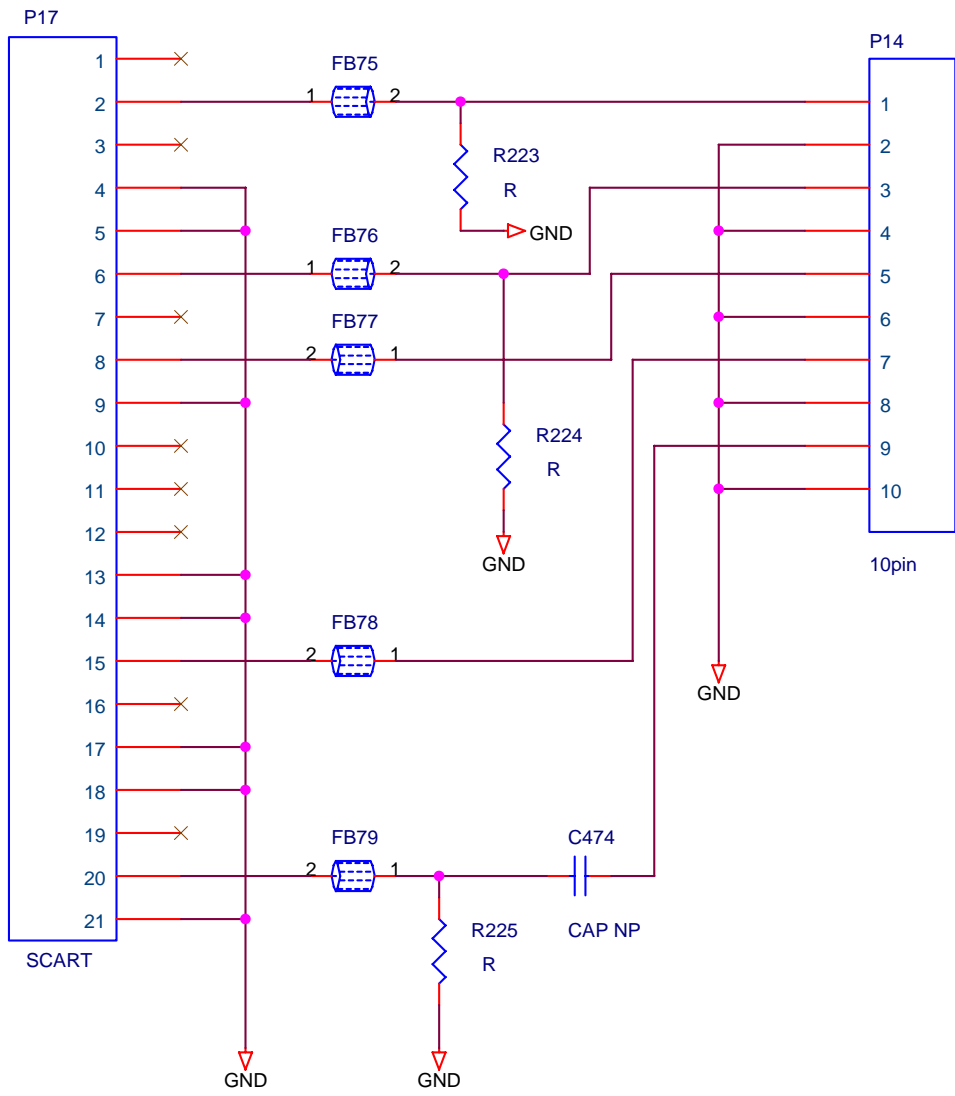
LOCATE VERY  
CLOSE TO  
CONNECTOR. ALL  
ROUTES MUST BE  
EQUAL LENGTH.

**TMDS RECEIVER**

PLACE TERMINATION  
RESISTANCE AT OUTPUTS  
BASED ON ROUTE LENGTH

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DVI INTERFACE		
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SCART CONNECT		
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	2	1