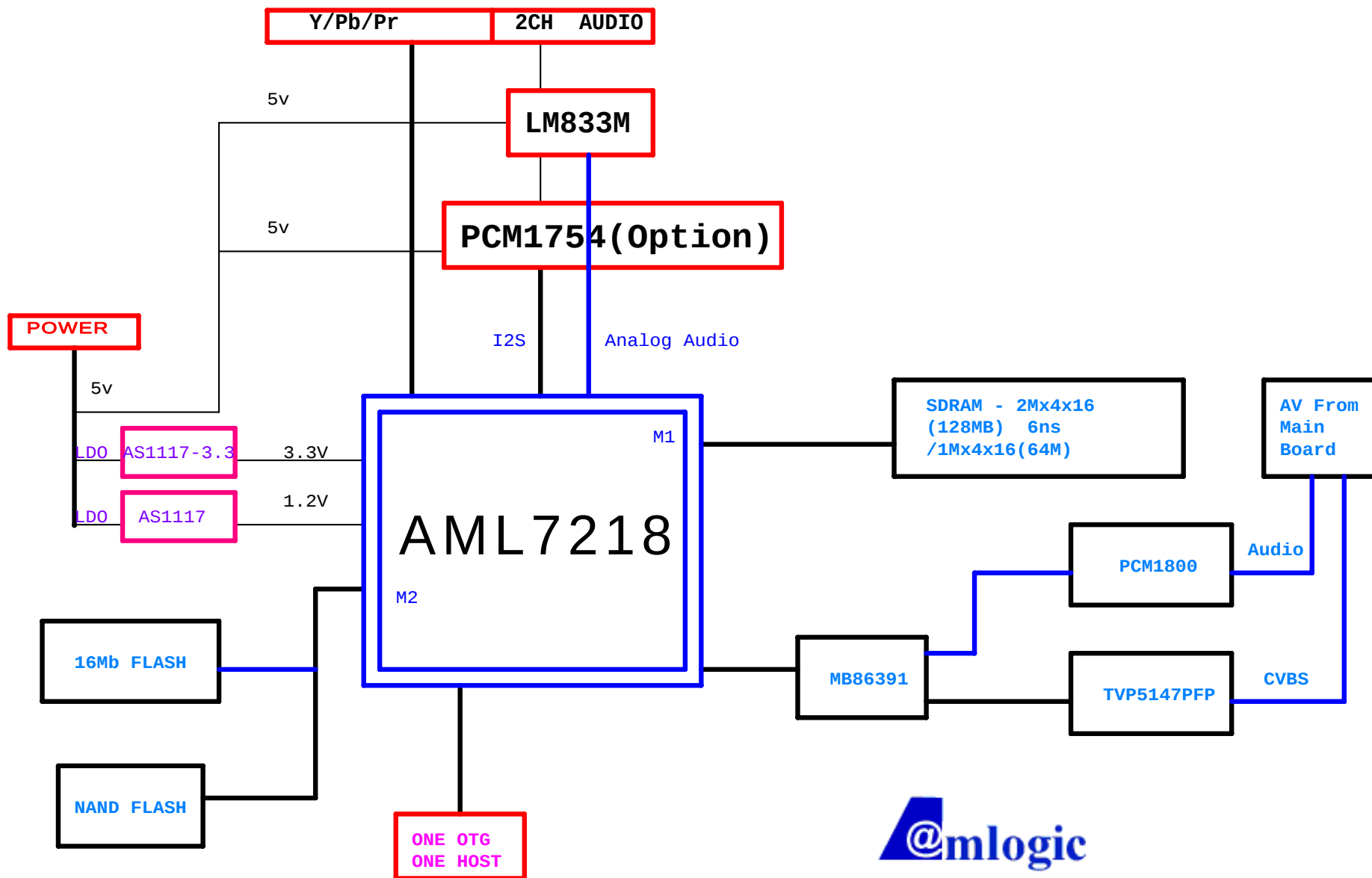




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Con1
Connect to
MainMoard
CON26

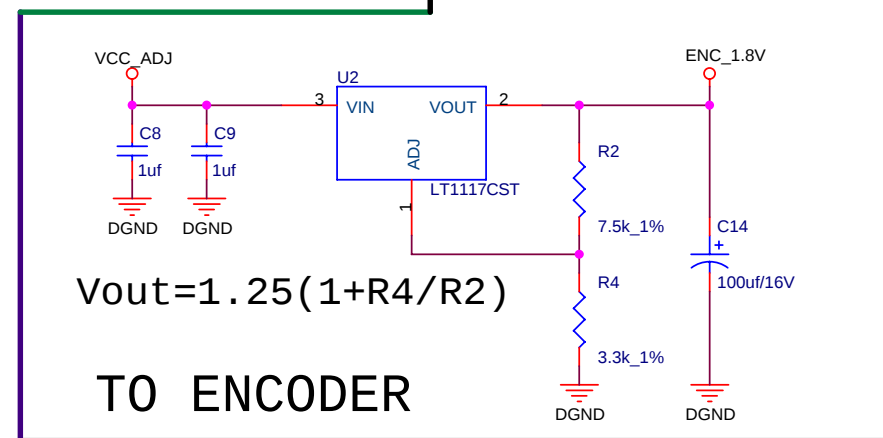
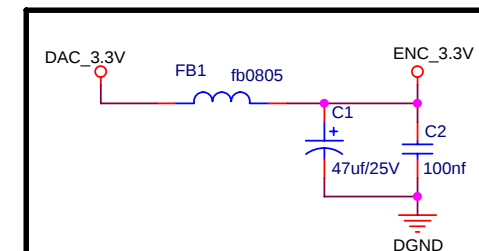
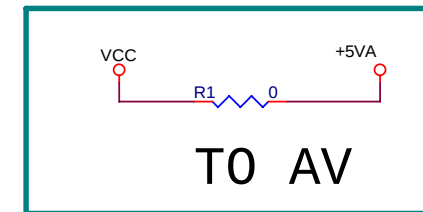
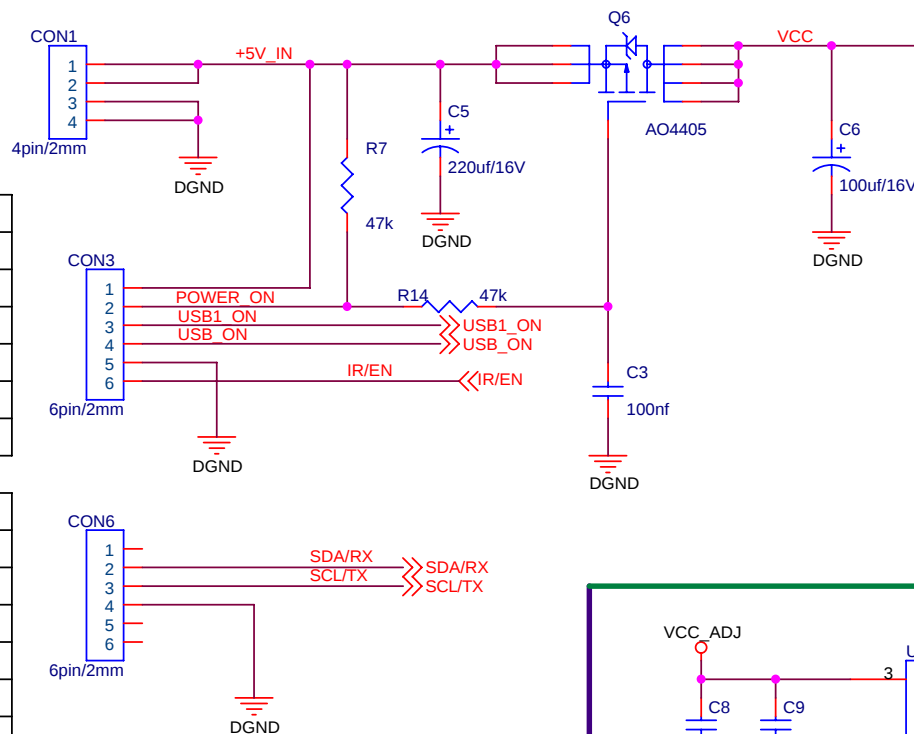
Pin	Description
1	5V
2	5V
3	GND
4	GND

Con3
Connect to
MainMoard
CON5

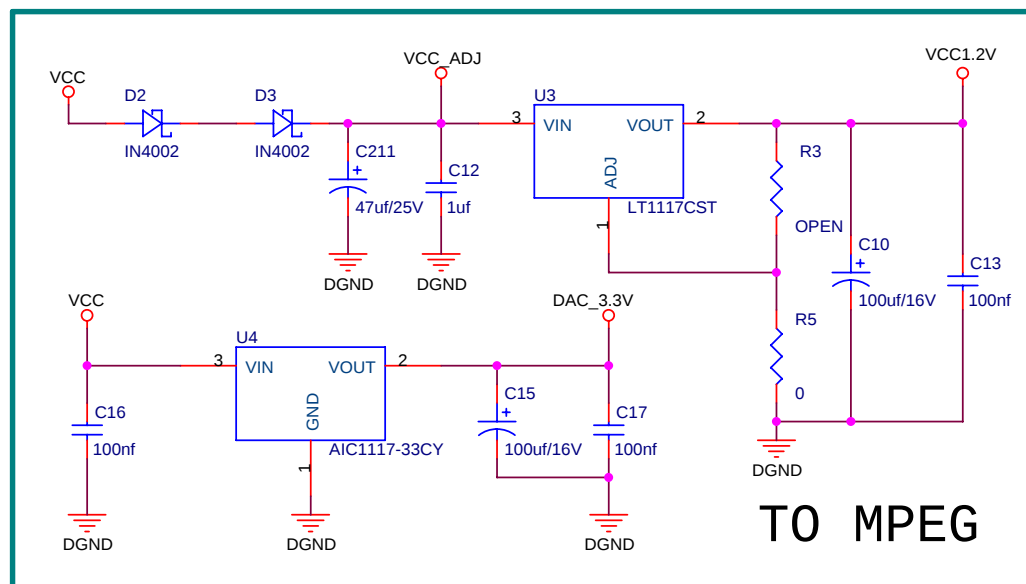
Pin	Description
1	5V
2	Power On/Off
3	USB1 On/Off
4	USB On/Off
5	GND
6	IR-IN

Con6
Connect to
MainMoard
CON29

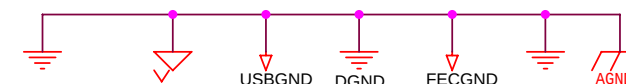
Pin	Description
1	WP
2	SDA
3	SCL
4	GND
5	5V_stb
6	POWER On/Off1



$V_{out} = 1.25(1 + R4/R2)$
TO ENCODER



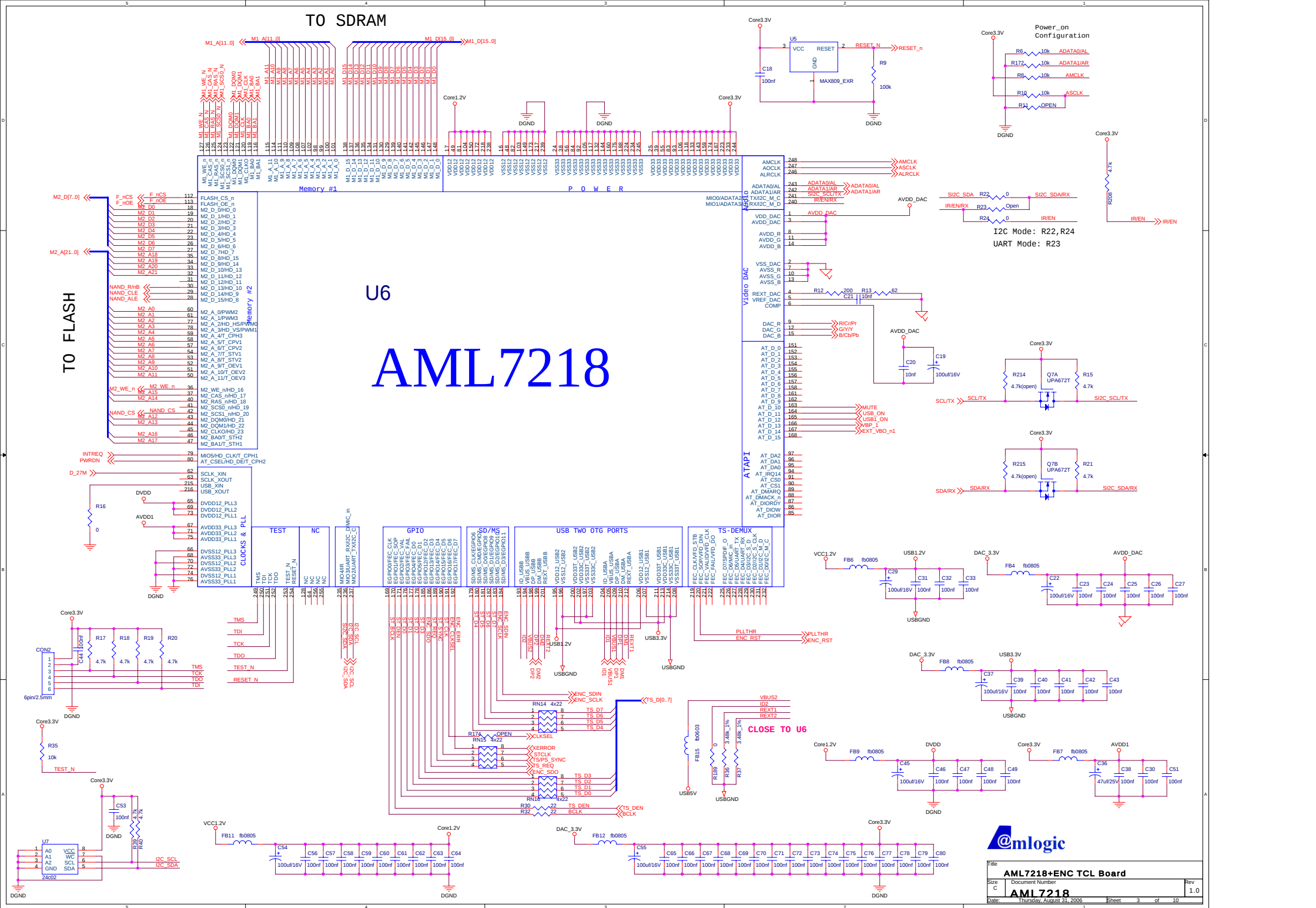
TO MPEG

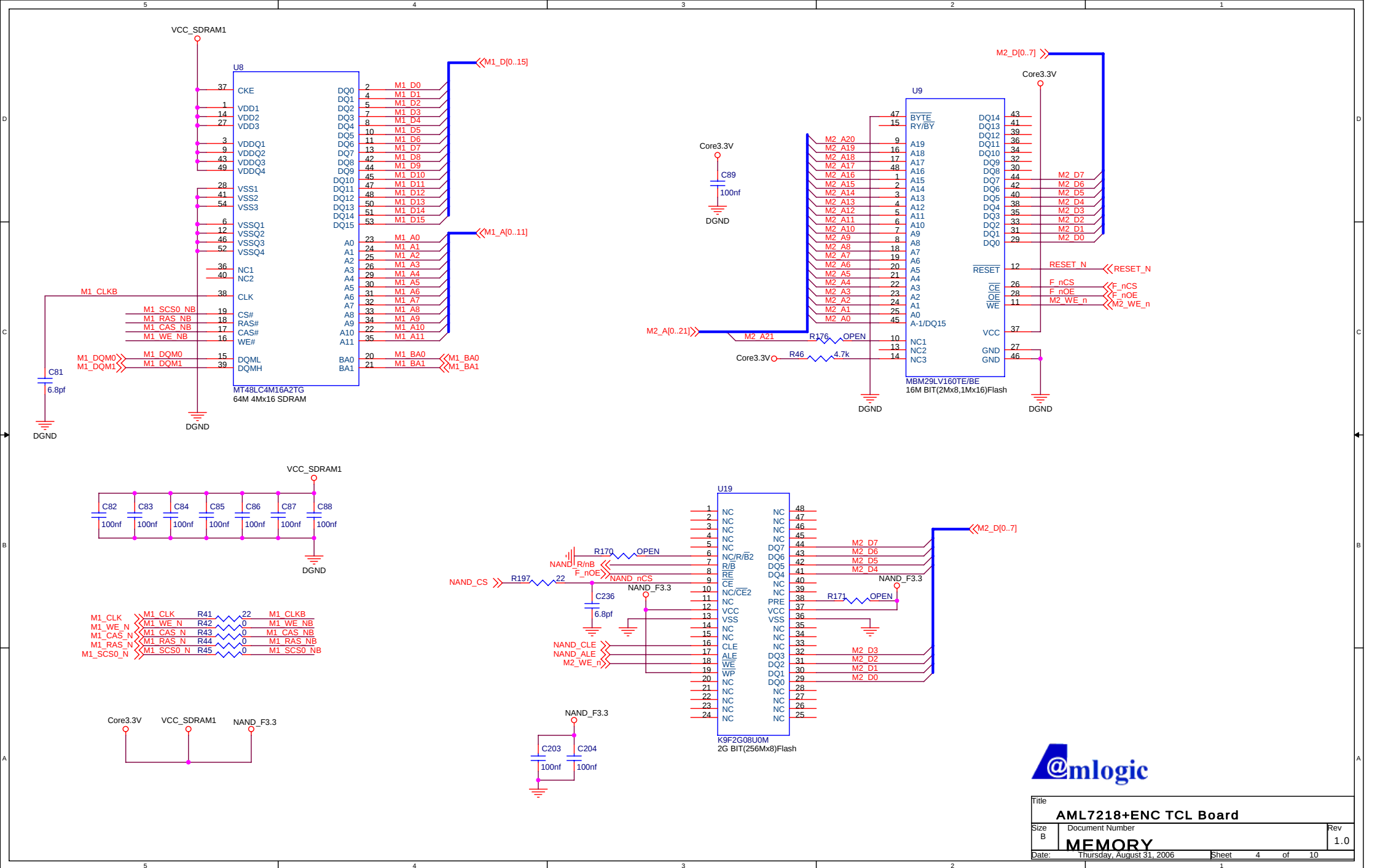


@mlogic

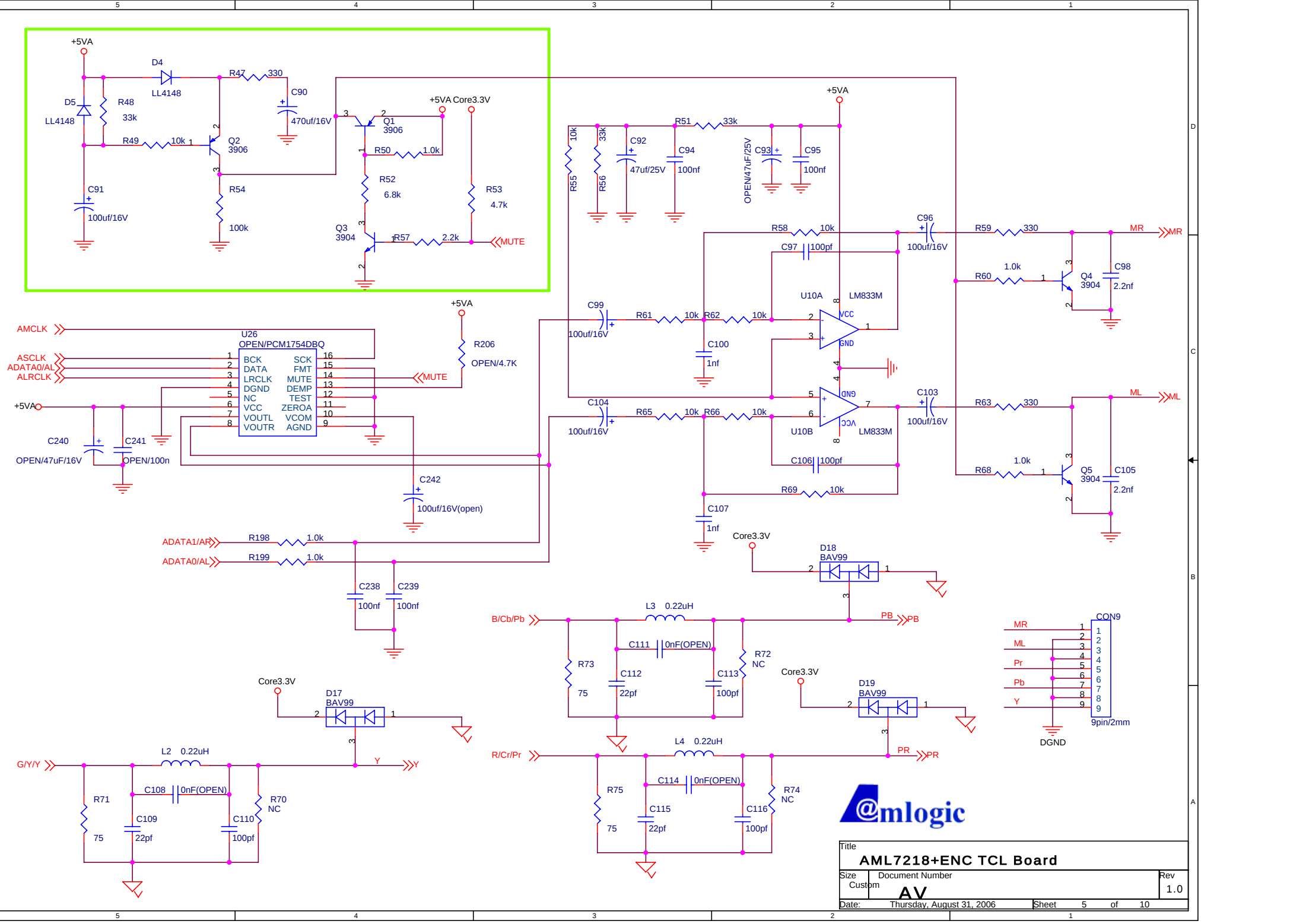
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TO SDRAM



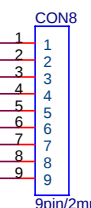
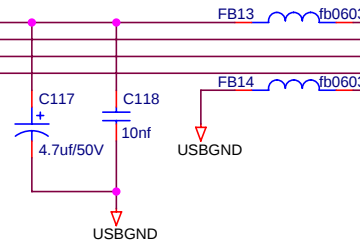


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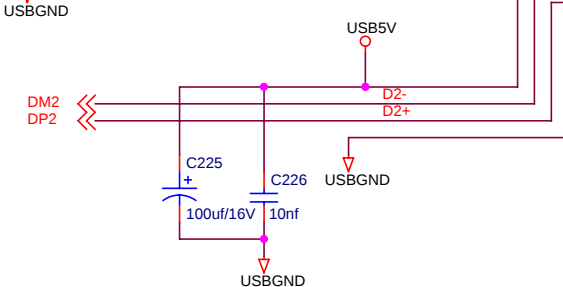
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VBUS1
DM1
DP1
ID1



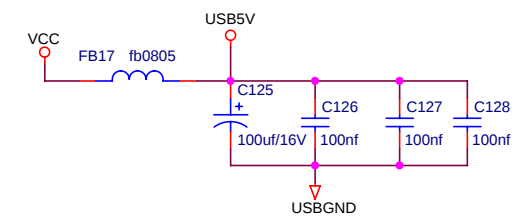
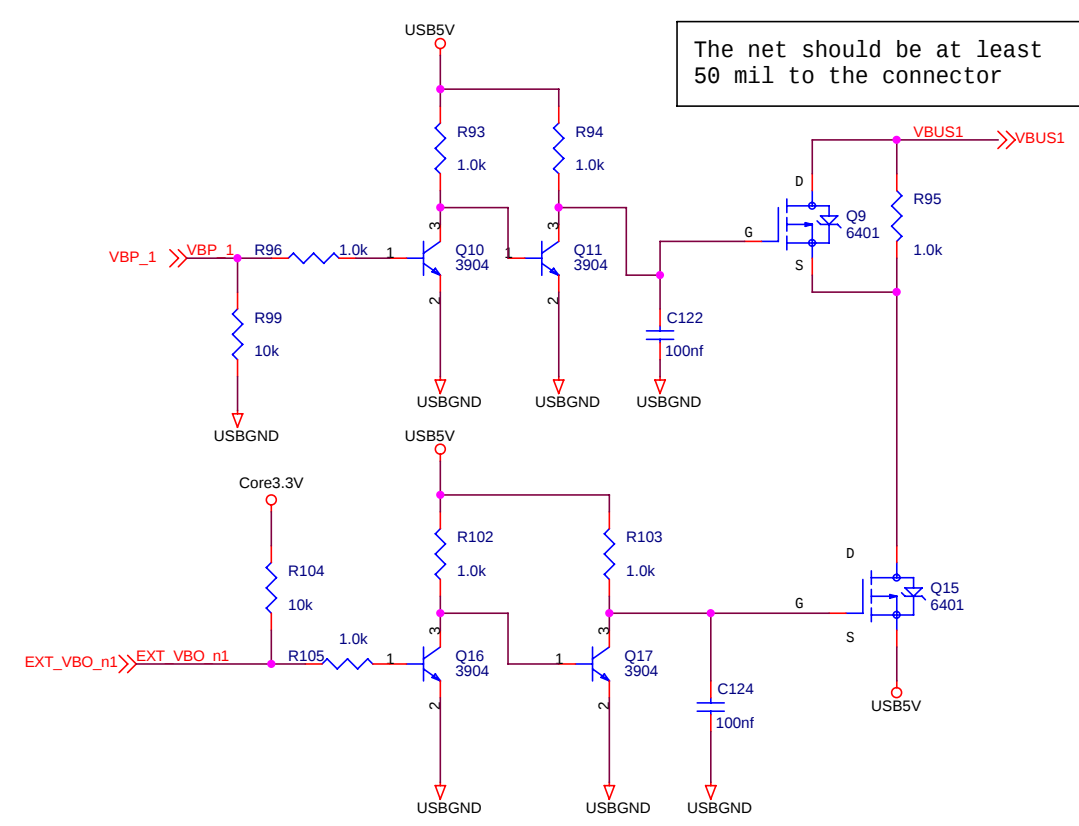
Con8
Connect to
MainMoard
CON5

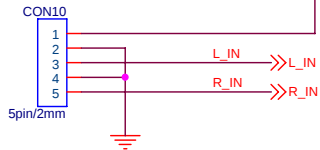
- 1.Keep the trace D- & D+ same length
- 2.Maintain parallelism between D- & D+
- 3.Do not route D+ & D- over the power plane split
- 4.Do not route D+ & D- over the other high Fre. signals
- 5.It is preferred to route D+ & D- over the ground layer
- 6.It is preferred to route D+ & D- using single layer



Pin	Description	
1	USB1 OTG	VBUS1
2		D1-
3		D1+
4		ID1
5		GND
6	USB2 HOST	5V
7		D2-
8		D2+
9		GND

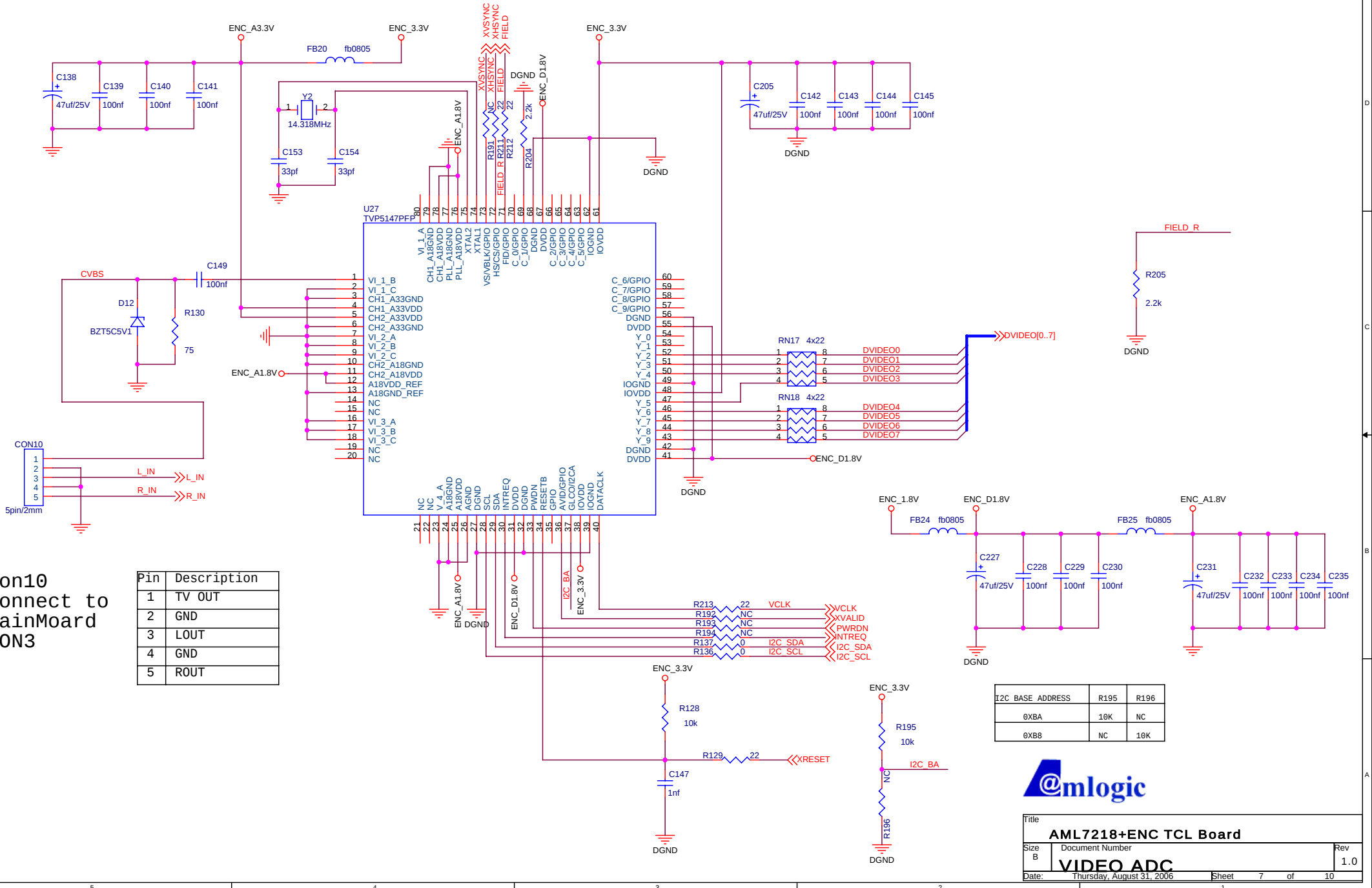
The net should be at least
50 mil to the connector





Con10
Connect to
MainBoard
CON3

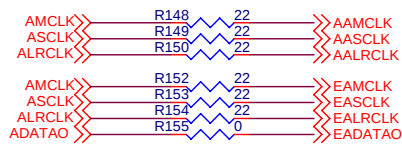
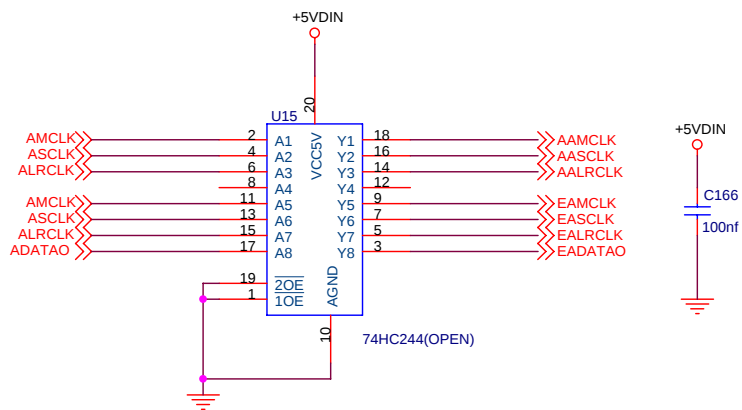
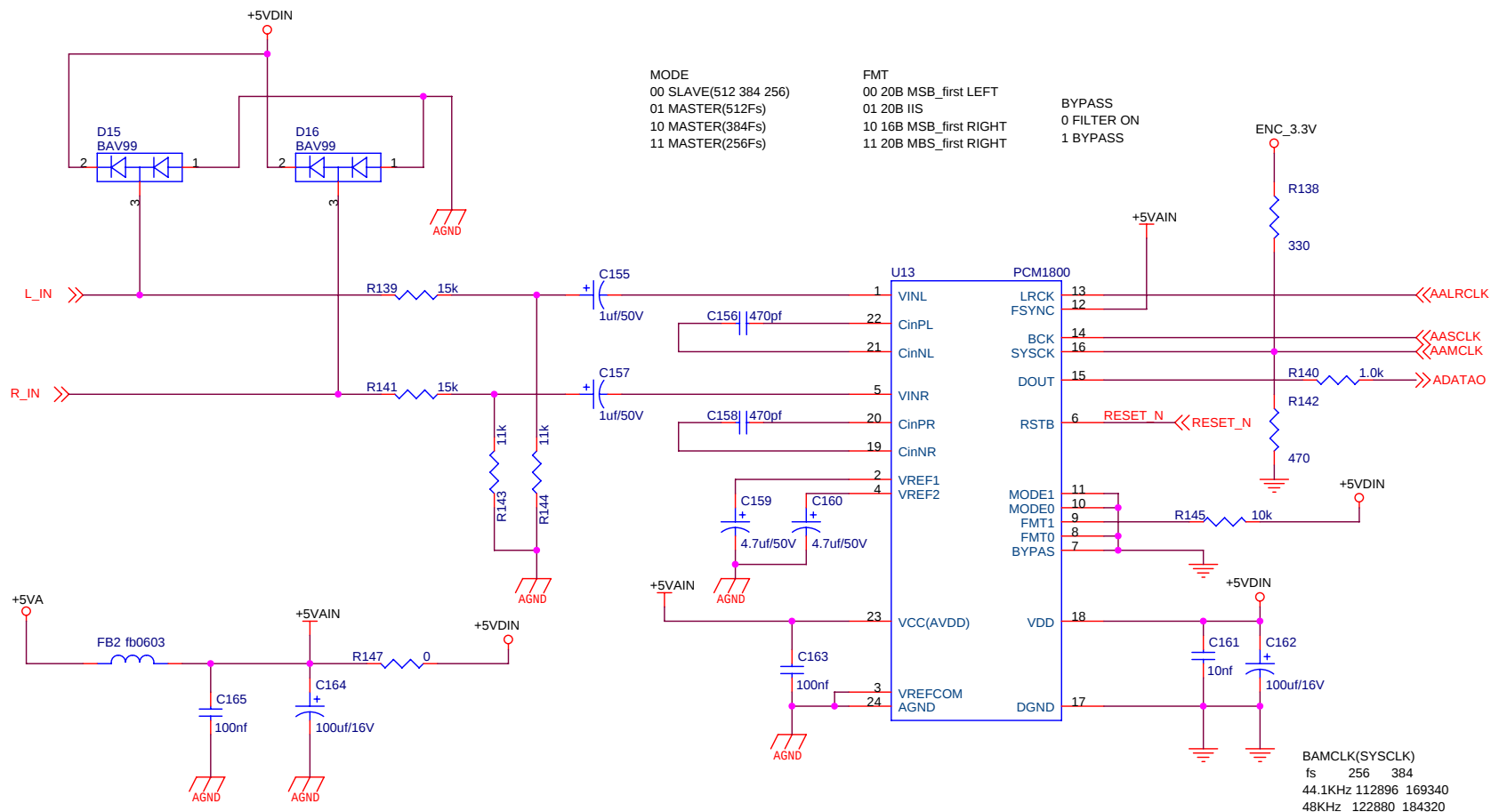
Pin	Description
1	TV OUT
2	GND
3	LOUT
4	GND
5	ROUT



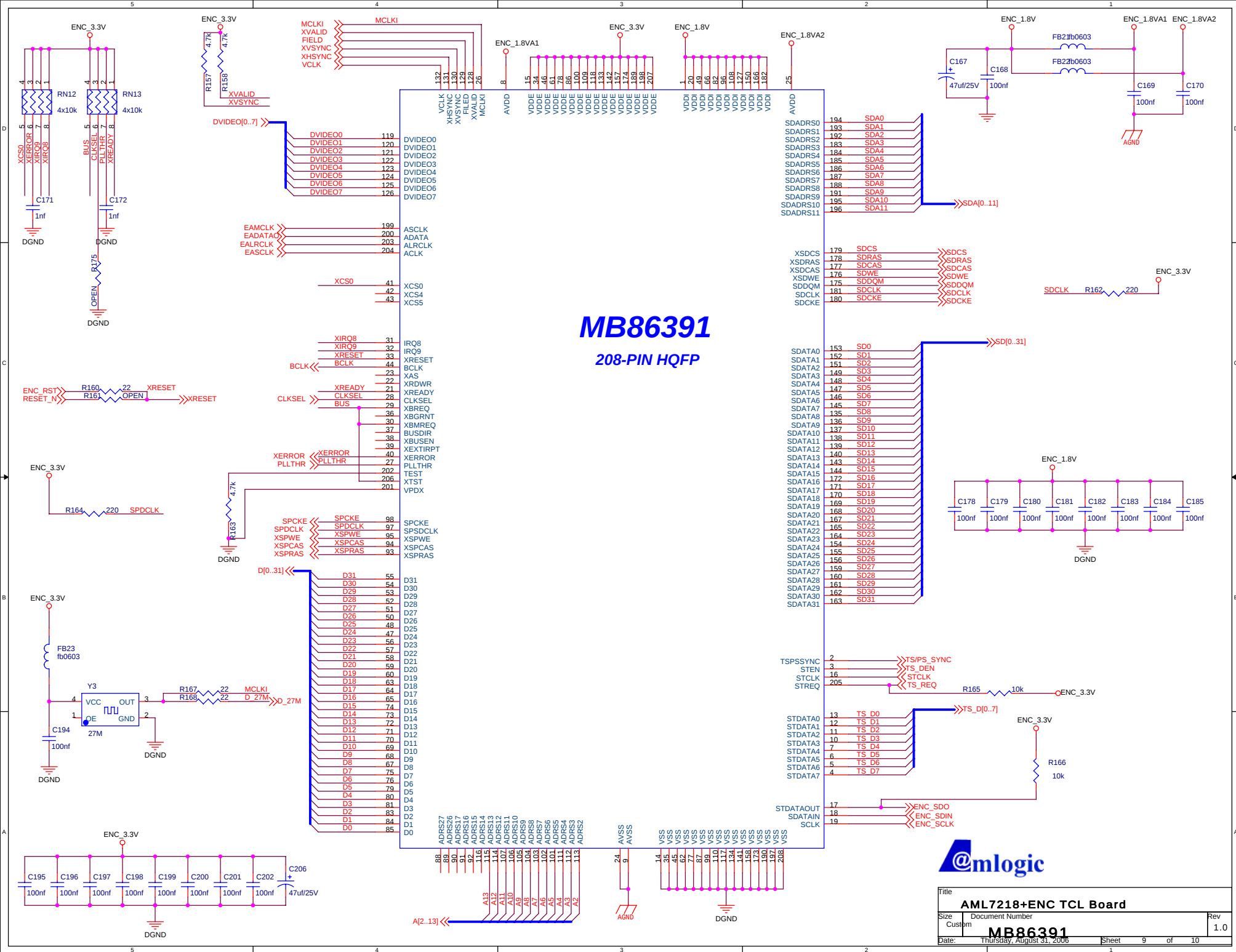
I2C BASE ADDRESS	R195	R196
0XB8	10K	NC
0XB8	NC	10K

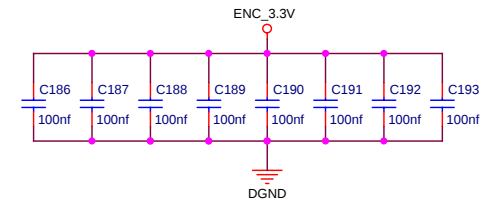
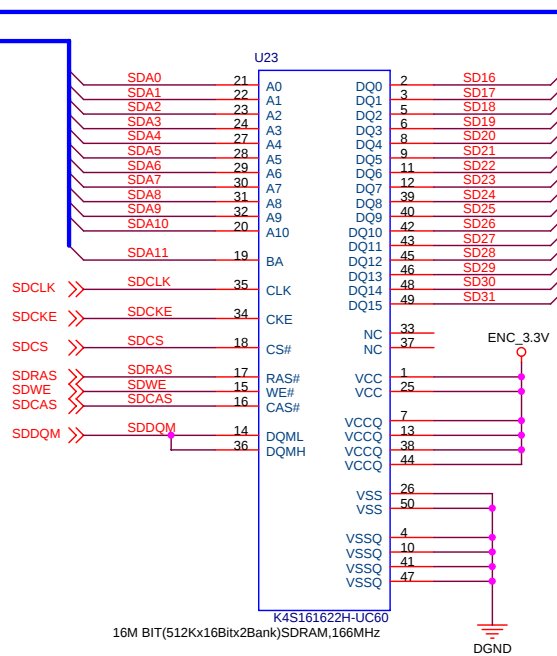
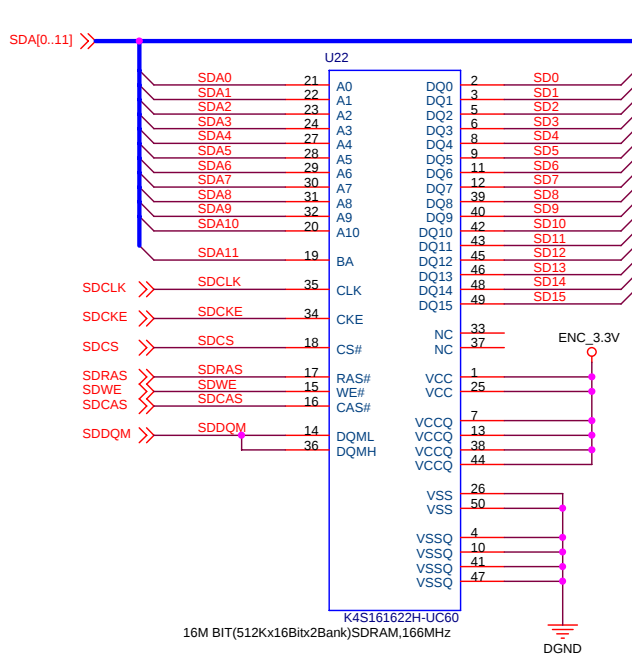
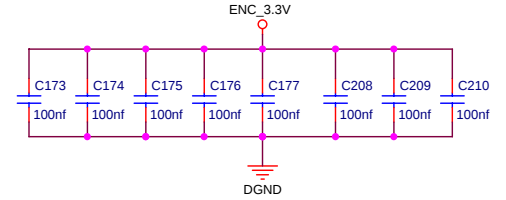
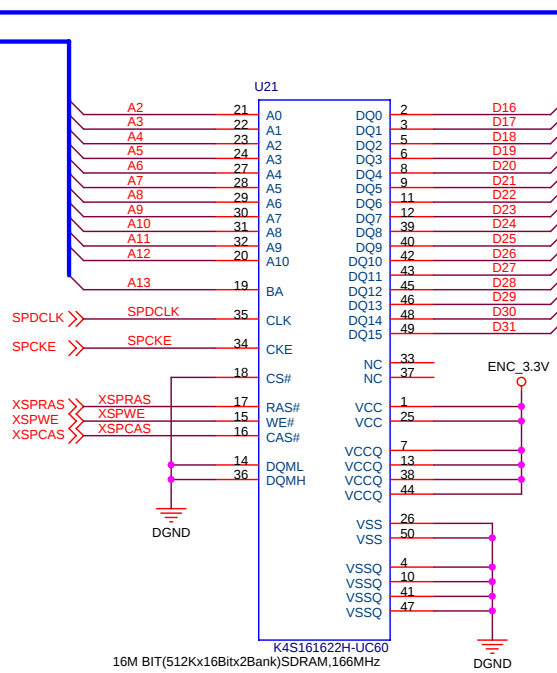
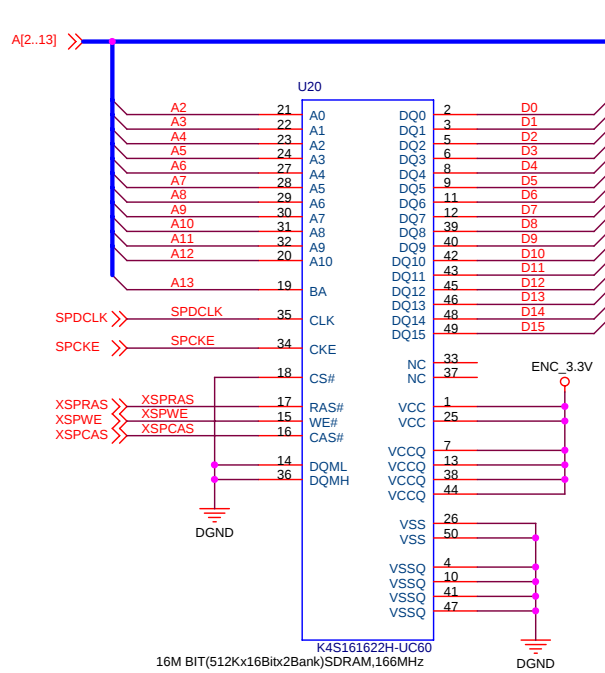


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