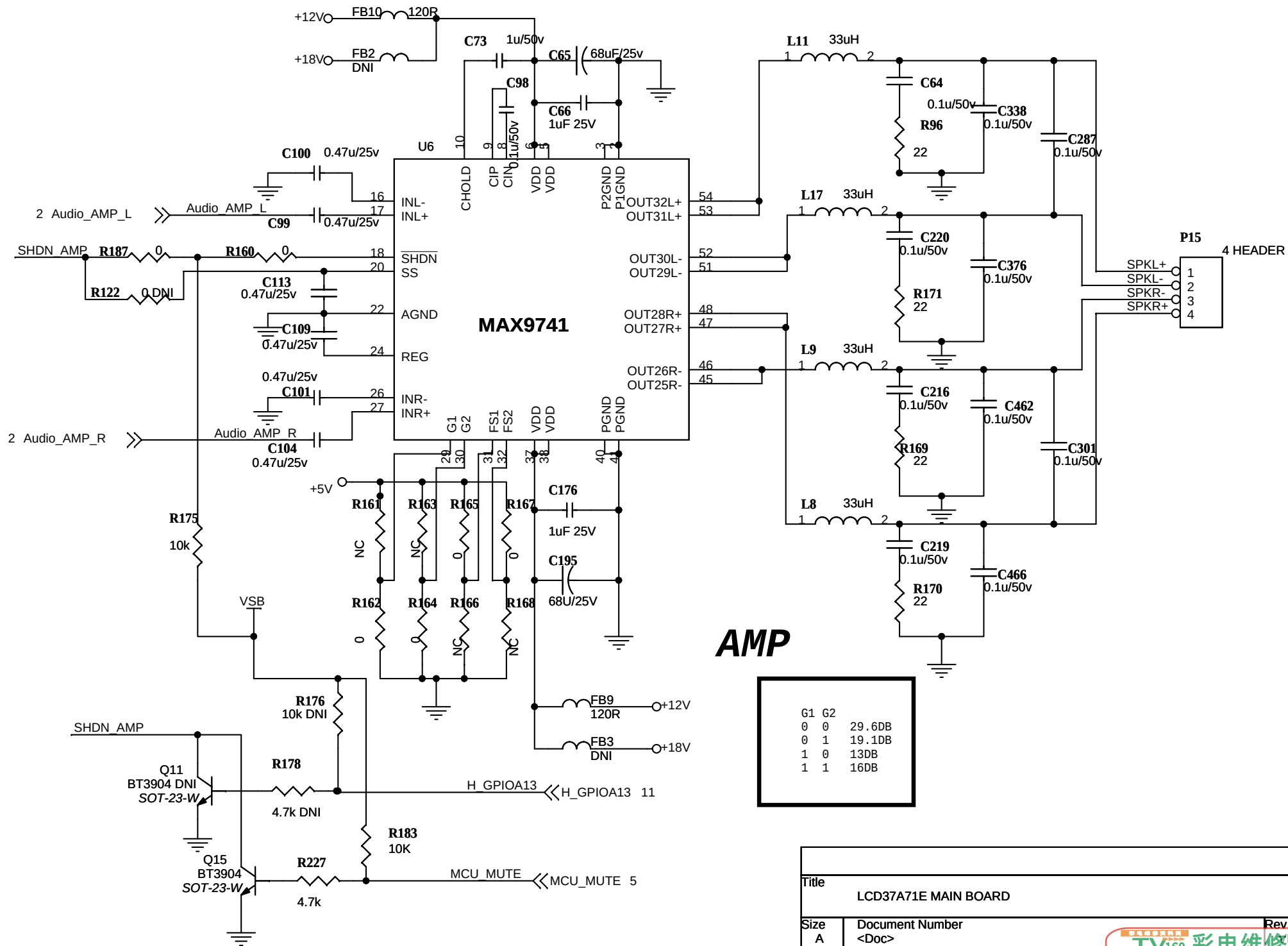
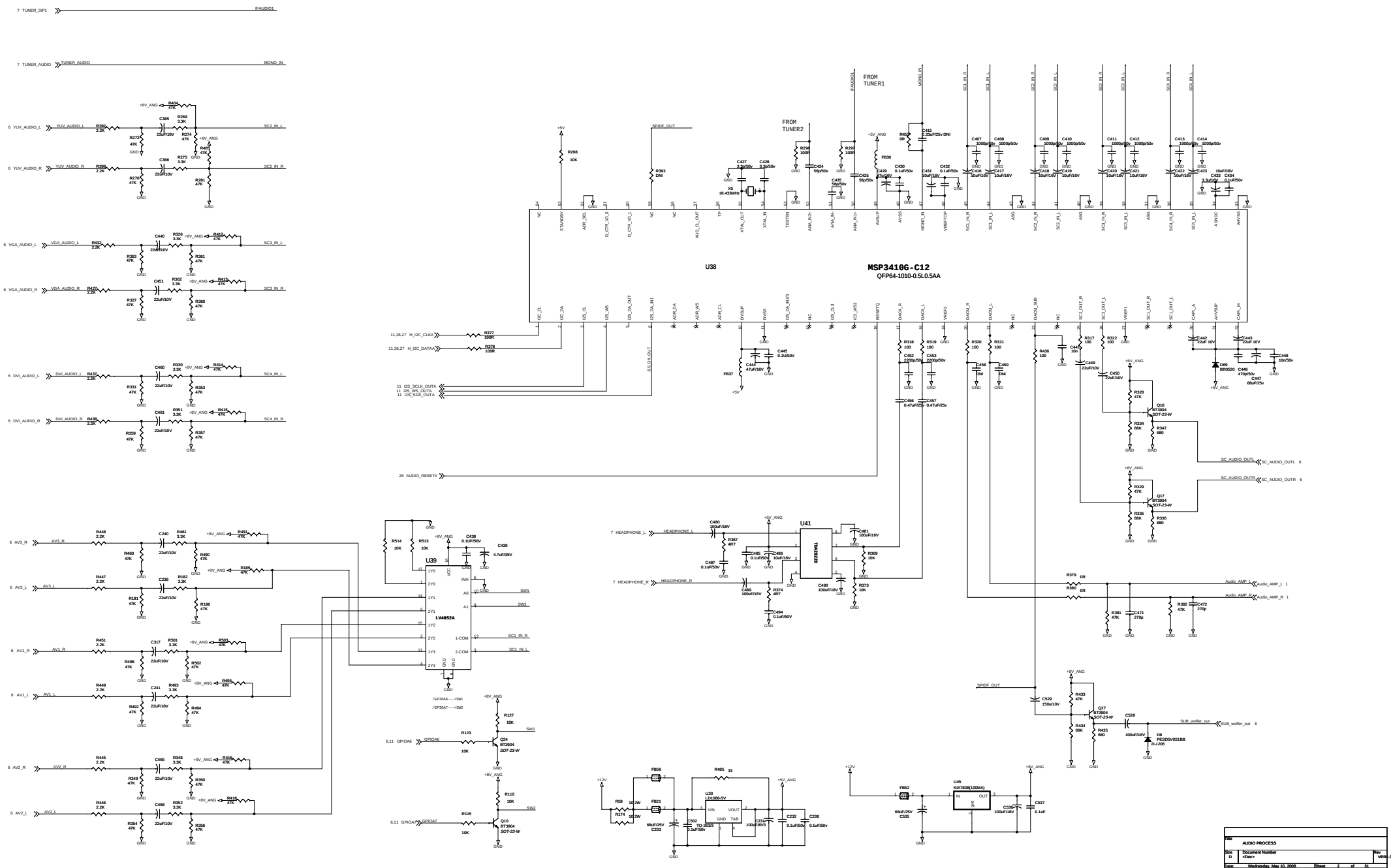




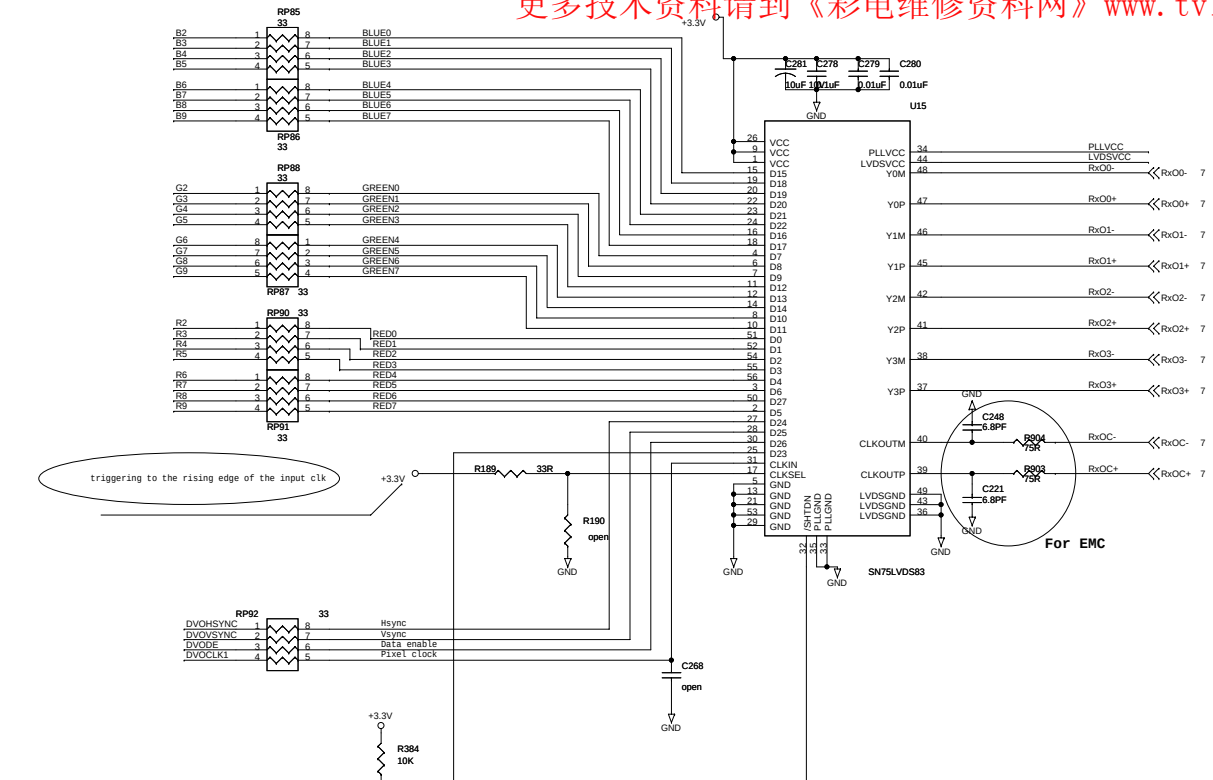
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0	1	19.1DB
1	0	13DB
1	1	16DB

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Size	Document Number	Rev	
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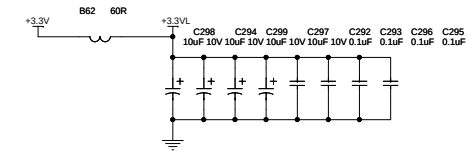
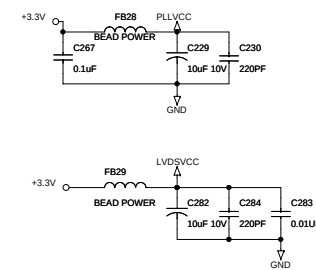
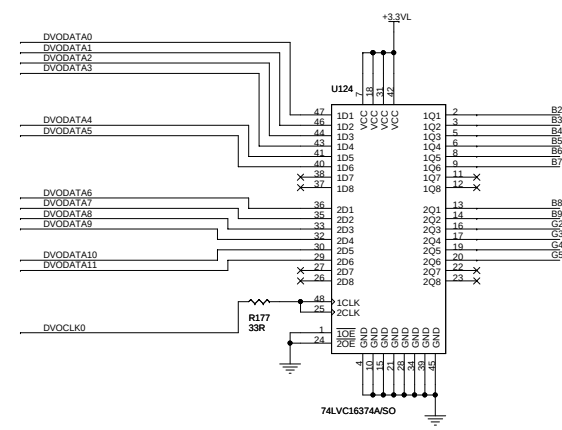


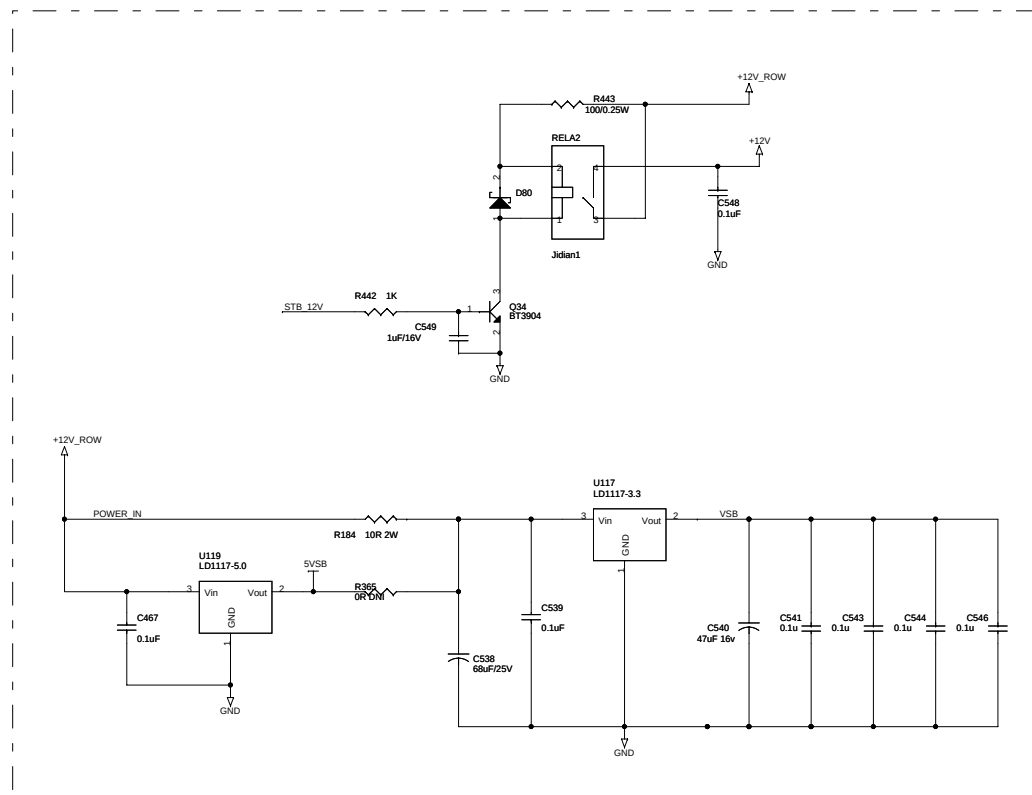
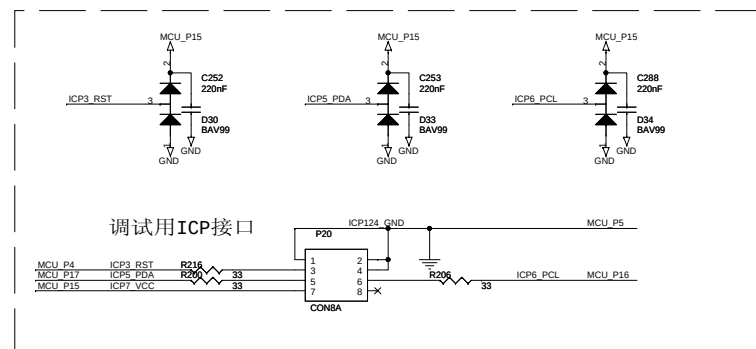
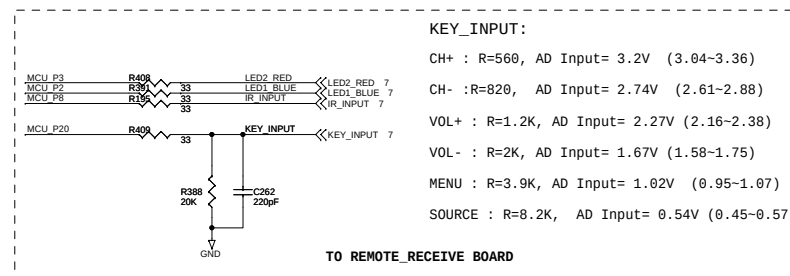
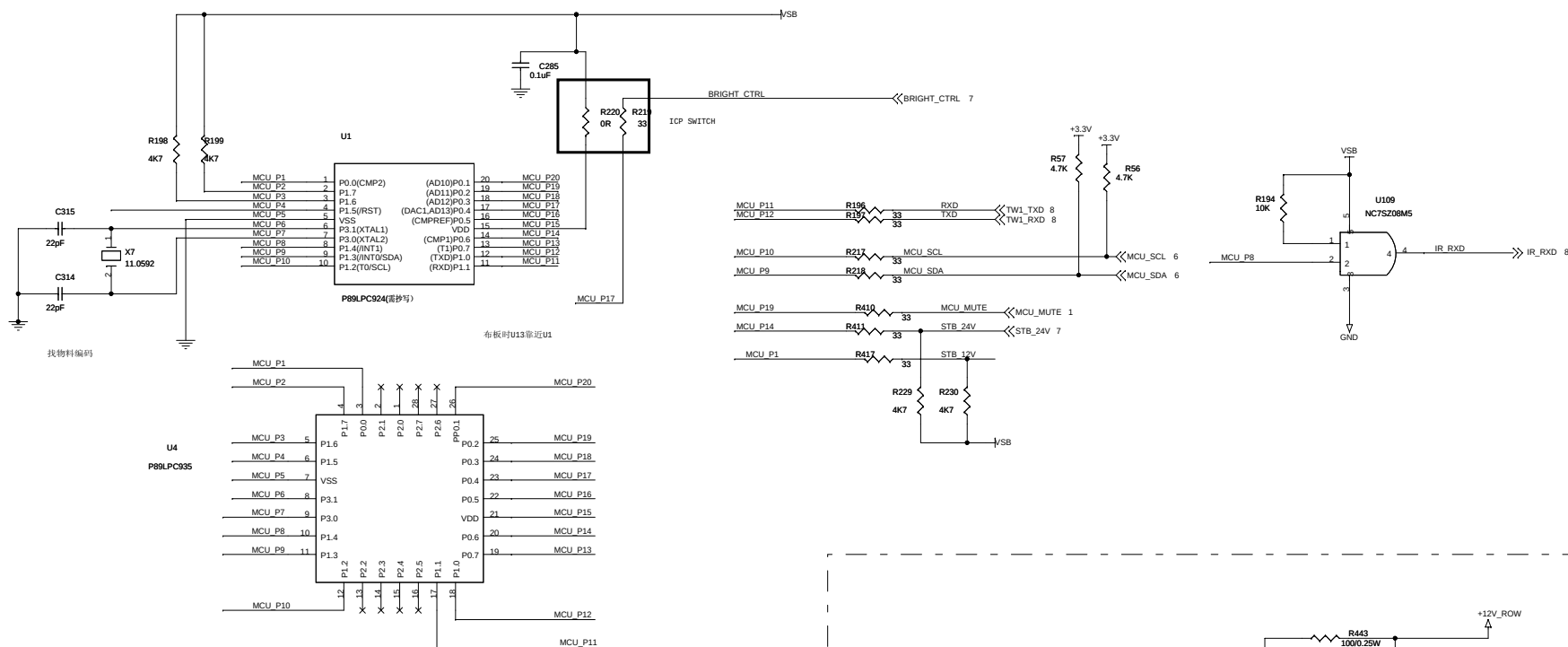
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81	Document Number	
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83	Wednesday, May 18, 2005	Page 1 of 1

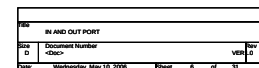


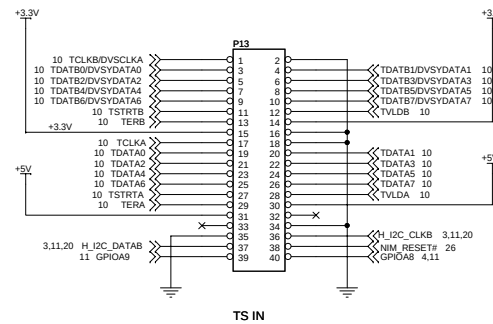


- 10 SMVCCS >> DVODATA0 G6
- 10 SMRSTS >> DVODATA1 G7
- 10 SMCLKS >> DVODATA2 G8
- 10 SMDATAA >> DVODATA3 G9
- 10 SMDTECTA >> DVODATA4 R2
- 10 SMDVCCA >> DVODATA5 R3
- 8 HD_IR_RXD >> DVODATA6 R4
- 8 HD_IR_TXD >> DVODATA7 R5
- 10 FLEX_CS2# >> DVODATA8 R6
- 10 FLEX_CS3# >> DVODATA9 R7
- 10 FLEX_CS4# >> DVODATA10 R8
- 10 FLEX_CS5# >> DVODATA11 R9
- 10 SMDATAS >> DVODATA12 R10
- 10 SMDTECTS >> DVODATA13 R11
- 10 FLEX_GNT# >> DVODATA14 R12
- 10 FLEX_STB >> DVODATA15 R13
- 10 FLEX_RDY# >> DVODATA16 R14

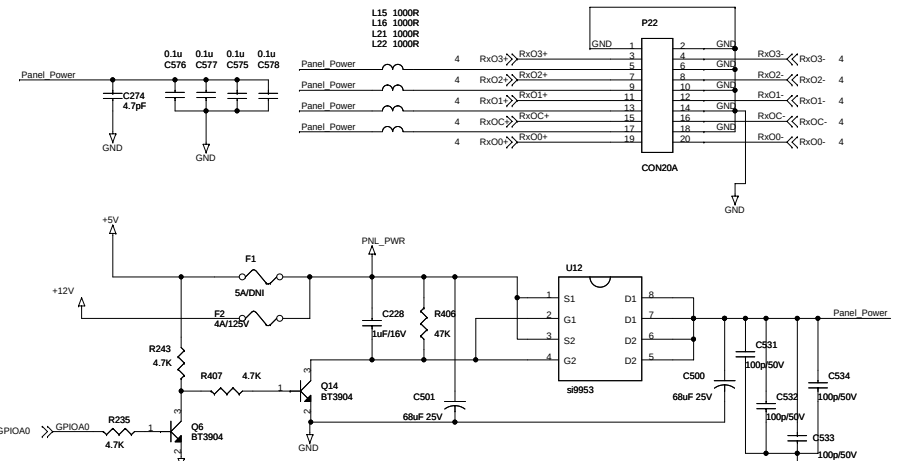
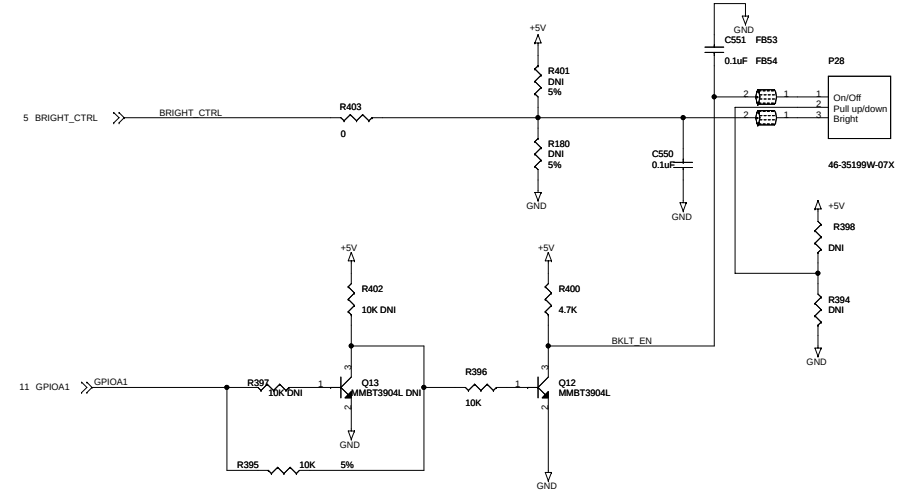
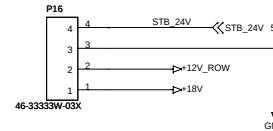




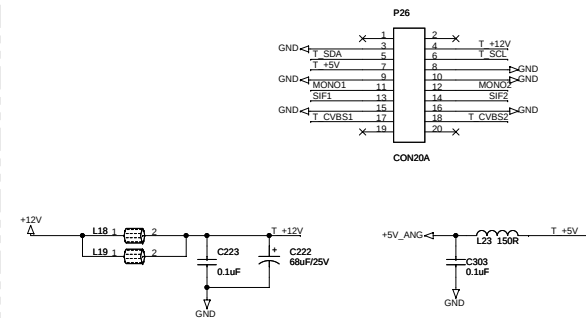




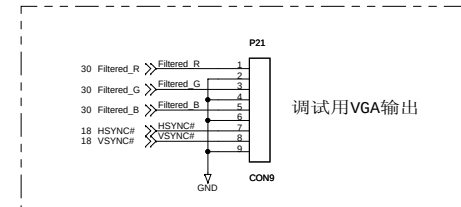
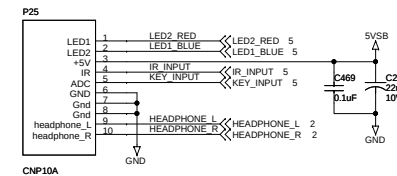
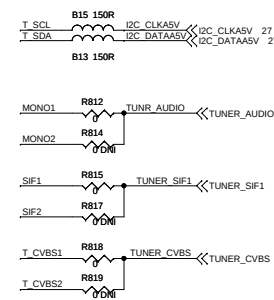
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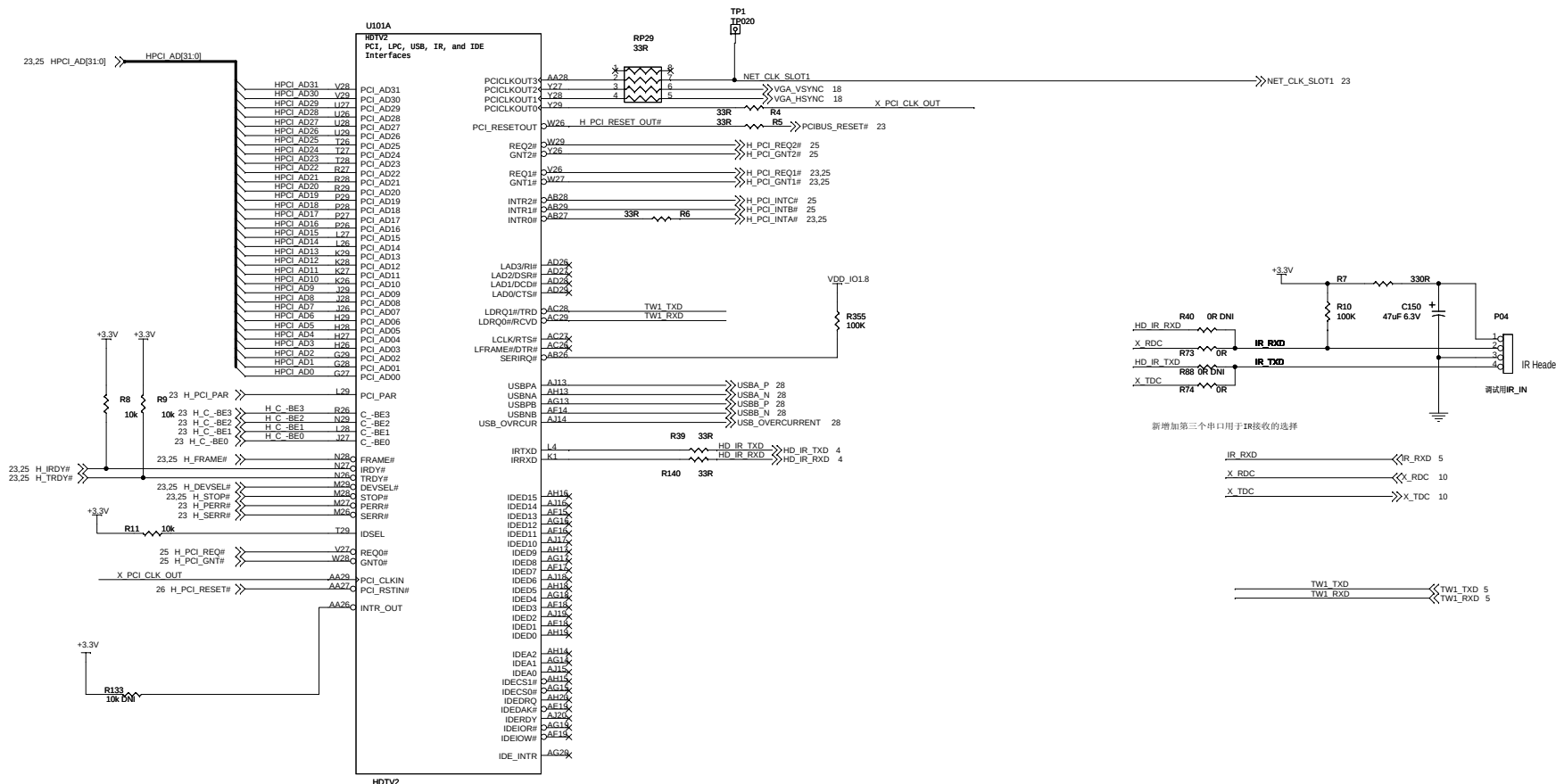


到LCD屏板接口电路

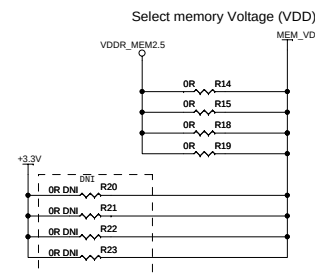
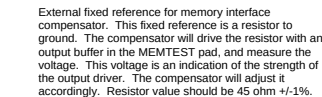
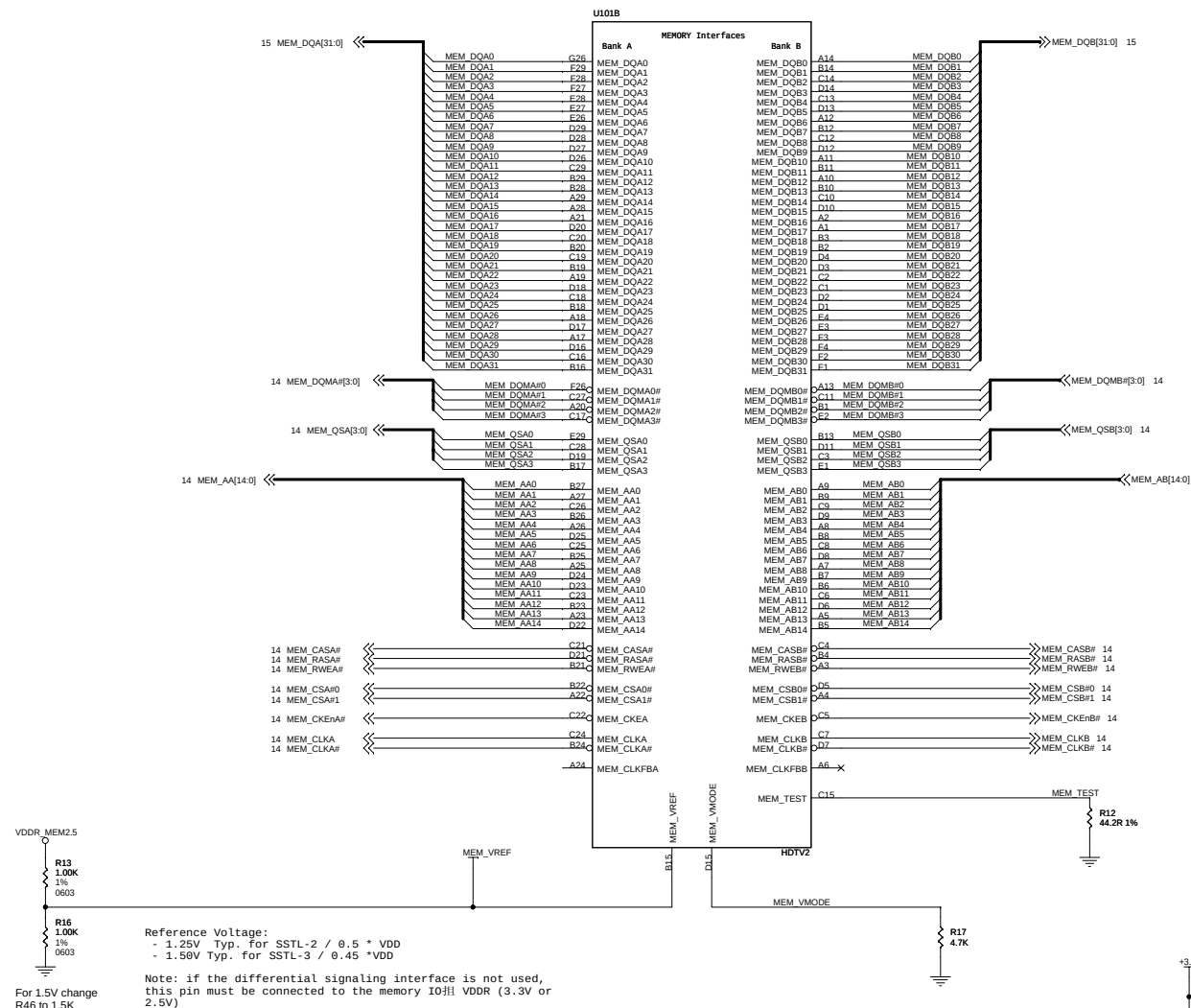


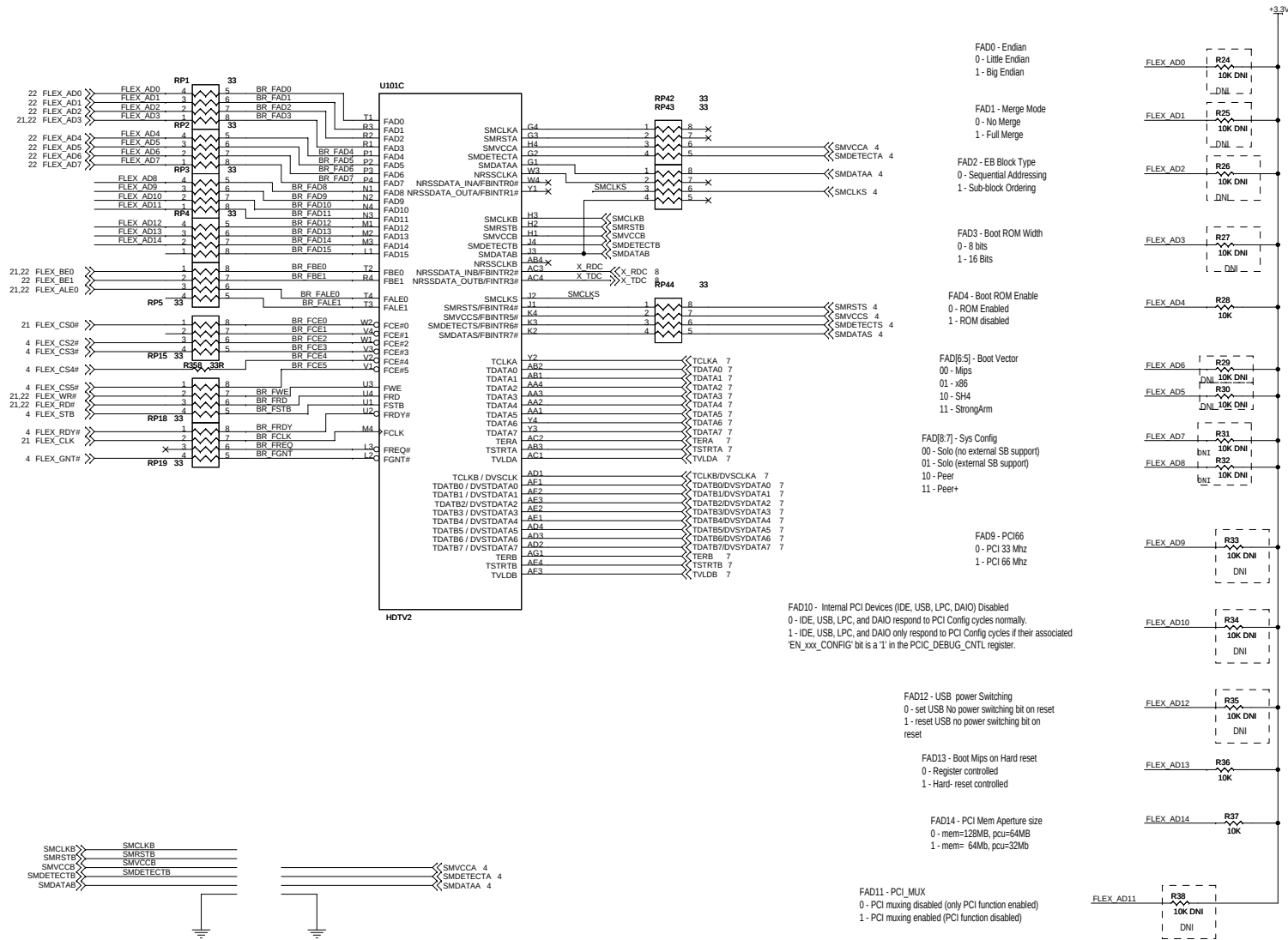
TUNNER板接口





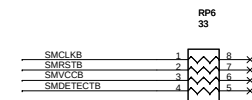
新增第三个串口用于IR接收的选择

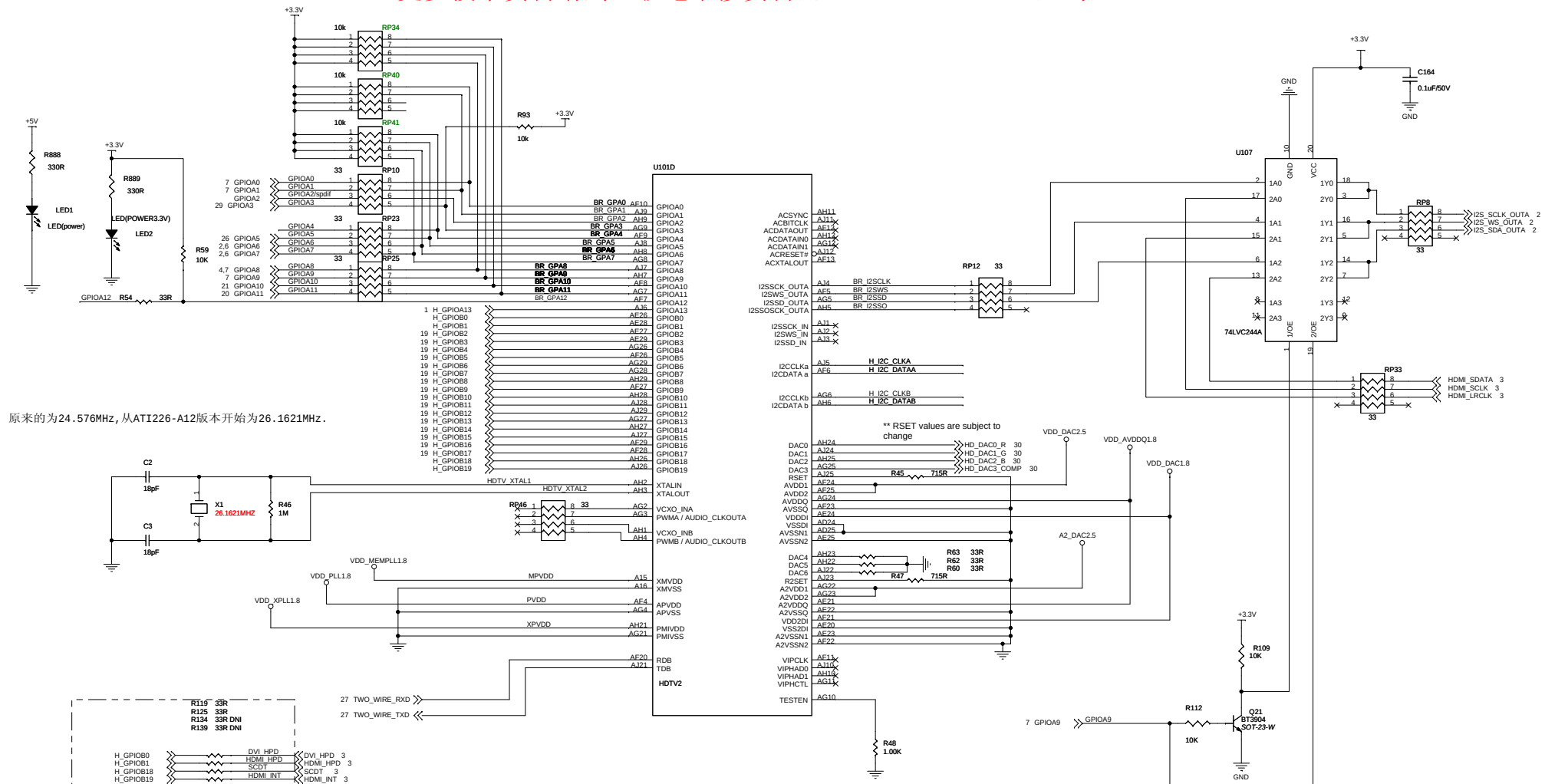




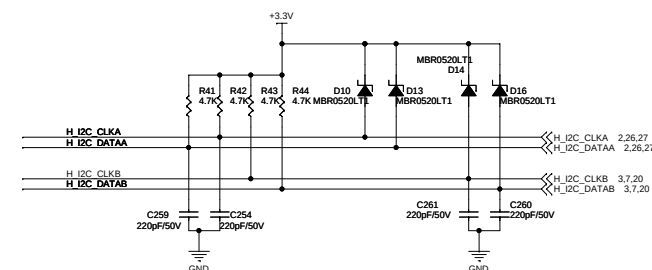
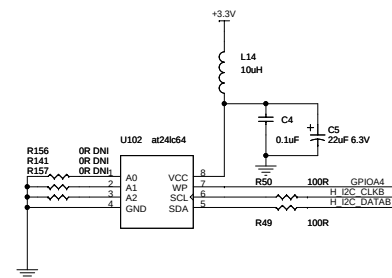
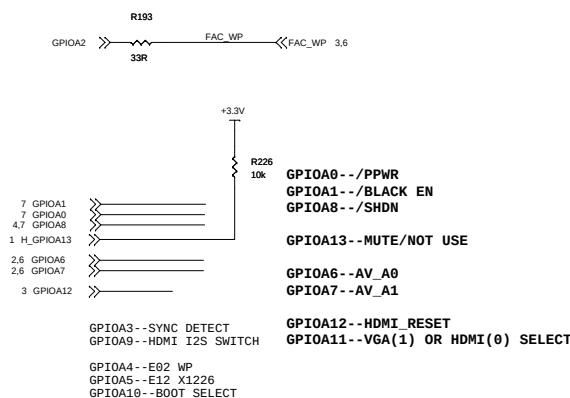
此处删除了smart card连接端子

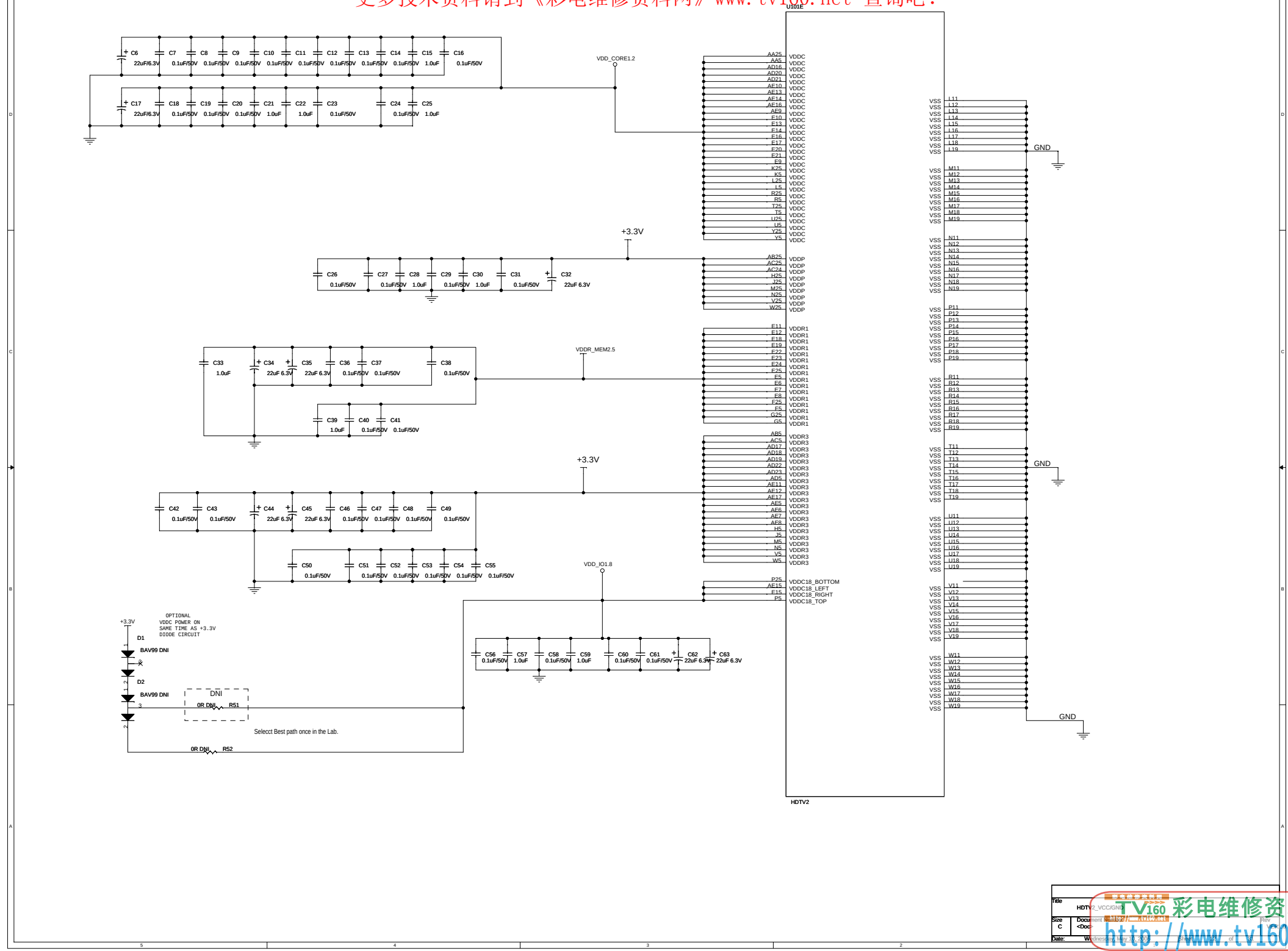
将其连接的信号线中未引出的部分用排阻RP43空着引腿及RP6引出

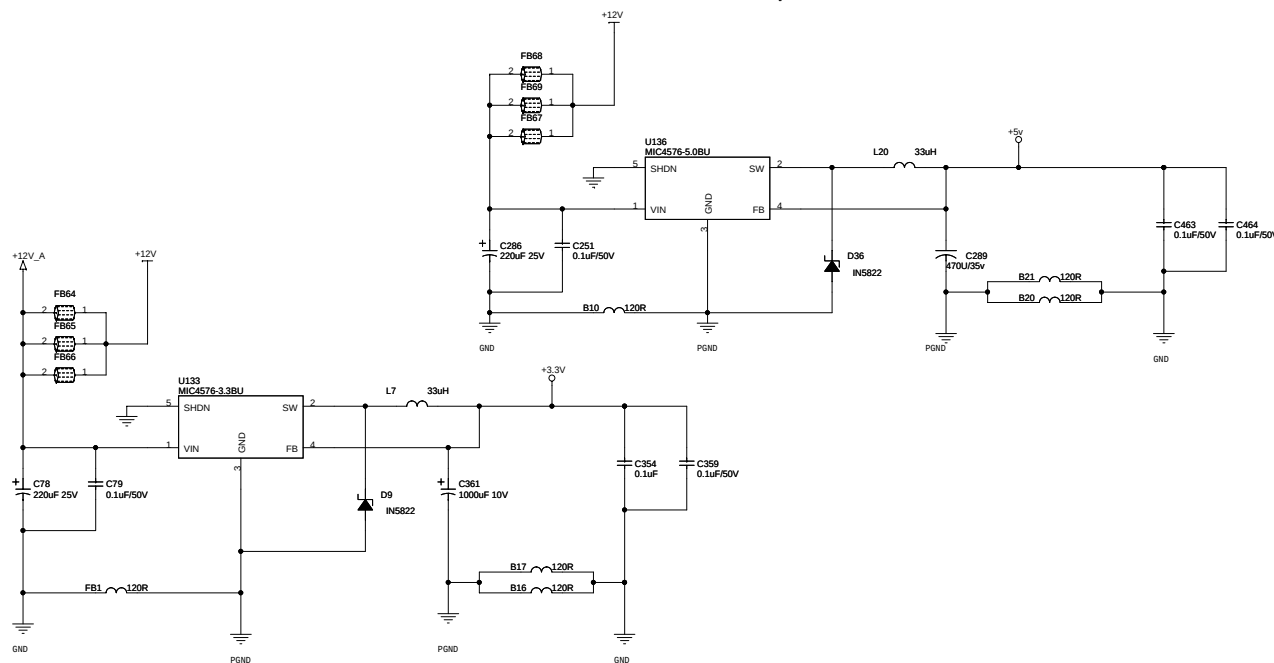
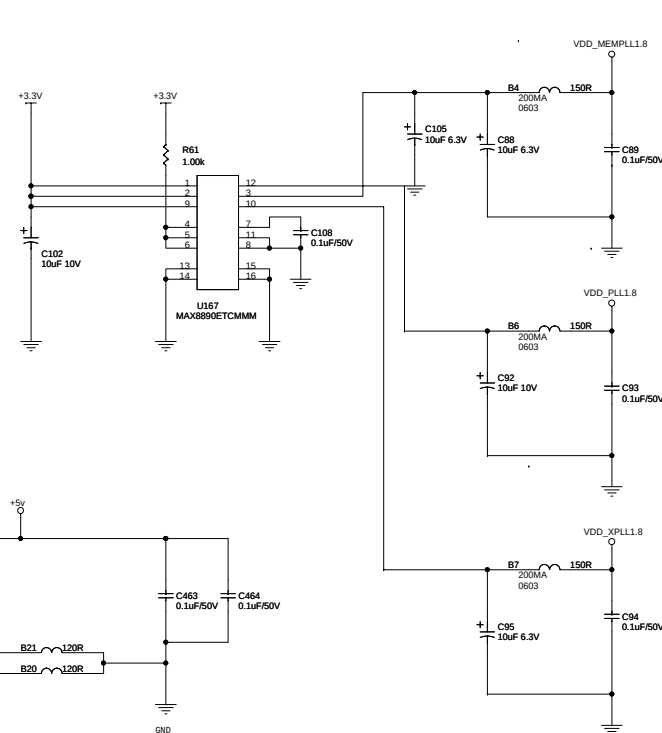
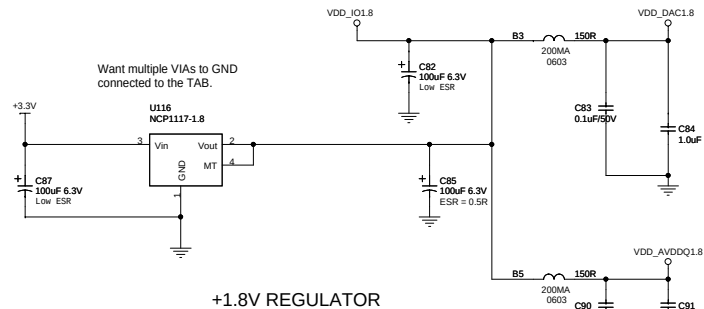
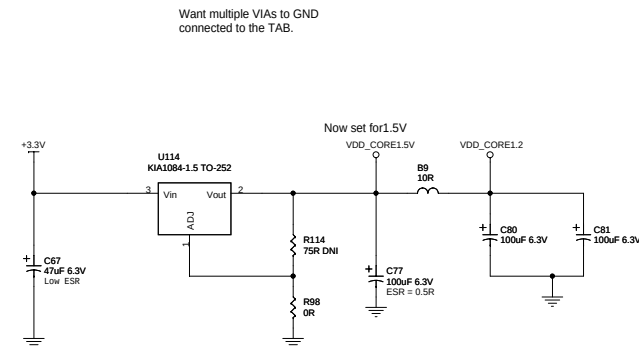
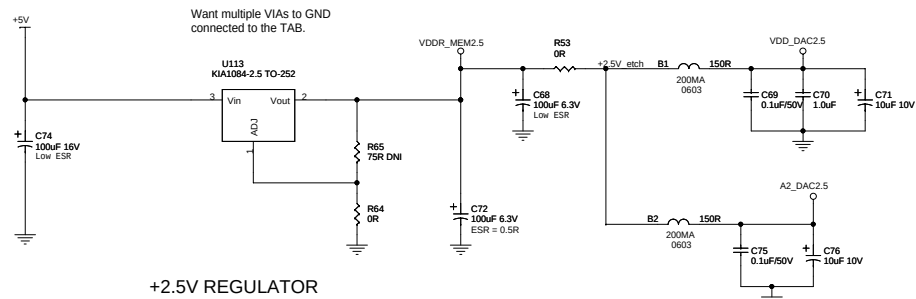


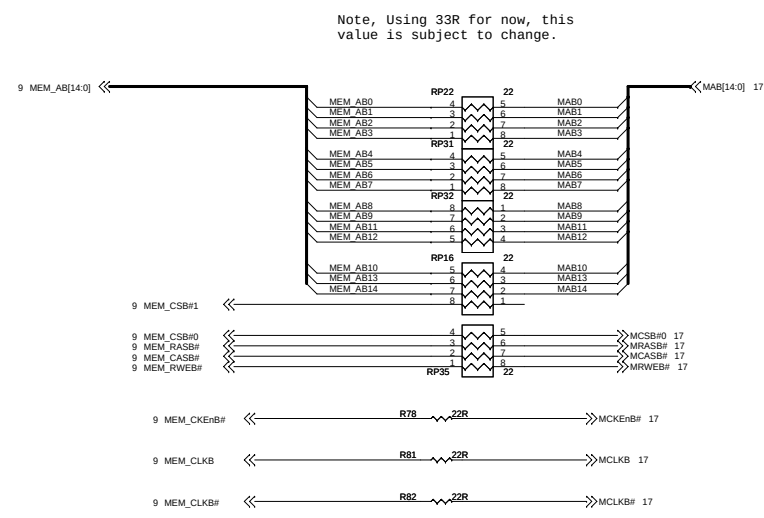
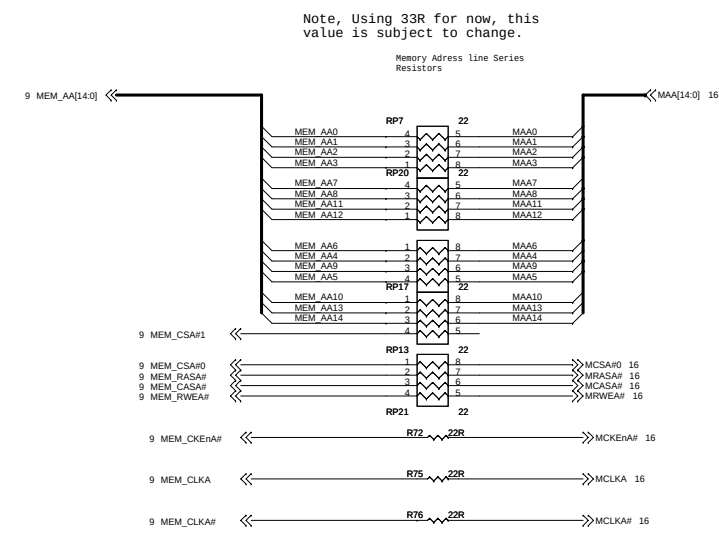
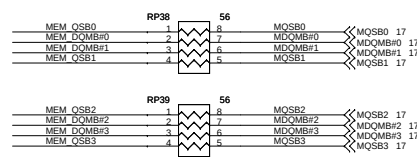
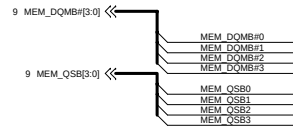
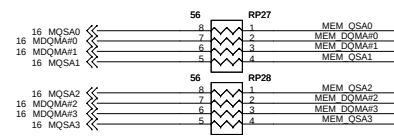
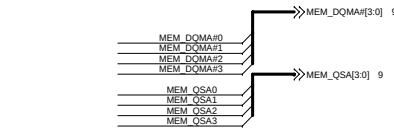


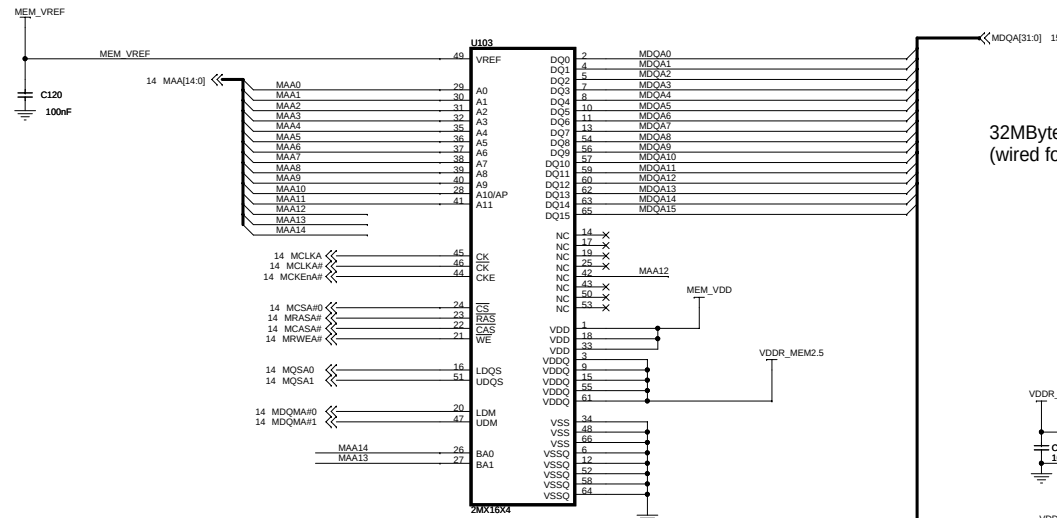
GPIOA9为1时VGA的I2S导通
为0时HDMI的I2S导通



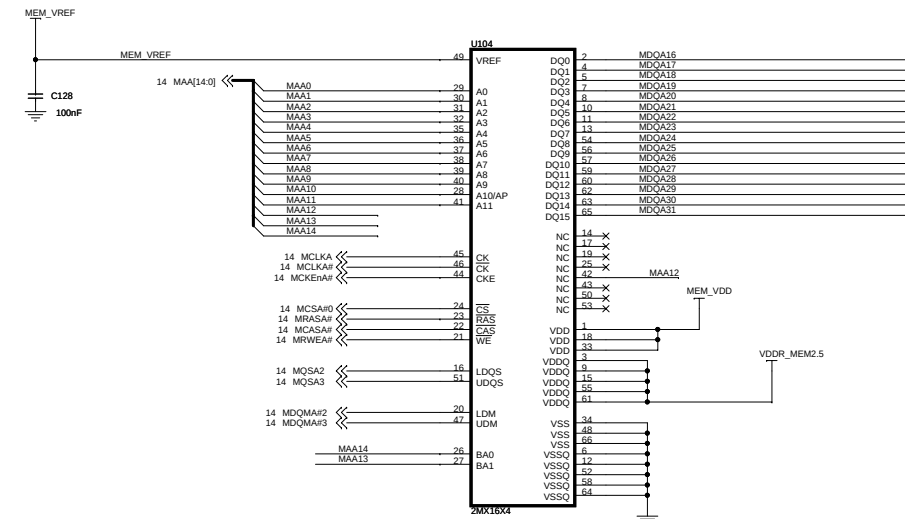
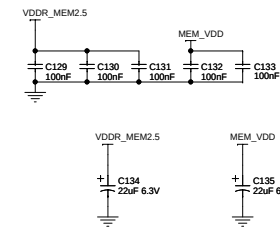
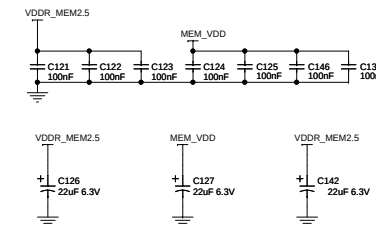


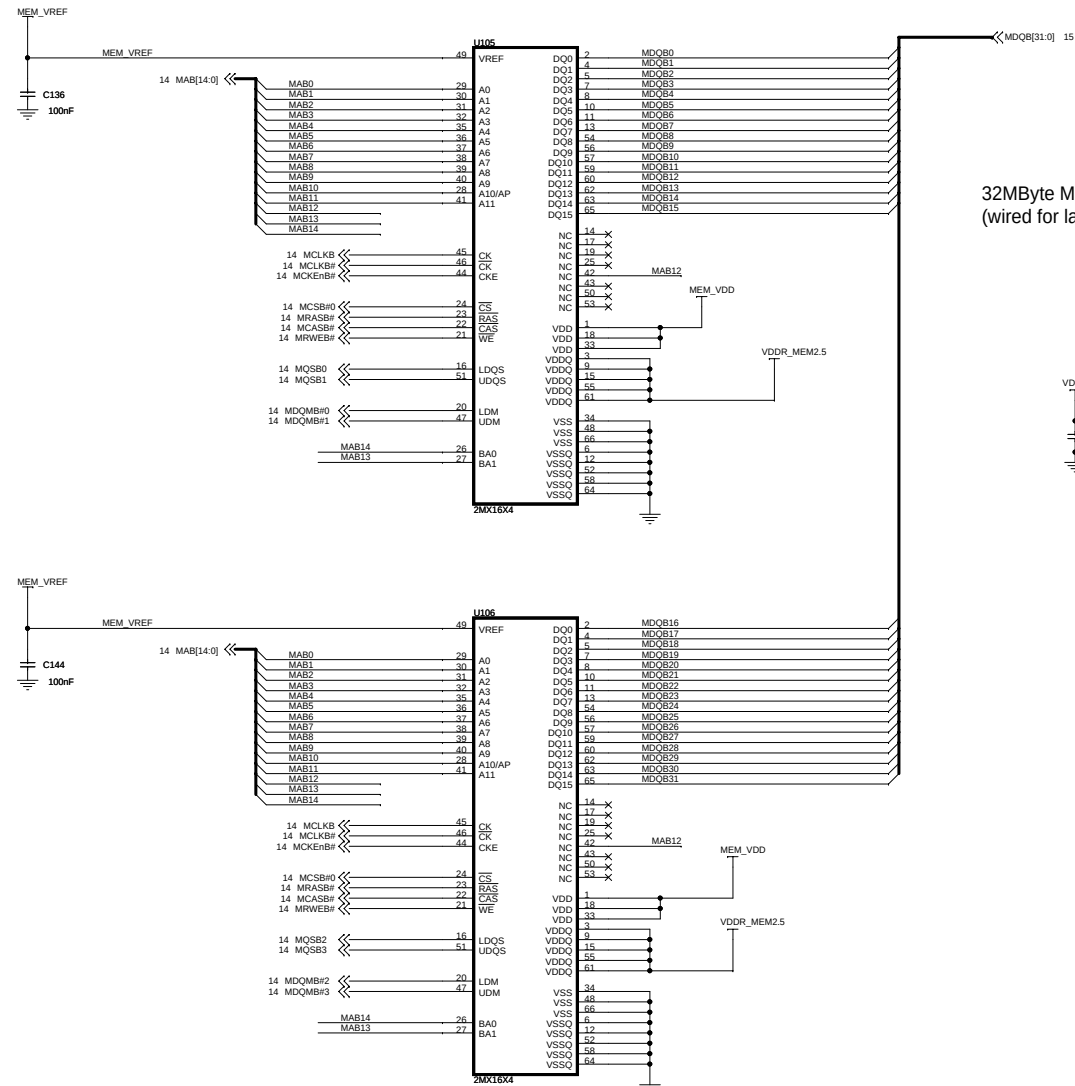




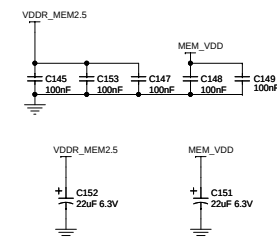
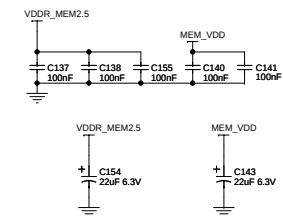


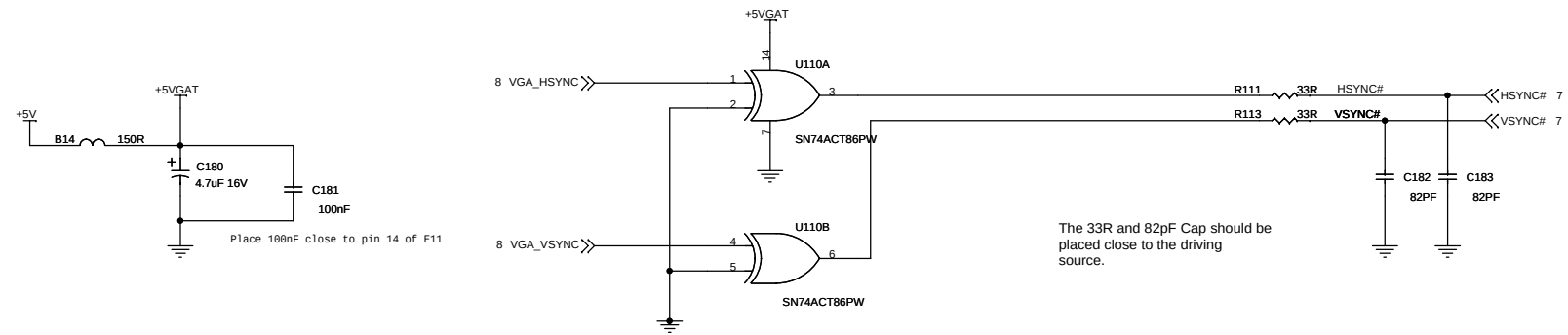
32MByte Memory Bank using 128Mbit (x 16) components.
(wired for larger parts - upto 512Mbit parts)

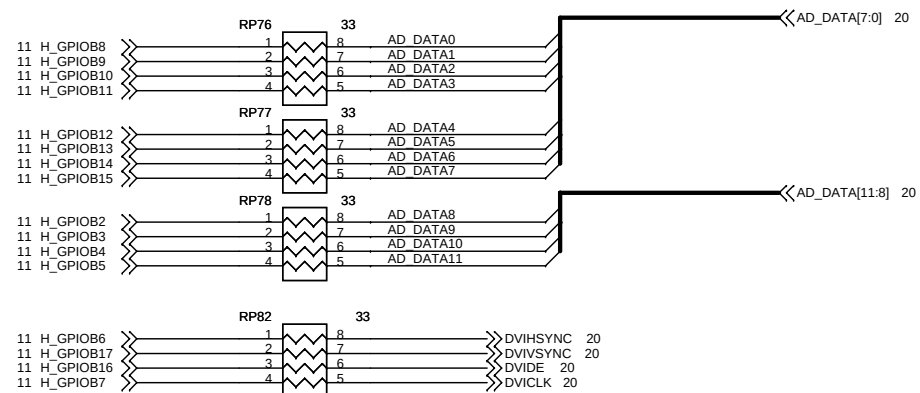




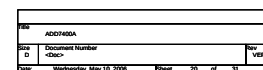
32MByte Memory Bank using 128Mbit (x 16) components.
(wired for larger parts - upto 512Mbit parts)

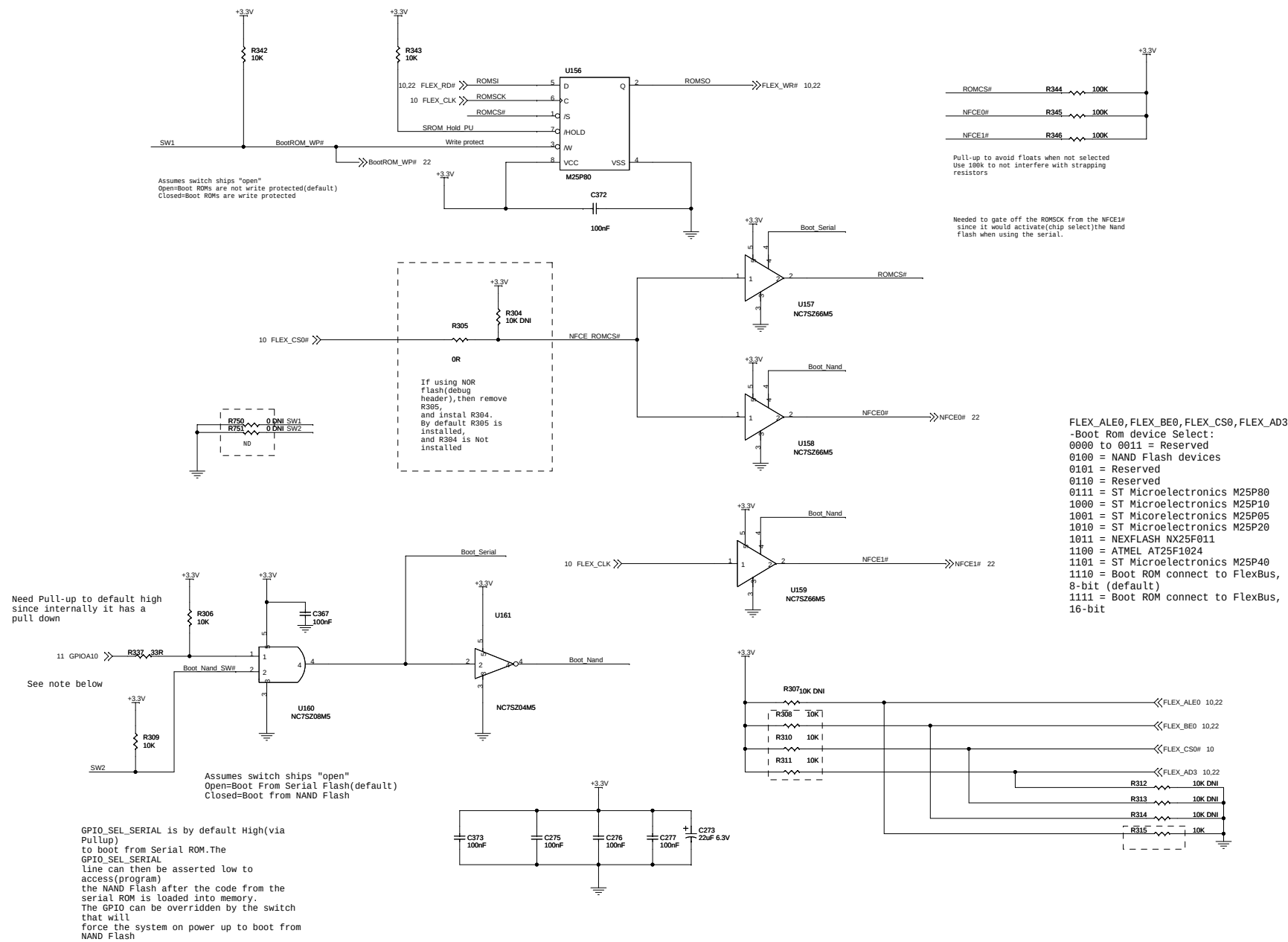


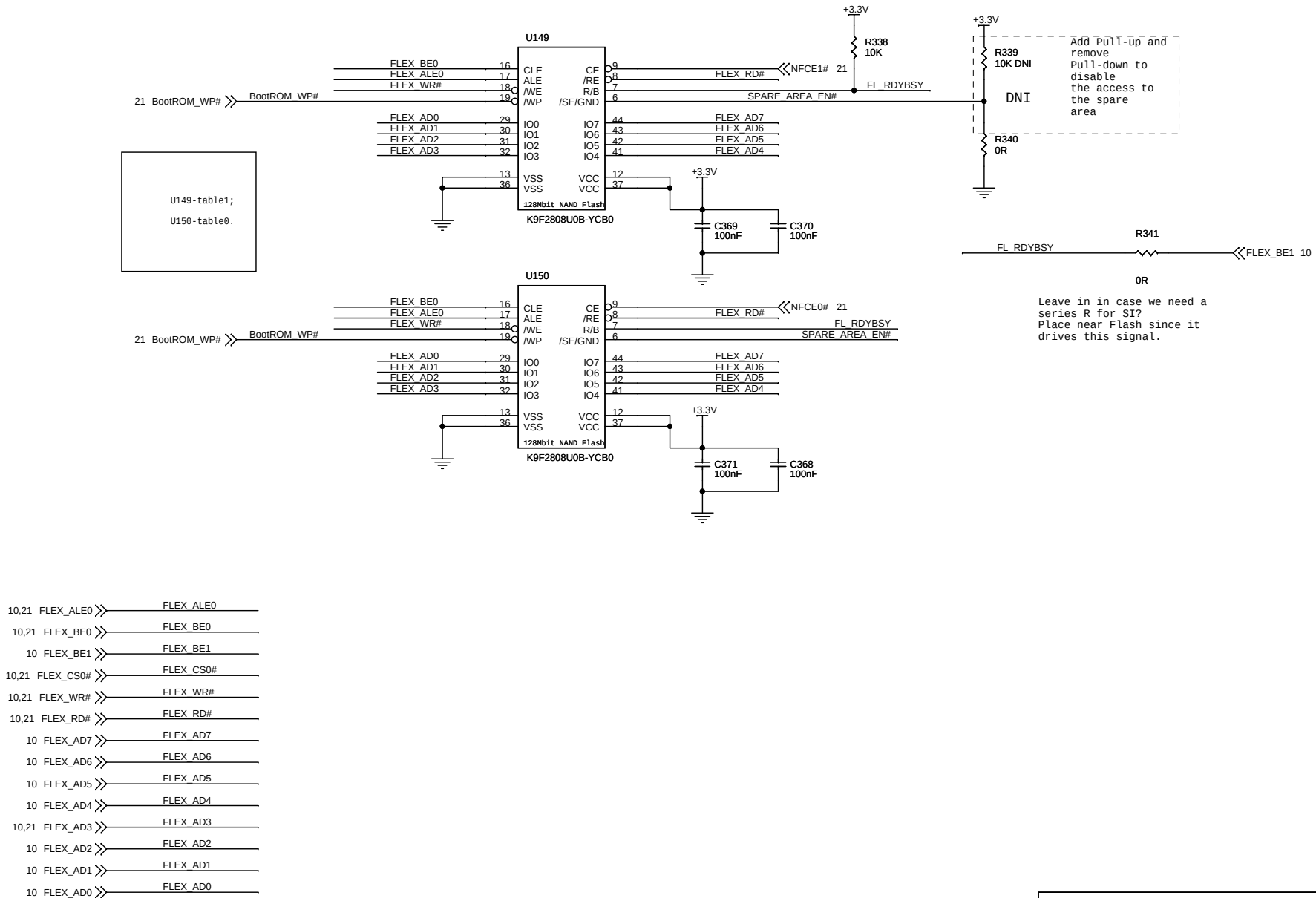




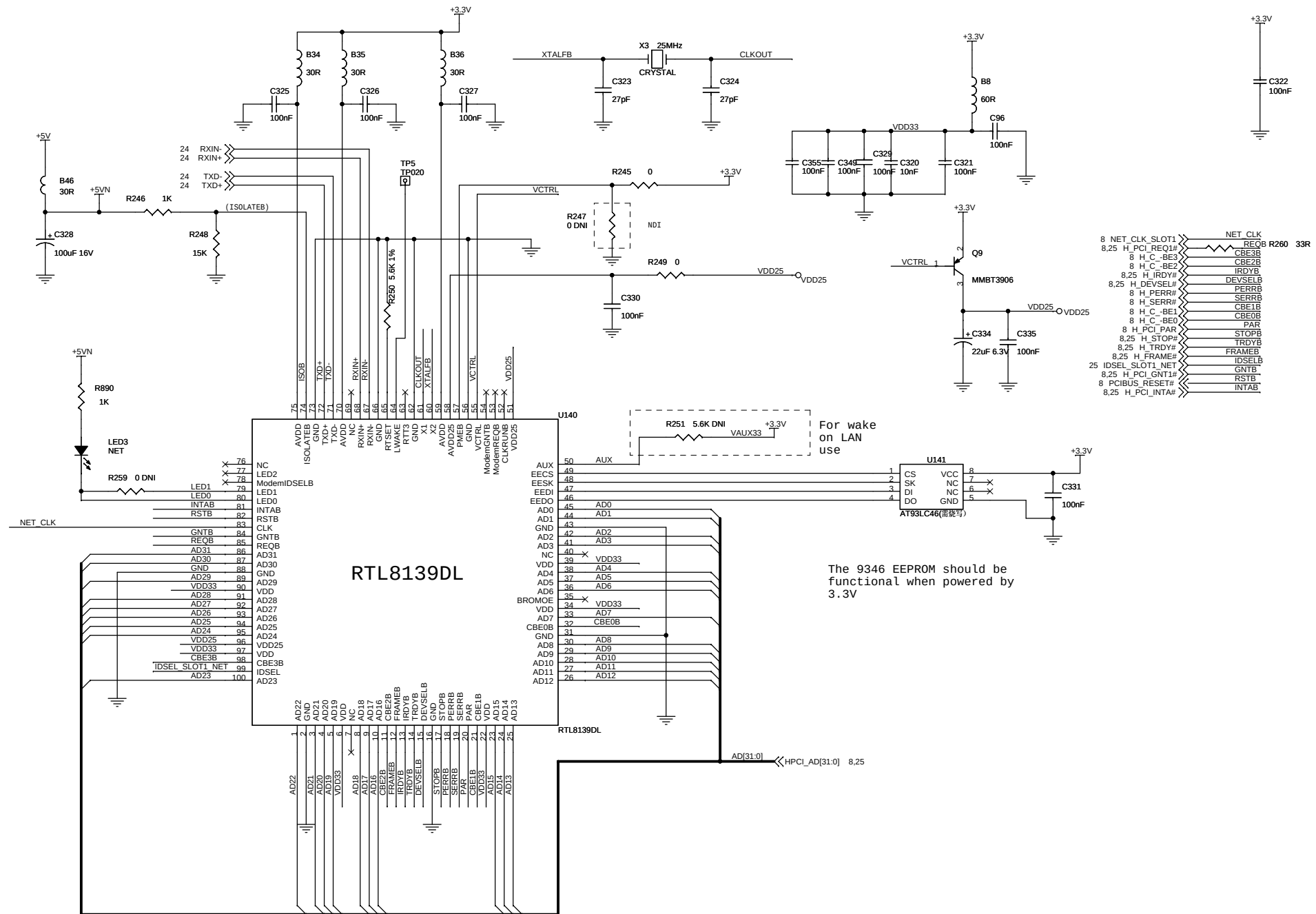
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Size	Document Number	Rev
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Date: Wednesday, May 10, 2006		
Sheet 19 of 31		

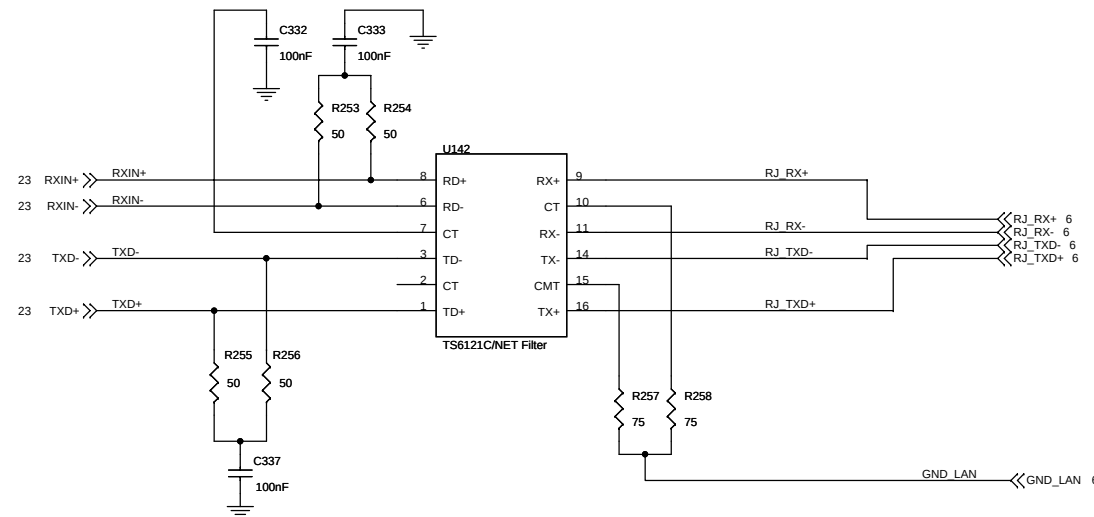


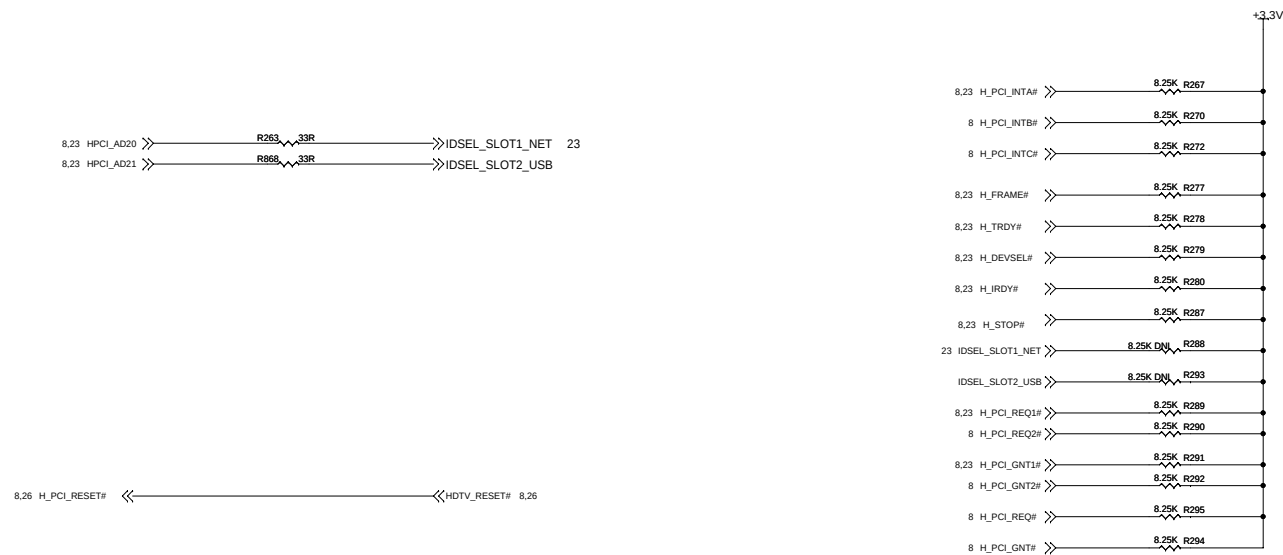


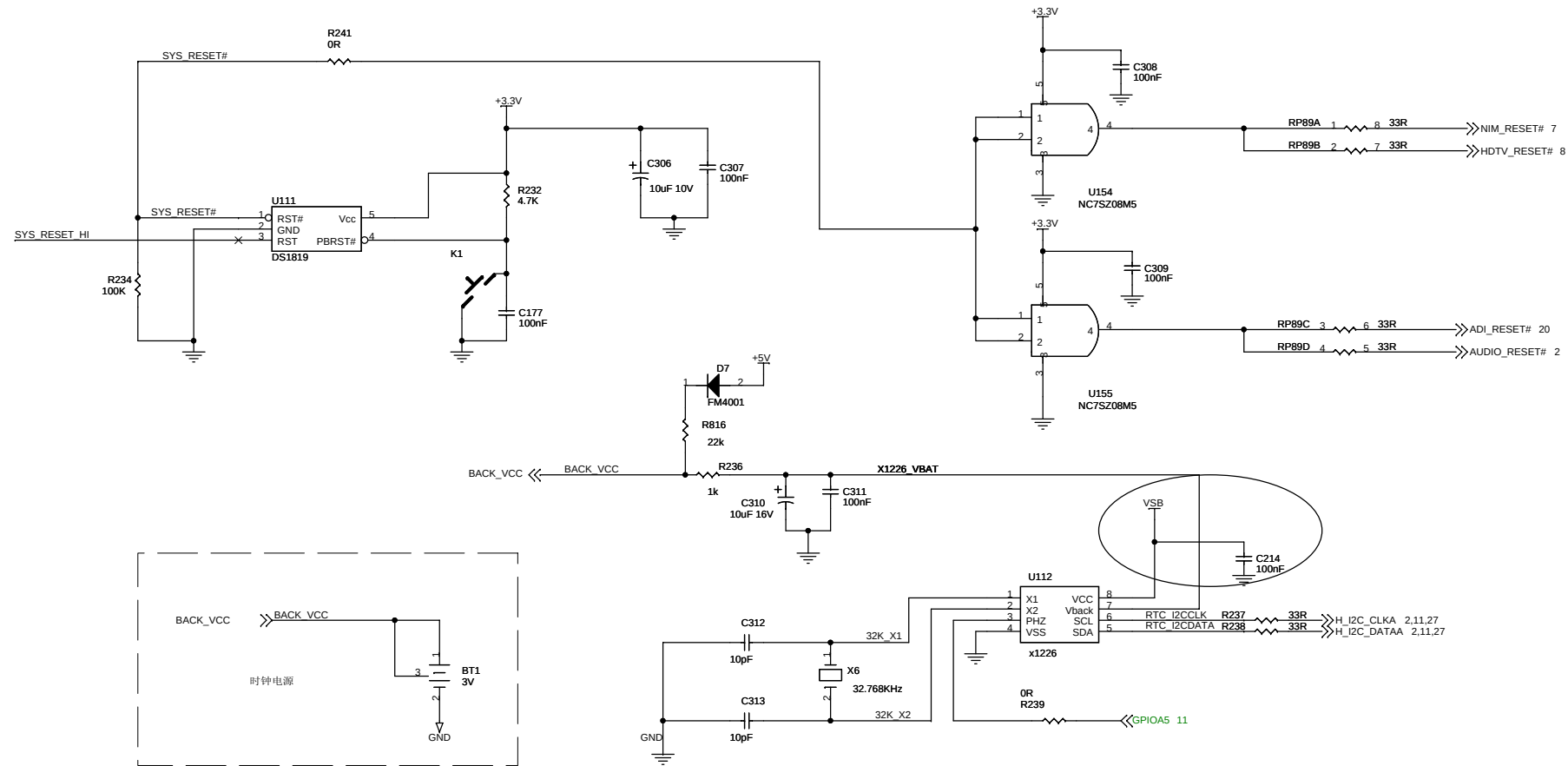


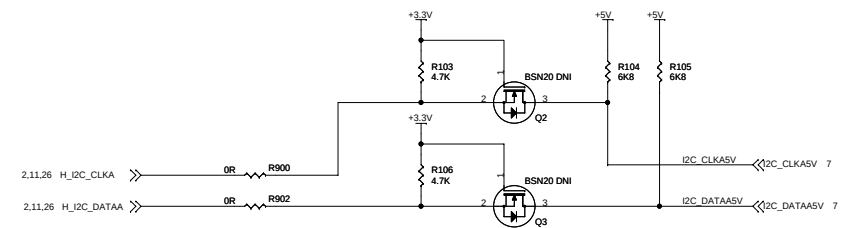
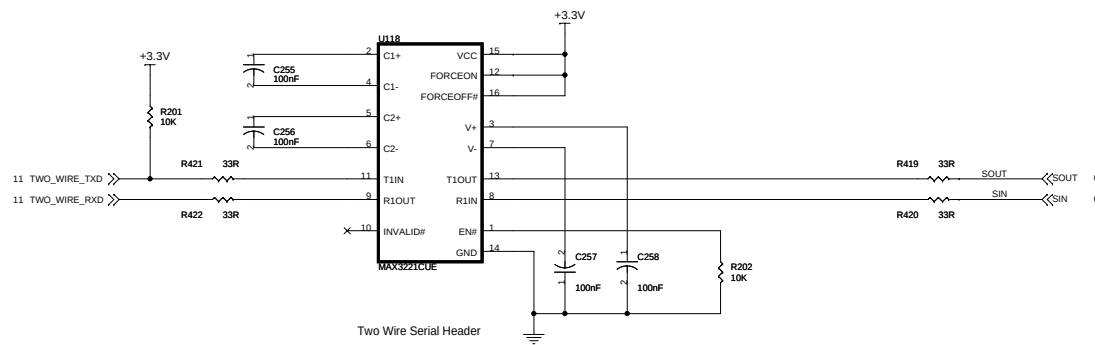
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Size	Document Number	Rev
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Date:	Wednesday, May 10, 2006	Sheet 22 of 31

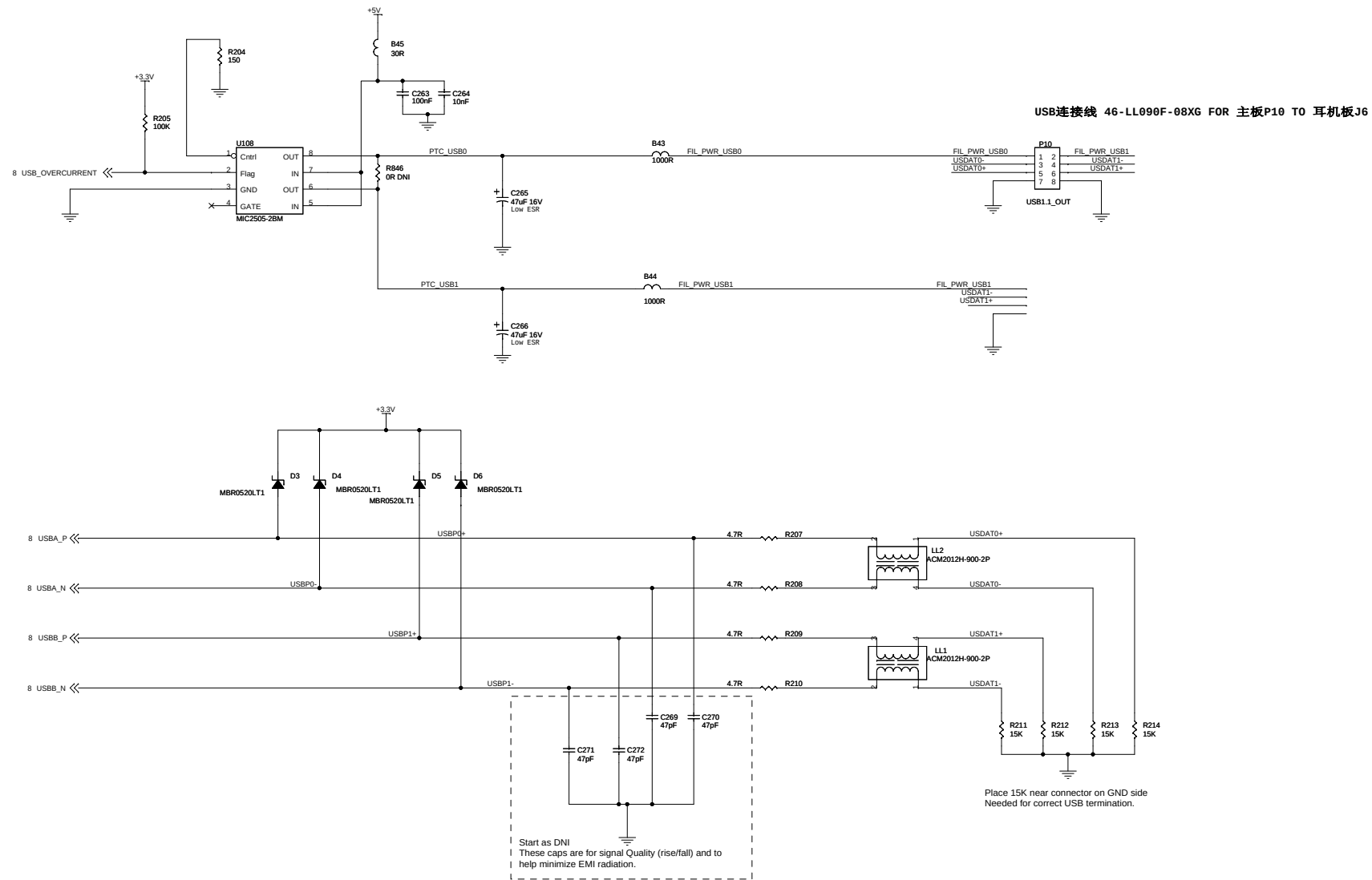


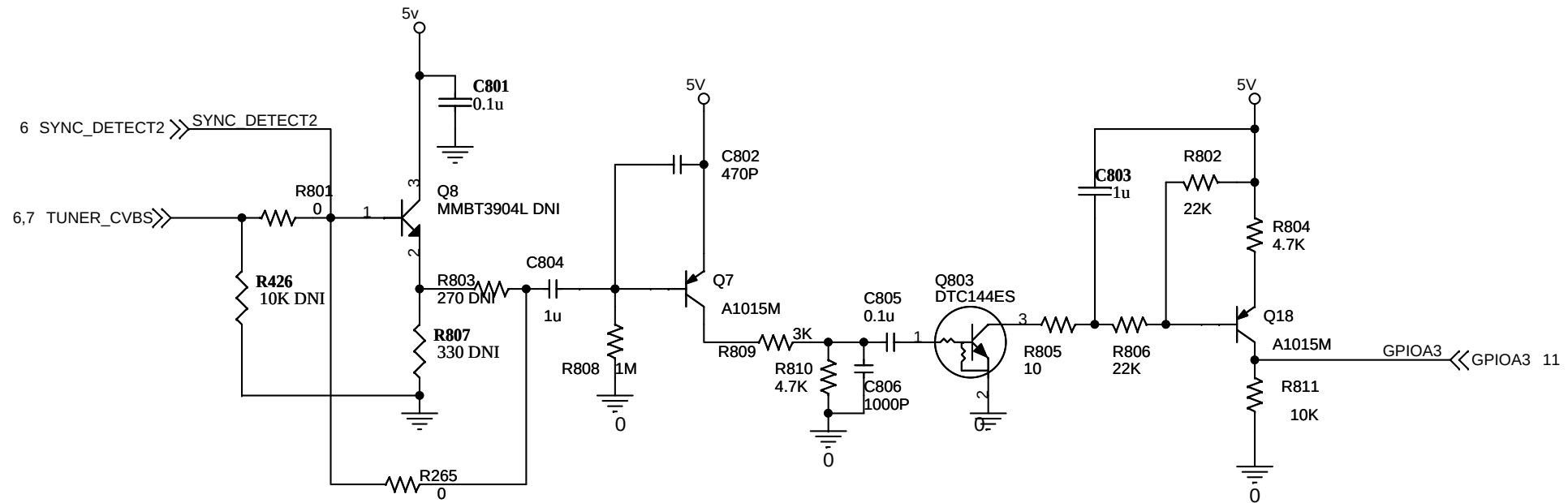










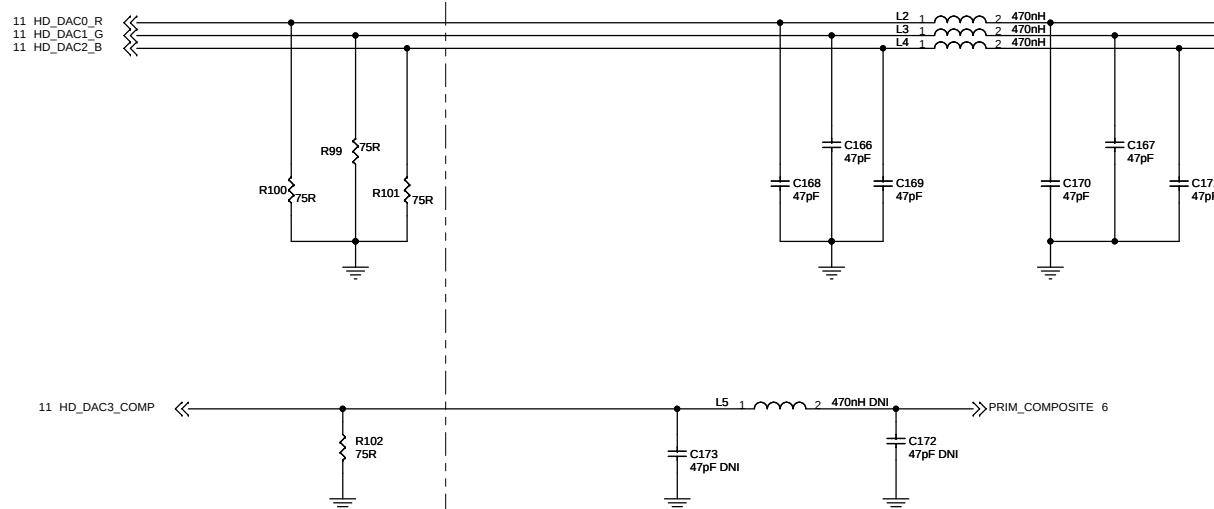


Title		同步电平检测电路
Size A	Document Number	<Doc>
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ALL Fliter Values are preliminary and subject to Change

Note, Inductors specifications should be:

- DC resistance less than 0.5R
- Spec'd Inductance measured anywhere between 1 and 30 MHz
- No power supply inductors, inductors designed for signal or RF only
- Capable of 50ma DC current of more
- Self resonant at least over 50MHz
- 5% tolerance over temperature



***** See table for all values *****

Title		AV/VGA FILTER
Size	Document Number	
Custom	Doc	
Date:	Wednesday, May 10, 2006	

软件虚拟物料编码:

V8-L-AT226-A3-M01 (mcu程序) FOR U1
V8-L-AT226-A3-F01 (系统主程序) FOR U149&U150
V8-L-AT226-A3-F02 (SERIAL FLASH 程序 P80) FOR U156
V8-L-AT226-A3-F03 (EEPROM程序) FOR U102
V8-L-AT226-A3-D01 (VGA DDC程序)FOR U3
V8-L-AT226-A3-D02 (HDMI HDCP程序) FOR U27
V8-L-AT226-A3-D03 (网卡MAC地址) FOR U141
V8-L-AT226-A3-D04 (DVI DDC程序)FOR U29

Title		
MEMO		
Size	Document Number	Rev
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Date:	Wednesday, May 10, 2006	Sheet