

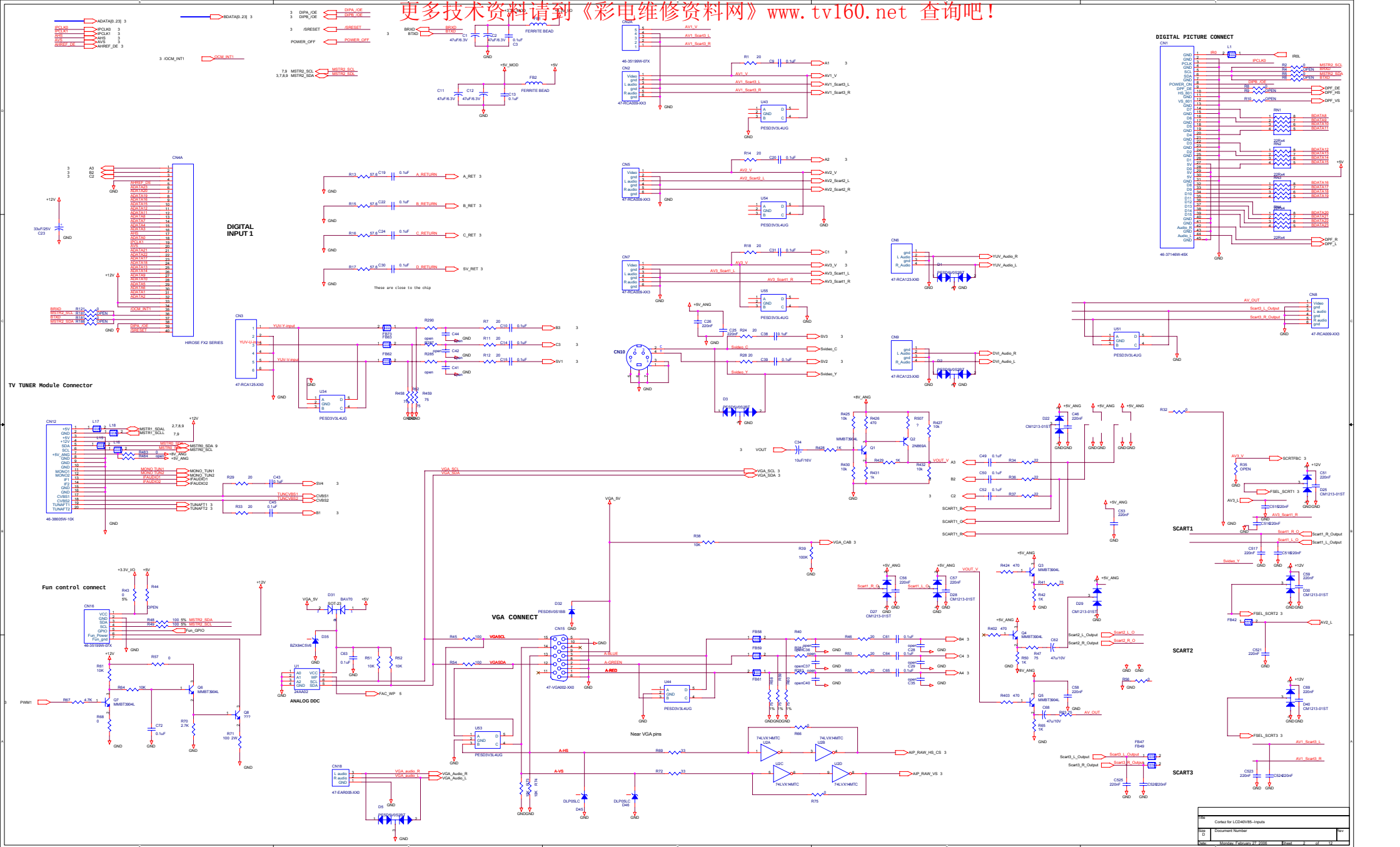
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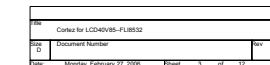
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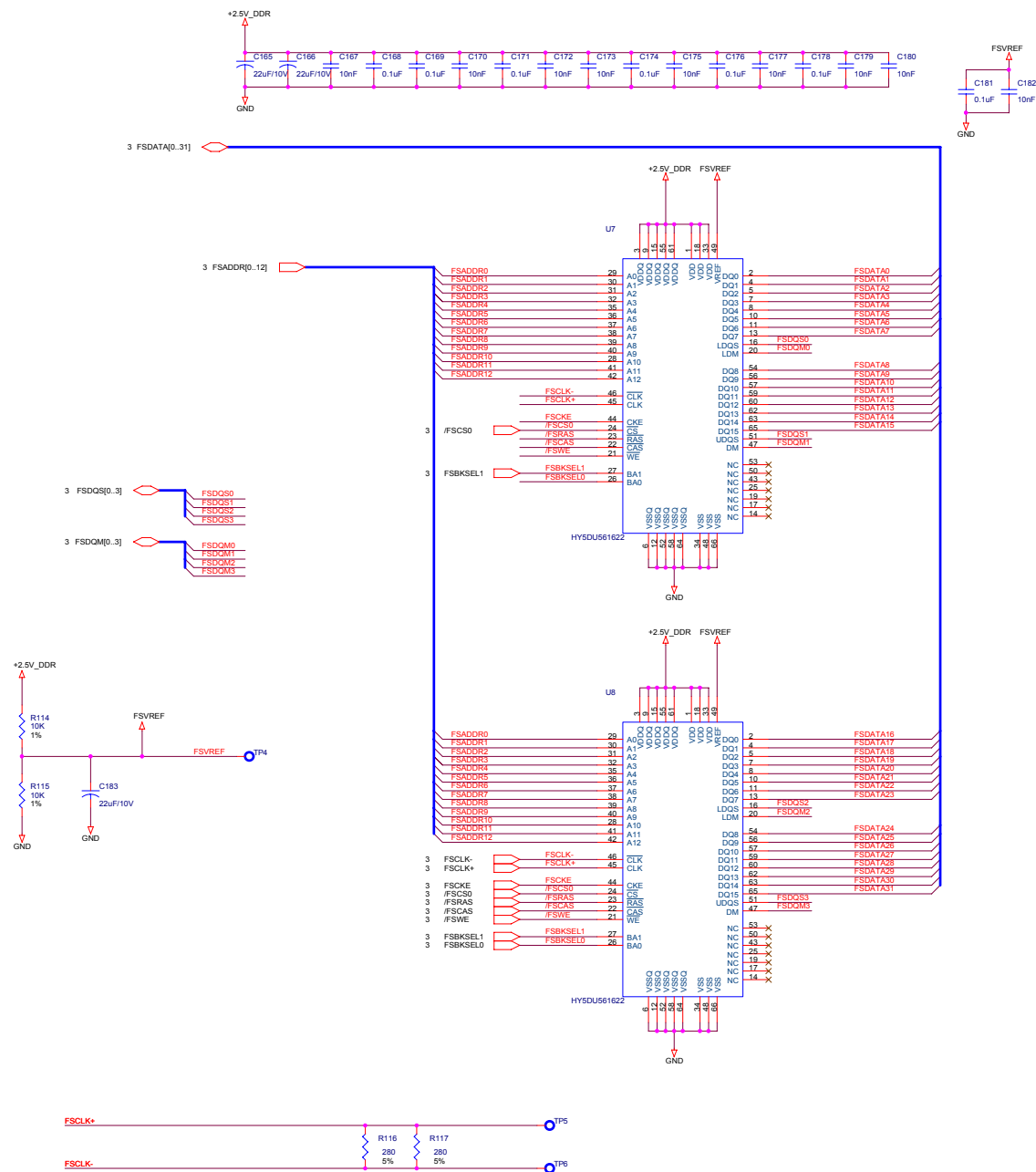
GC32 For L37E64
40-LD40V9-MAC4X

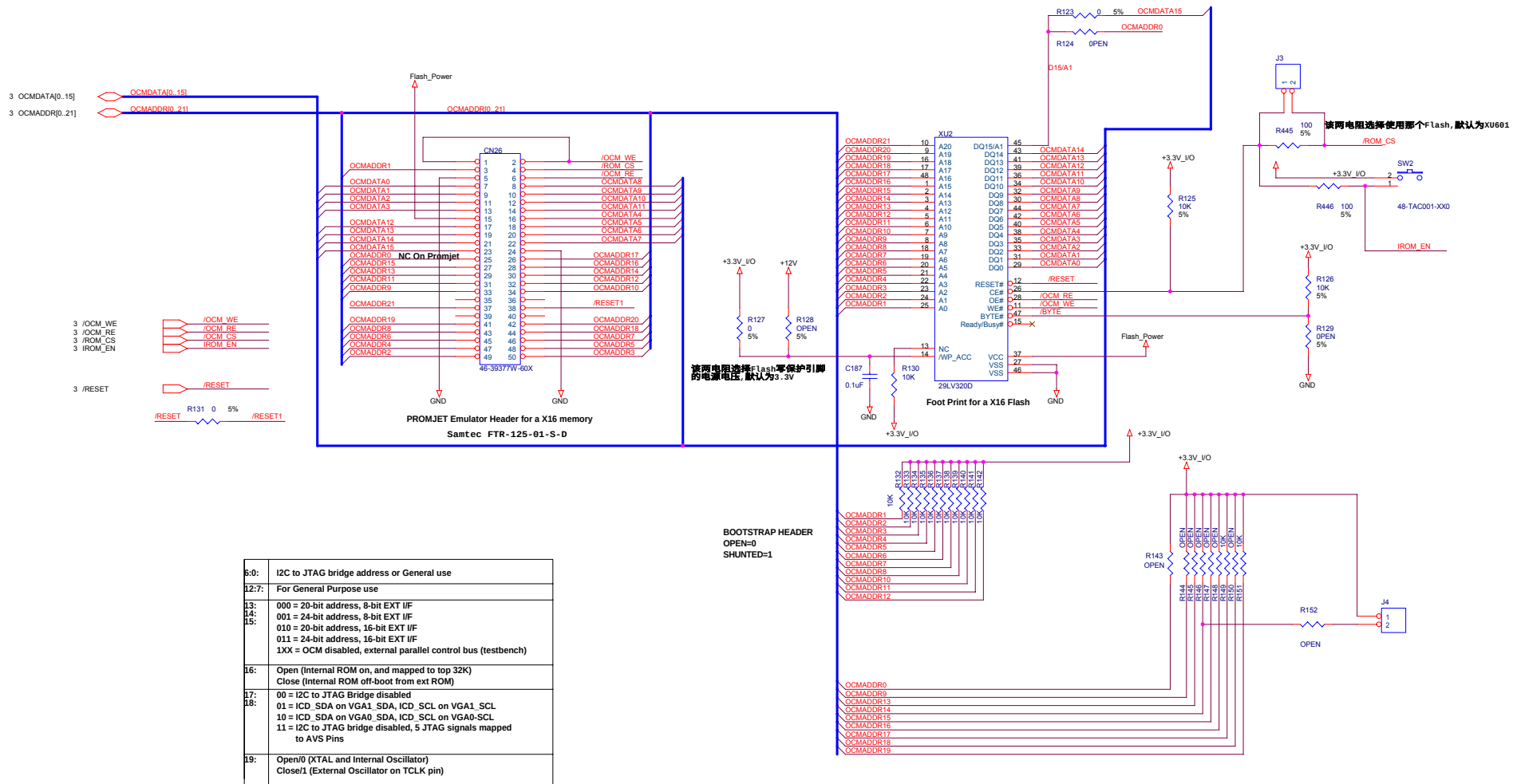
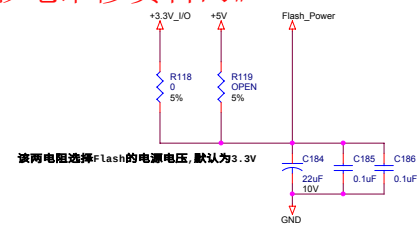
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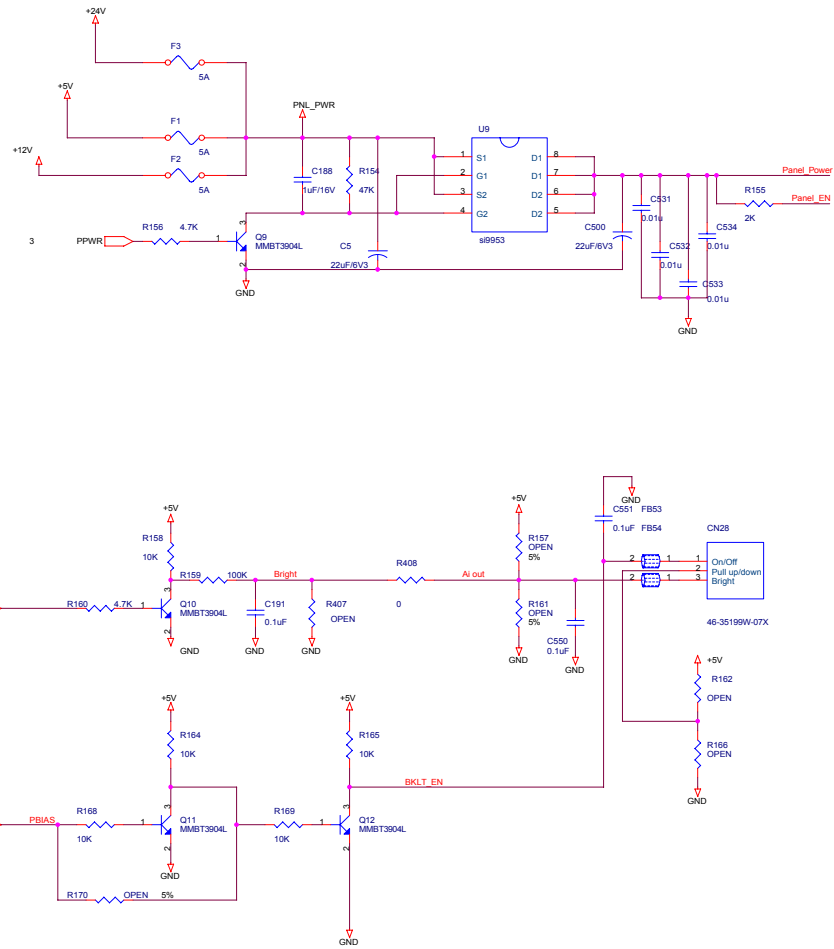
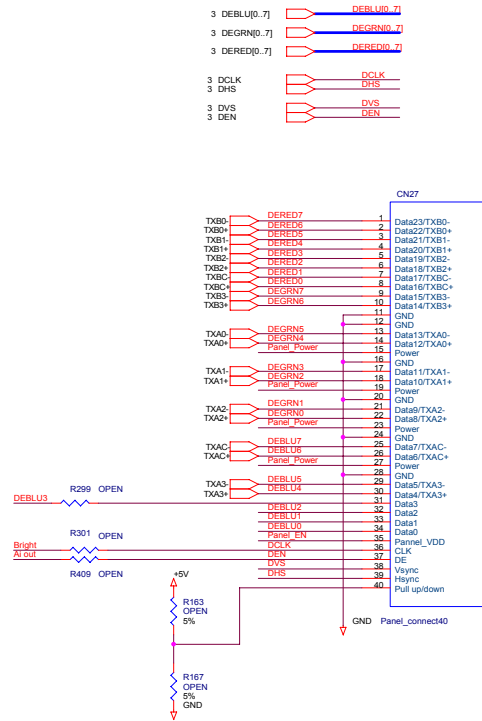




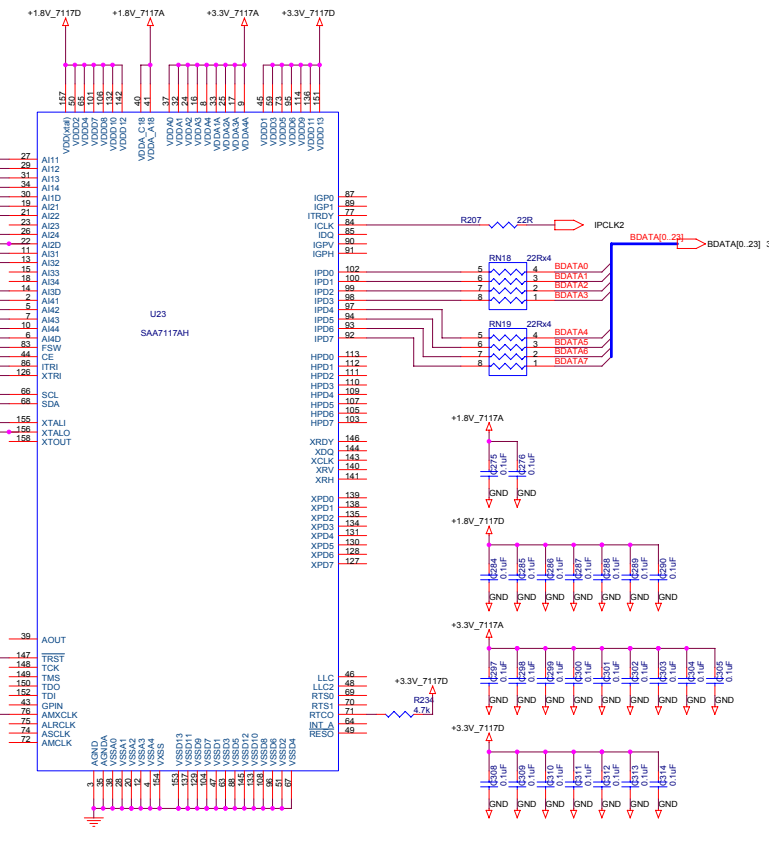
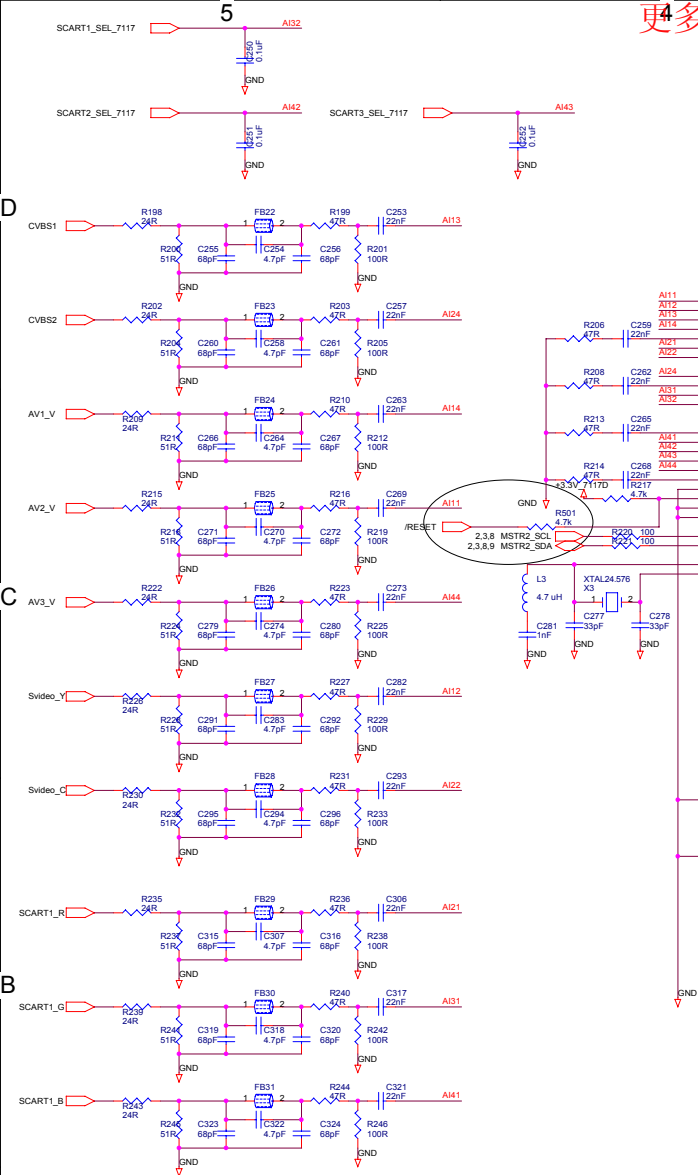


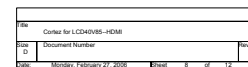


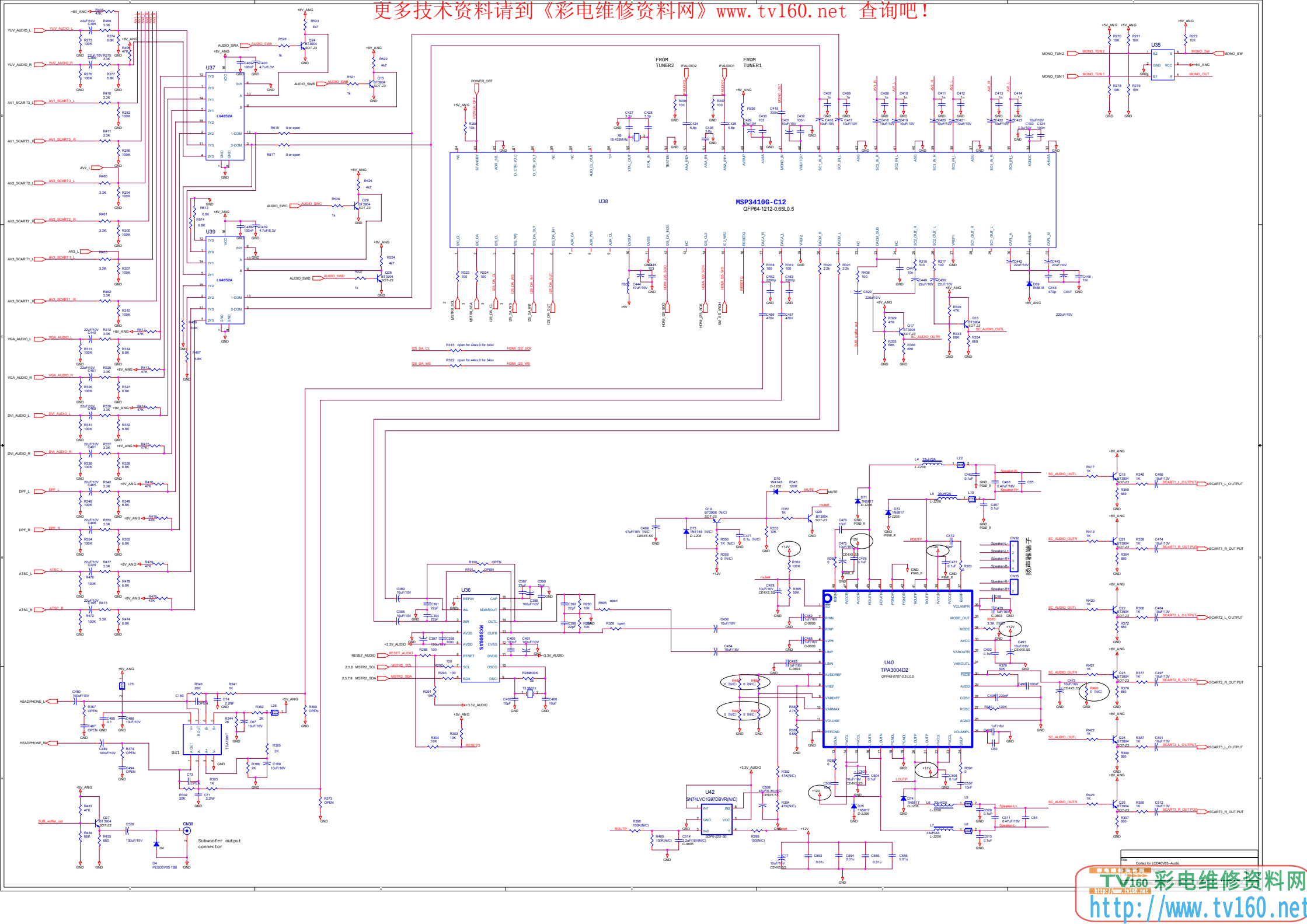
6:0:	I2C to JTAG bridge address or General use
12:7:	For General Purpose use
13:	000 = 20-bit address, 8-bit EXT IF
14:	001 = 24-bit address, 8-bit EXT IF
15:	010 = 20-bit address, 16-bit EXT IF
	011 = 24-bit address, 16-bit EXT IF
	1XX = OCM disabled, external parallel control bus (testbench)
16:	Open (Internal ROM on, and mapped to top 32K) Close (Internal ROM off-out from ext ROM)
17:	00 = I2C to JTAG Bridge disabled
18:	01 = ICD_SDA on VGA1_0 SDA, ICD_SCL on VGA1_0 SCL 10 = ICD_SDA on VGA0_0 SDA, ICD_SCL on VGA0_0 SCL 11 = I2C to JTAG Bridge disabled, 5 JTAG signals mapped to AVS Pins
19:	Open/0 (XTAL and Internal Oscillator) Close/1 (External Oscillator on TCLK pin)

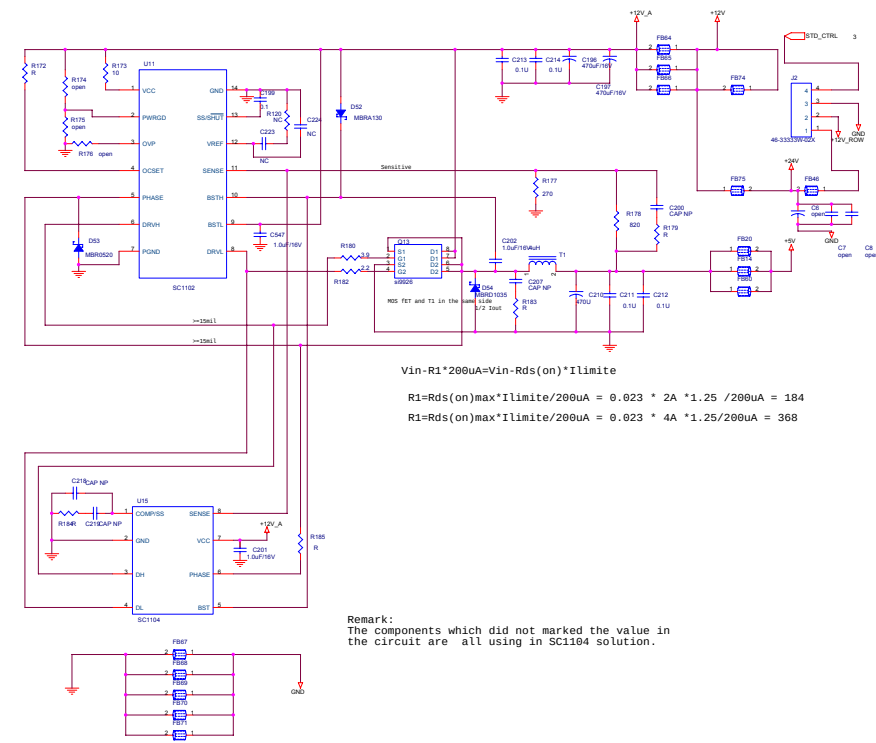
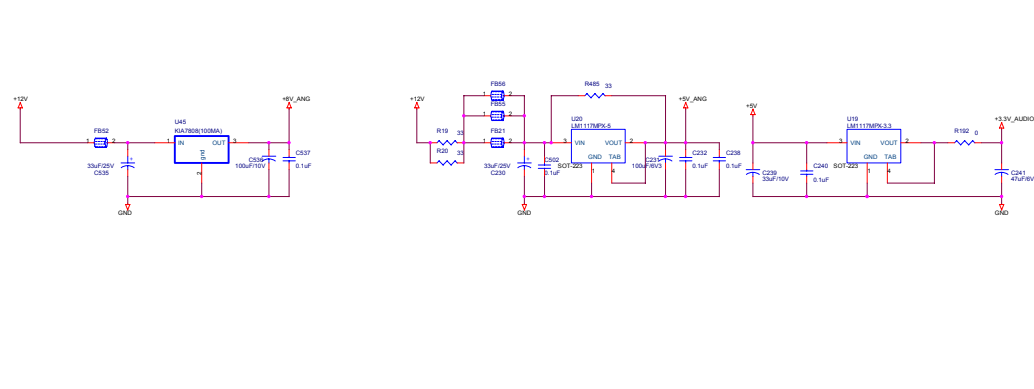


A





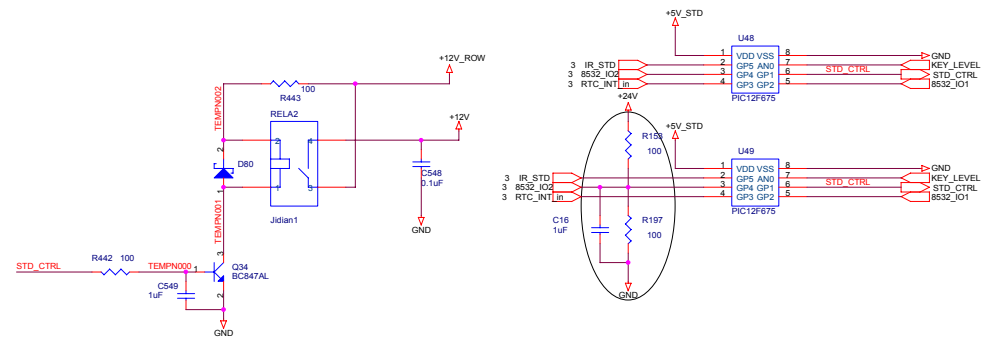
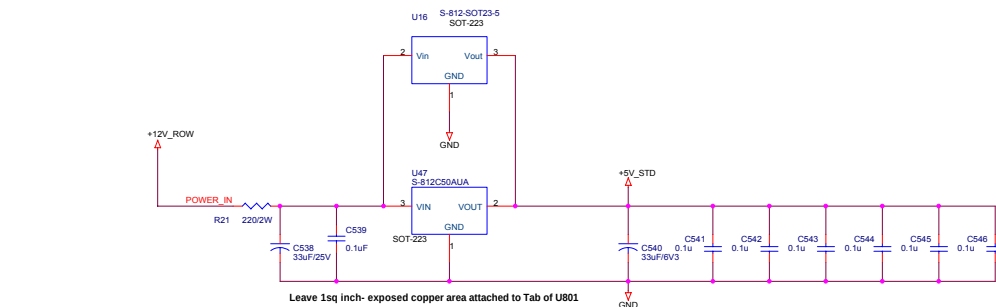




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Vin-R1*200uA=Vin-Rds(on)*Ilimite  
R1=Rds(on)max*Ilimite/200uA = 0.023 * 2A *1.25 /200uA = 184  
R1=Rds(on)max*Ilimite/200uA = 0.023 * 4A *1.25/200uA = 368
```

Remark:
The components which did not marked the value in the circuit are all using in SC1104 solution.

File		Cortez for LCD49/B5-Power	
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STD_CTRL: To relay, to Power supply, to Front control board LED
1---TV on
0---TV Standby

B532_IO1: Input to STD_MCU, three info transmitted----Power
off, set power remember after power on, set standby after power
on

KEY_LEVEL: Detect Programme-- key

IR_STD: Only IR Power_on sequence detect pin, mask this interrupt
when TV is on

RTC_INT1: Just detect it when TV in standby mode, if TV is on, Mask
this interrupt, the interrupt will be disposed by B532

B532_IO2: reserved

